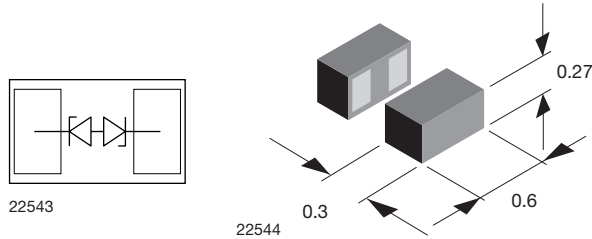


Bidirectional Symmetrical (BiSy) Single Line ESD-Protection Diode in Silicon Package



FEATURES

- Ultra compact CLP0603 package
- Low package height < 0.3 mm
- 1-line ESD-protection
- Working range ± 3.3 V
- Low leakage current < 0.1 μ A
- Low load capacitance $C_D < 14$ pF
- ESD-protection acc. IEC 61000-4-2
 ± 30 kV contact discharge
 ± 30 kV air discharge
- Lead plating: Au (e4)
- Lead material: Ni
- Topside coating
- e4 - precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



MARKING (example only)



1 = year code

Open circle = month code and pin 1

XY = type code

ORDERING INFORMATION

| DEVICE NAME | ORDERING CODE | TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL) | MINIMUM ORDER QUANTITY |
|--------------|--------------------|--|------------------------|
| VCUT03E1-SD0 | VCUT03E1-SD0-G4-08 | 15 000 | 15 000 |

PACKAGE DATA

| DEVICE NAME | PACKAGE NAME | TYPE CODE | WEIGHT | SOLDERING CONDITIONS |
|--------------|--------------|-----------|---------|--|
| VCUT03E1-SD0 | CLP0603 | 3E | 0.12 mg | 260 °C/10 s at terminals Reflow soldering according JEDEC® STD-020 |

ABSOLUTE MAXIMUM RATINGS VCUT05E1-SD0

| PARAMETER | TEST CONDITIONS | SYMBOL | VALUE | UNIT |
|-----------------------|---|-----------|-------------|------|
| Peak pulse current | acc. IEC 61000-4-5, 8/20 μ s/single shot | I_{PPM} | 6 | A |
| Peak pulse power | Pin 1 to pin 2 acc. IEC 61000-4-5; $t_p = 8/20$ μ s; single shot | P_{PP} | 78 | W |
| ESD immunity | Contact discharge acc. IEC 61000-4-2; 10 pulses | V_{ESD} | ± 30 | kV |
| | Air discharge acc. IEC 61000-4-2; 10 pulses | | ± 30 | |
| Operating temperature | Junction temperature | T_J | -55 to +150 | °C |
| Storage temperature | | T_{stg} | -55 to +150 | °C |

CUT THE SPIKES WITH VCUT03E1-SD0

The VCUT03E1-SD0 is a Bidirectional and Symmetrical (BiSy) ESD-protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT03E1-SD0 offers a high isolation (low leakage current, low capacitance) within the specified working range. Due to the short leads and small package size of the tiny CLP0603 package the line inductance is very low, so that fast transients like and ESD-strike can be clamped with minimal over- or undershoots.

| ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) | | | | | | |
|--|---|---------------|------|------|------|---------------|
| PARAMETER | TEST CONDITIONS/REMARKS | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Protection paths | Number of lines which can be protected | $N_{channel}$ | - | - | 1 | lines |
| Reverse stand-off voltage | Max. reverse working voltage | V_{RWM} | - | - | 3.3 | V |
| Reverse voltage | at $I_R = 0.1\text{ }\mu\text{A}$ | V_R | 3.3 | - | - | V |
| Reverse current | at $V_{RWM} = 3.3\text{ V}$ | I_R | - | - | 0.1 | μA |
| Reverse breakdown voltage | at $I_R = 1\text{ mA}$ | V_{BR} | 6.5 | 8 | 9 | V |
| Reverse clamping voltage | at $I_{PP} = 1\text{ A}$ | V_C | - | 8.8 | 10 | V |
| | at $I_{PP} = I_{PPM} = 6\text{ A}$ | V_C | - | 11 | 13 | V |
| Capacitance | at $V_R = 0\text{ V}$; $f = 1\text{ MHz}$ | C_D | - | 13 | 14 | pF |
| | at $V_R = 2.5\text{ V}$; $f = 1\text{ MHz}$ | C_D | - | 11 | - | pF |
| Clamping voltage | Transmission Line Pulse (TLP); $t_p = 100\text{ ns}$ $I_{TLP} = 8\text{ A}$ | V_{C-TLP} | - | 9.8 | - | V |
| Clamping voltage | Transmission Line Pulse (TLP); $t_p = 100\text{ ns}$ $I_{TLP} = 16\text{ A}$ | V_{C-TLP} | - | 11 | - | V |
| Dynamic resistance | Transmission Line Pulse (TLP); $t_p = 100\text{ ns}$ | R_{DYN} | - | 0.15 | - | Ω |

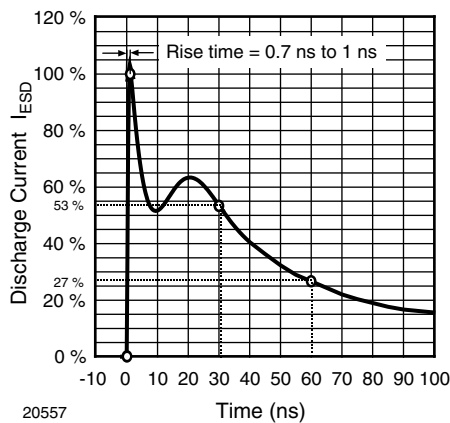
TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)


Fig. 1 - ESD Discharge Current Wave Form
acc. IEC 61000-4-2 (330 Ω /150 pF)

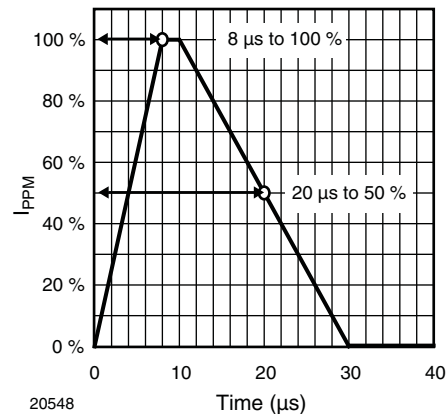


Fig. 2 - 8/20 μs Peak Pulse Current Wave Form
acc. IEC 61000-4-5

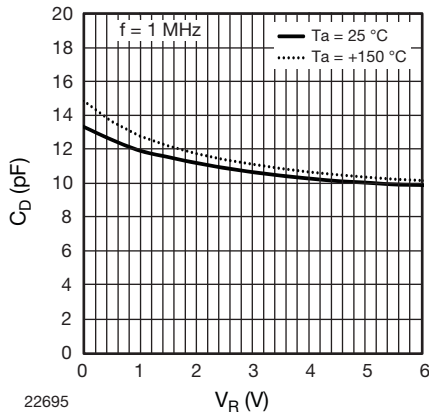


Fig. 3 - Typical Capacitance C_D vs. Reverse Voltage V_R

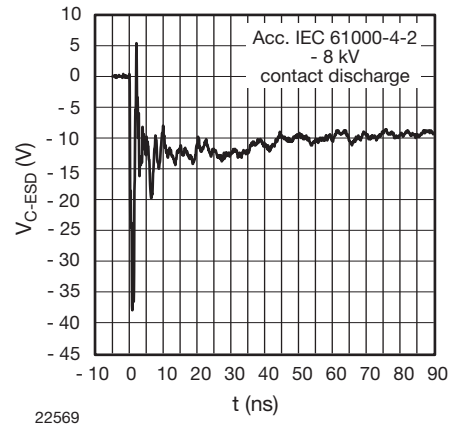


Fig. 6 - Typical Clamping Performance at 8 kV Contact Discharge acc. IEC 61000-4-2

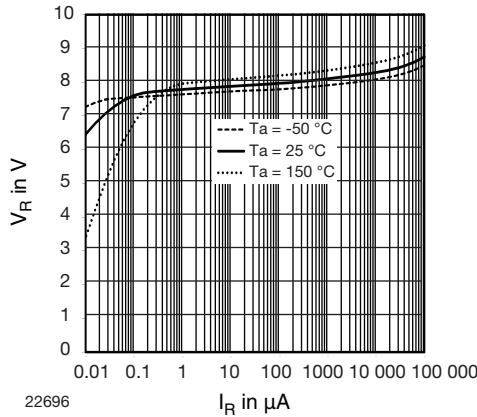


Fig. 4 - Typical Reverse Voltage V_R vs. Reverse Current I_R

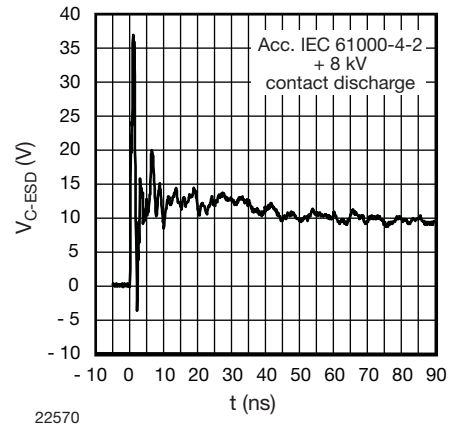


Fig. 7 - Typical Clamping Performance at 8 kV Contact Discharge acc. IEC 61000-4-2

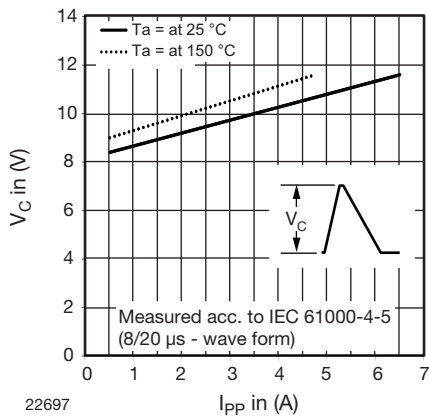


Fig. 5 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

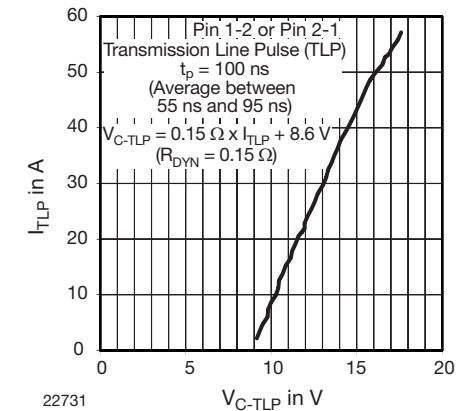
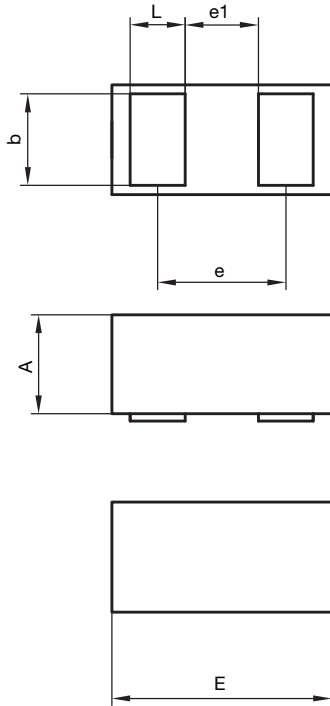


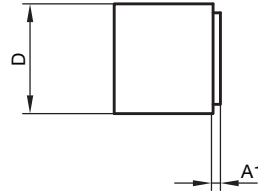
Fig. 8 - Typical Clamping Voltage at 100 ns Transmission Line Pulse (TLP)



PACKAGE DIMENSIONS in millimeters (mils): **CLP0603-2L**



Package = chip dimensions in mm



| | Millimeters | | | mils | | |
|----|-------------|------|------|-------|-------|-------|
| | min. | nom. | max. | min. | nom. | max. |
| A | 0.24 | 0.27 | 0.30 | 9.44 | 10.63 | 11.81 |
| A1 | | | 0.02 | | | 0.79 |
| b | 0.22 | 0.25 | 0.28 | 8.66 | 9.84 | 11.02 |
| D | 0.27 | 0.30 | 0.33 | 10.62 | 11.81 | 12.99 |
| E | 0.57 | 0.60 | 0.63 | 22.44 | 23.62 | 24.80 |
| e | | 0.40 | | | 15.75 | |
| e1 | | 0.25 | | | 9.84 | |
| L | 0.12 | 0.15 | 0.18 | 4.72 | 5.91 | 7.09 |

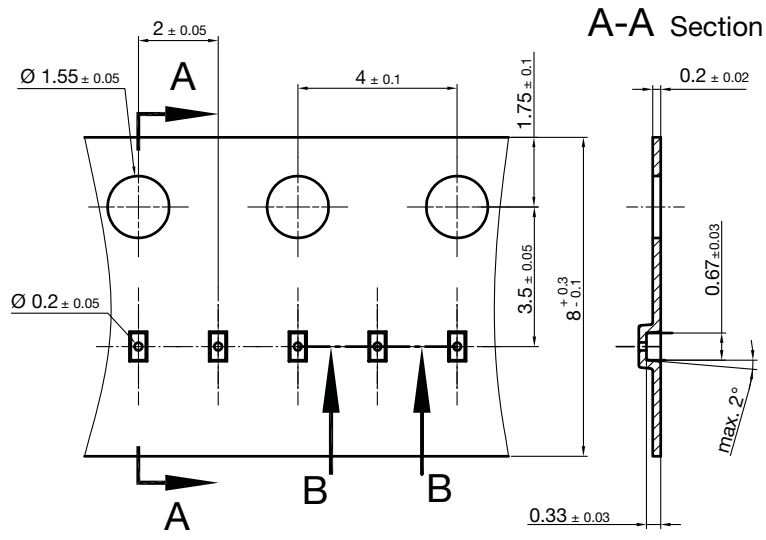
22740

2 terminal leadless package (CLP0603-2L LLP)
 Document no.: S8-V-3906.04-023 (4)
 Created - Date: 22. Nov. 2010
 Rev.4 - Date: 07. May 2014

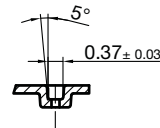
Footprint and soldering recommendation:
 please see Application Note: www.vishay.com/doc?85917



CARRIER TAPE in millimeters: **CLP0603**



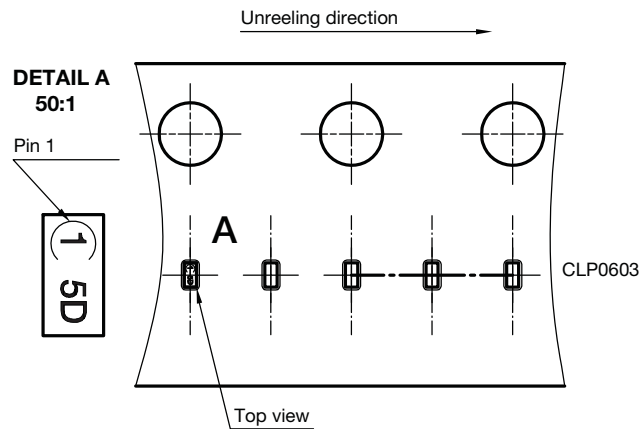
B-B Section



Cummulative tolerances of 10 sprocket holes is $\pm 0.2\text{mm}$

22591
Document no. S8-V-3906.04-0025 (4)
Created - Date: 22. Nov. 2010

ORIENTATION IN CARRIER CLP0603



22607

Orientation in Carrier Tape (CLP0603)
S8-V-3906.04-026 (4)
22.10.2010



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