

**US6J12** 

## -12V Pch+Pch Small Signal MOSFET

## Datasheet

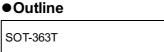
V <sub>DSS</sub>	-12V
R <sub>DS(on)</sub> (Max.)	105mΩ
I <sub>D</sub>	±2A
P <sub>D</sub>	1.0W

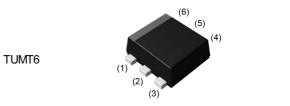
#### Features

Application

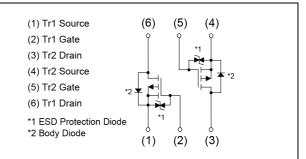
Switching

- 1) Low on resistance
- 2) Built-in G-S Protection Diode
- 3) Small Surface Mount Package (TUMT6)
- 4) Pb-free lead plating ; RoHS compliant





#### Inner circuit



#### Packaging specifications

	Packing	Embossed Tape
Туре	Reel size (mm)	180
	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TCR
	Marking	J12

#### • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified) <Tr1 and Tr2>

<b>U</b> ( u	• •			
Parameter	Symbol	Value	Unit	
Drain - Source voltage	V <sub>DSS</sub>	-12	V	
Continuous drain current	Ι <sub>D</sub>	±2	А	
Pulsed drain current	I <sub>DP</sub> *1	±6	А	
Gate - Source voltage	V <sub>GSS</sub>	0~-8	V	
	P <sub>D</sub> *2	1.0	10/	
Power dissipation (total)	P <sub>D</sub> *3	0.91	W	
Junction temperature	Tj	150	°C	
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	C°	

#### •Thermal resistance

Deremeter	Symbol	Values			llait
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innotion embient (total)	$R_{thJA}^{*2}$	-	-	125	°C \\ \
Thermal resistance, junction - ambient (total)	$R_{thJA}^{*3}$	-	-	137	°C/W

## •Electrical characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Deremeter	Ourresh ed	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -1mA	-12	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-5.0	-	mV/°C
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -12V, V <sub>GS</sub> = 0V	-	-	-10	μA
Gate - Source leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = -8V	-	-	-10	μA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = -6V, I <sub>D</sub> = -1mA	-0.3	-	-1.0	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	2.7	-	mV/°C
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2A	-	75	105	
Static drain - source	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -1A	-	105	145	
on - state resistance		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -1A	-	150	225	mΩ
		V <sub>GS</sub> = -1.5V, I <sub>D</sub> = -0.4A	-	200	400	
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain	-	19	-	Ω
Forward Transfer Admittance	$ Y_{fs} ^{*4}$ $V_{DS} = -6V, I_D = -2A$		2.3	-	-	S



## •Electrical characteristics ( $T_a = 25^{\circ}C$ ) <Tr1 and Tr2>

Deremeter	Cump of	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	850	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -6V	-	70	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	60	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq -6V, V_{GS} = -4.5V$	-	9	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -1A	-	4	-	20
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L = 6\Omega$	-	80	-	ns
Fall time	t <sub>f</sub> *4	R <sub>G</sub> = 10Ω	-	25	-	

## •Gate charge characteristics ( $T_a = 25^{\circ}C$ ) <Tr1 and Tr2>

Parameter	Symbol Cond	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Onit
Total gate charge	$Q_g^{*4}$	$V_{DD} \simeq -6V$	-	7.6	-	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = -2A	-	1.4	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	V <sub>GS</sub> = -4.5V	-	0.9	-	

## •Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	۱ <sub>s</sub>	$T = 25^{\circ}$	-	-	-0.5	^	
Pulse forward current	$I_{SP}^{*1}$	T <sub>a</sub> = 25°C	-	-	-6.0	A	
Forward voltage	$V_{SD}^{*4}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = -2A	-	-	-1.2	V	

\*1 Pw $\leq$ 10µs , Duty cycle $\leq$ 1%

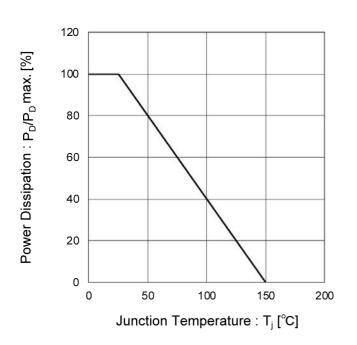
\*2 Mounted on a ceramic board(30×30×0.8mm)

\*3 Mounted on a FR4 (25×25×0.8mm)

\*4 Pulsed



#### • Electrical characteristic curves



## Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

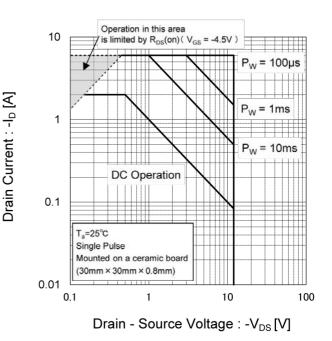
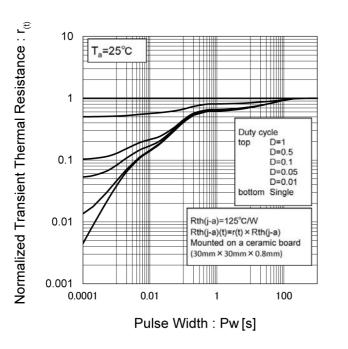
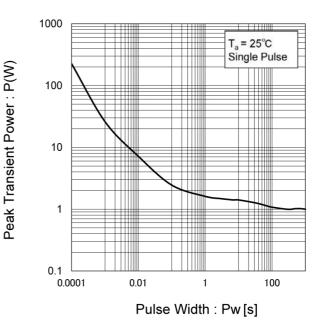


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

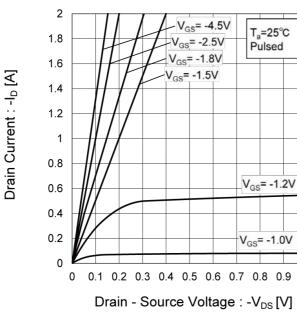


# Fig.4 Single Pulse Maximum Power dissipation





#### Electrical characteristic curves



## Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)

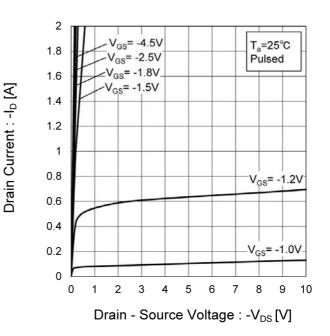
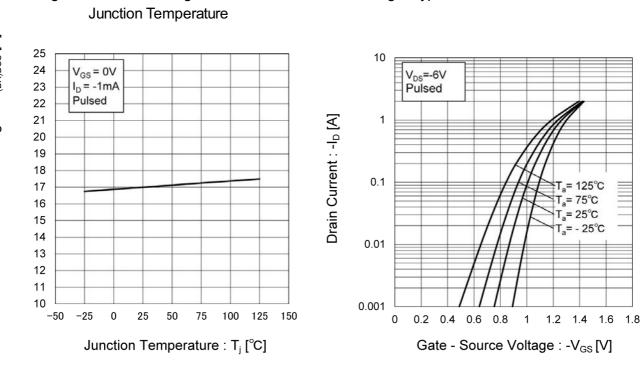


Fig.8 Typical Transfer Characteristics

Fig.7 Breakdown Voltage vs. Junction Temperature

Drain-Source Breakdown Voltage : -V<sub>(BR)DSS</sub> [V]



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#### • Electrical characteristic curves

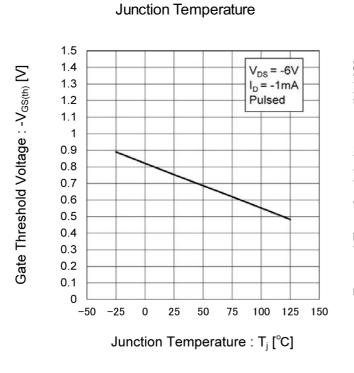


Fig.9 Gate Threshold Voltage vs.

## Fig.10 Forward Transfer Admittance vs. Drain Current

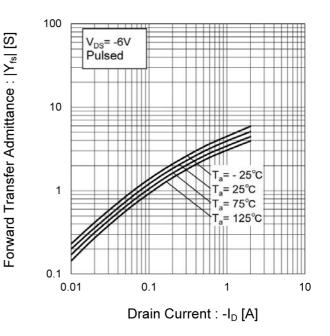


Fig.11 Drain Current Derating Curve

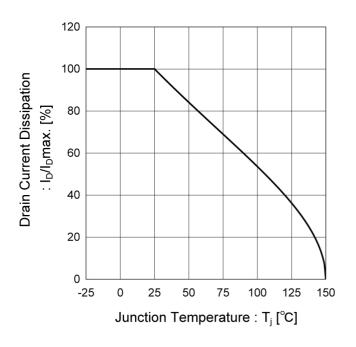




Fig.13 Static Drain - Source On - State

## •Electrical characteristic curves

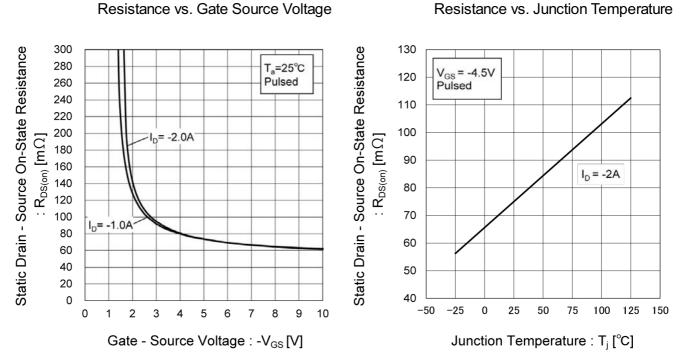
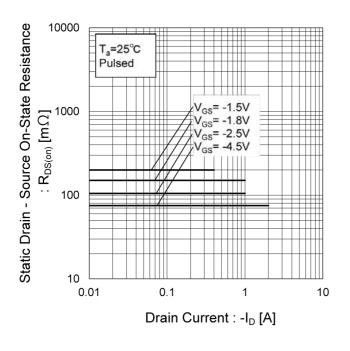


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)





## •Electrical characteristic curves

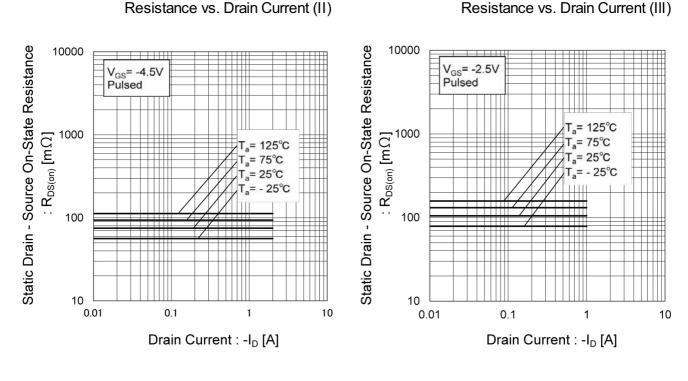


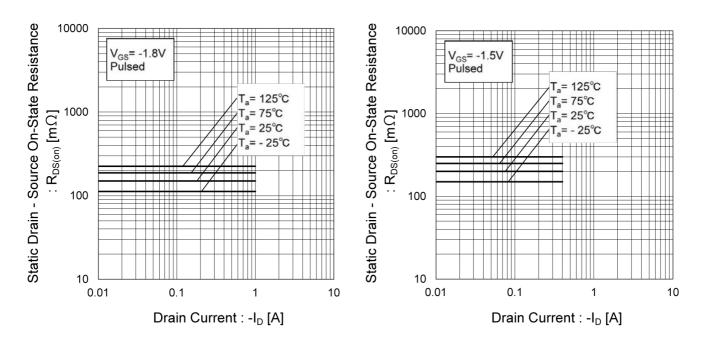
Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

Fig.17 Static Drain - Source On - State

Resistance vs. Drain Current (IV)

Fig.18 Static Drain - Source On - State Resistance vs. Drain Current (V)

Fig.16 Static Drain - Source On - State



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#### • Electrical characteristic curves

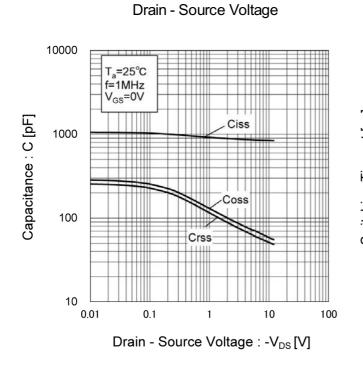


Fig.19 Typical Capacitance vs.

#### Fig.20 Switching Characteristics

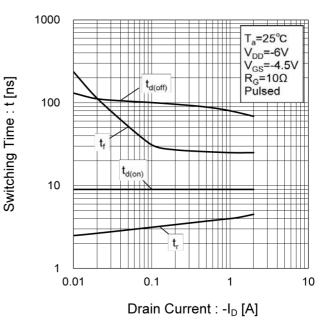


Fig.21 Dynamic Input Characteristics

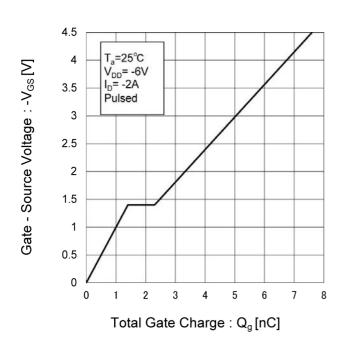
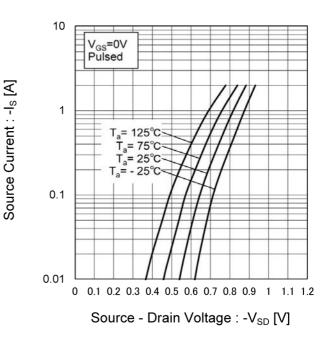


Fig.22 Source Current vs. Source Drain Voltage



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## •Measurement circuits <It is the same for the Tr1 and Tr2>

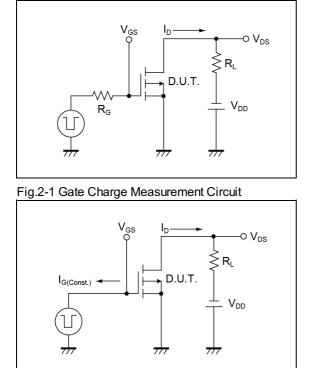


Fig.1-1 Switching Time Measurement Circuit

Fig.1-2 Switching Waveforms

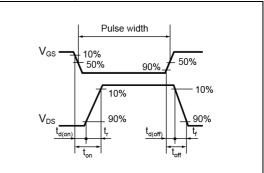
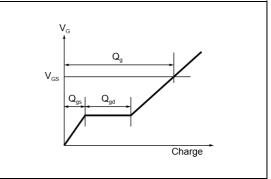


Fig.2-2 Gate Charge Waveform



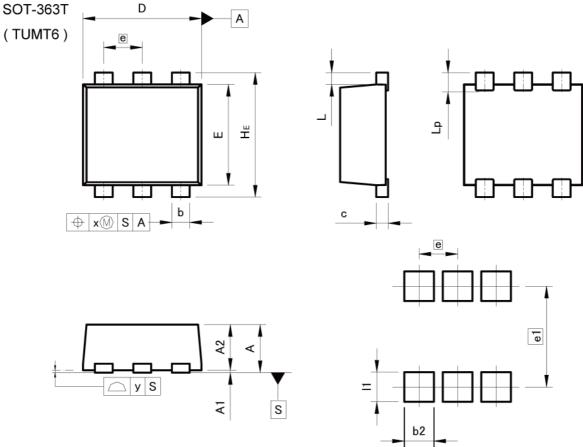
## Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.





#### Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

		ETERS	INC	HES	
	MIN	MAX	MIN	MAX	
A	<u>11</u> 2	0.85	-	0.033	
A1	0.00	0.05	0.000	0.002	
A2	0.72	0.82	0.028	0.032	
b	0.25	0.40	0.010	0.016	
с	0.12	0.22	0.005	0.009	
D	1.90	2.10	0.075	0.083	
E	1.60	1.80	0.063	0.071	
е	0.	65	0.0	0.026	
HE	2.00	2.20	0.079	0.087	
L	0.	20	0.008		
Lp	<u>20</u> 03	0.40	12	0.016	
x	<u>11</u> 25	0.10	12	0.004	
у		0.10	-	0.004	
	MILIM	ETERS	INC	HES	
	MIN	MAX	MIN	MAX	
b2		0.50	-	0.020	
e1	1.	70	0.0	067	
11	<u>4</u> 2	0.50	-	0.020	

Dimension in mm/inches





## Notice

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	CLASSⅣ	CLASSII	CLASSII	CLASSI

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  - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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When disposing Products please dispose them properly using an authorized industry waste company.

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