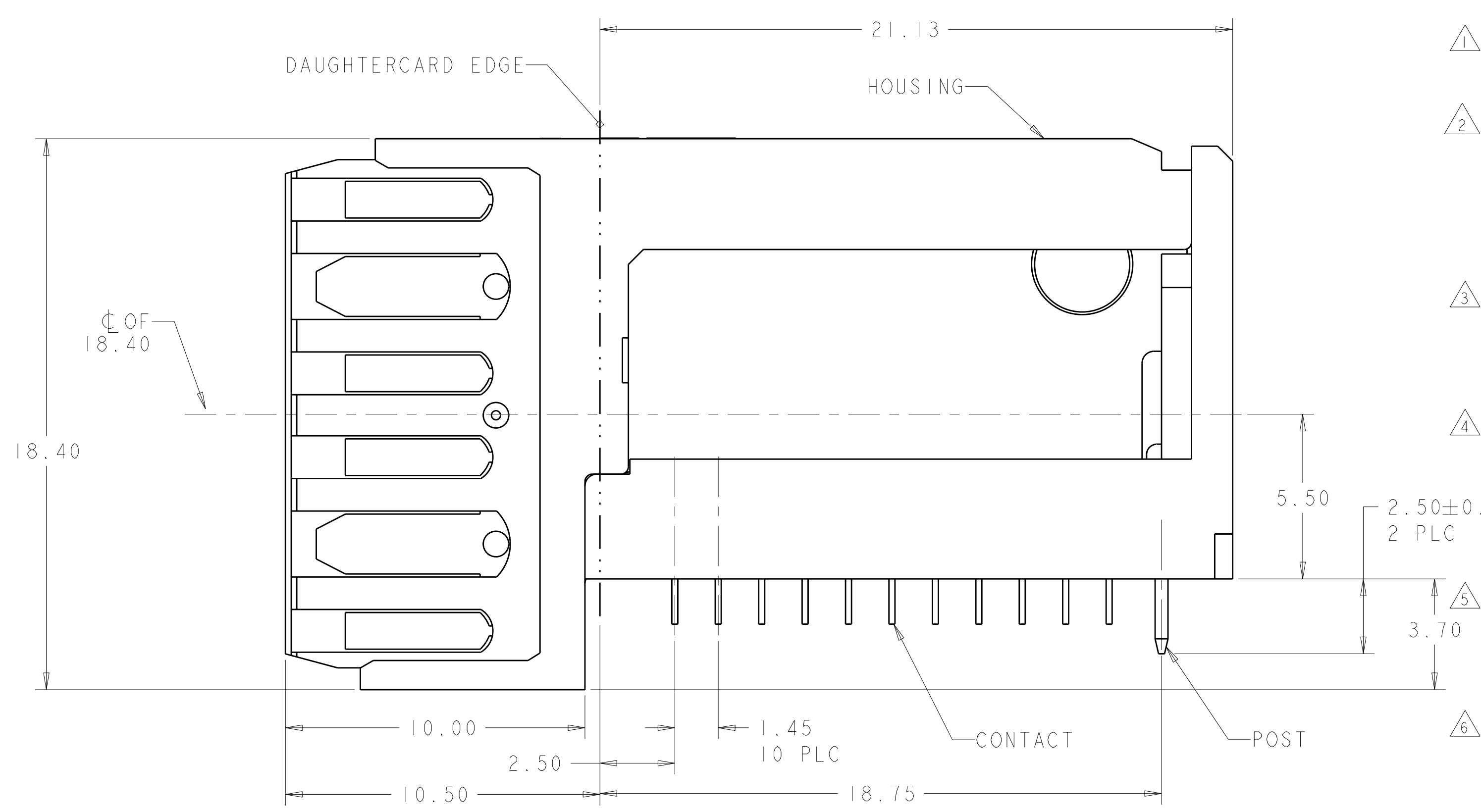
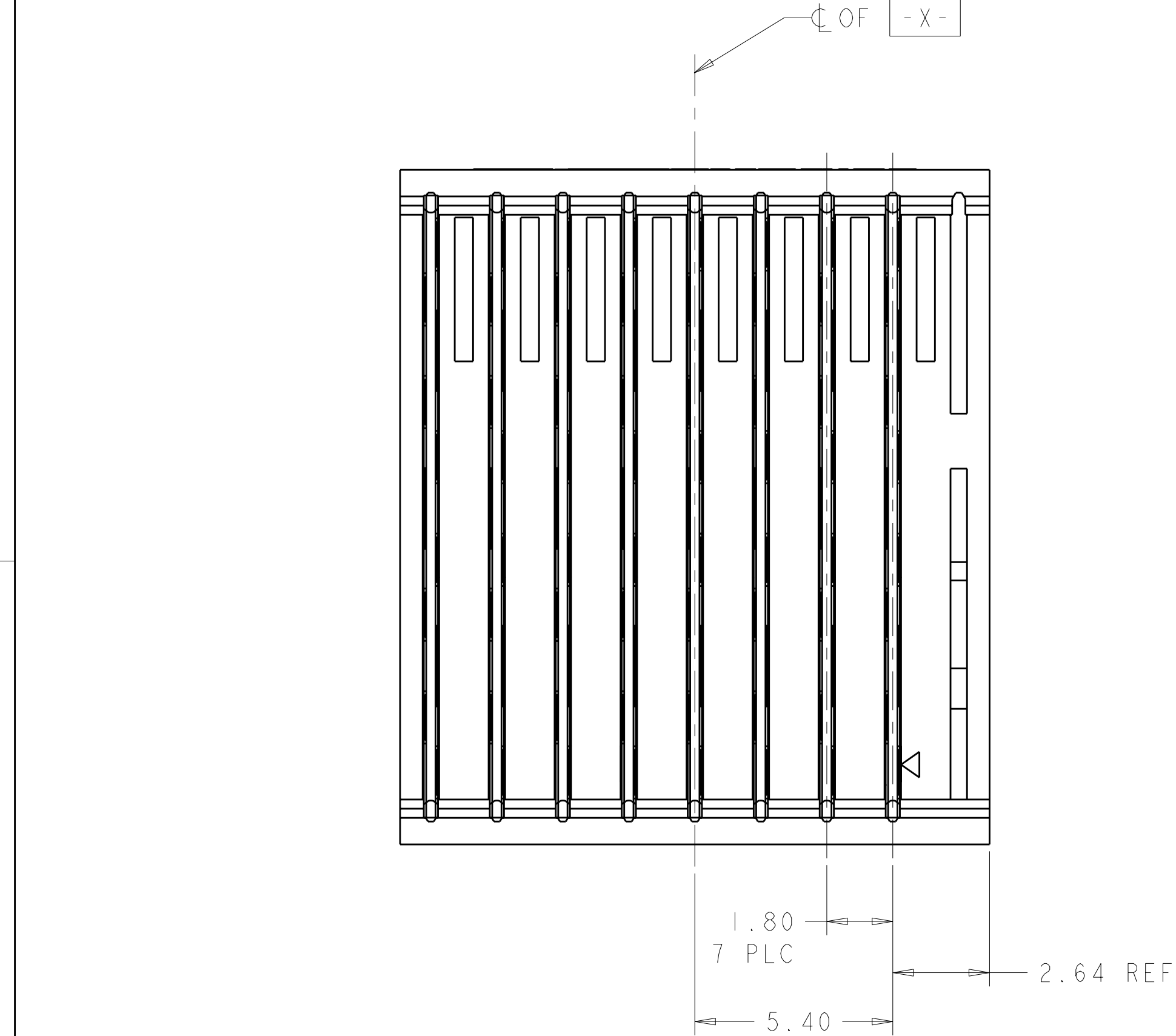
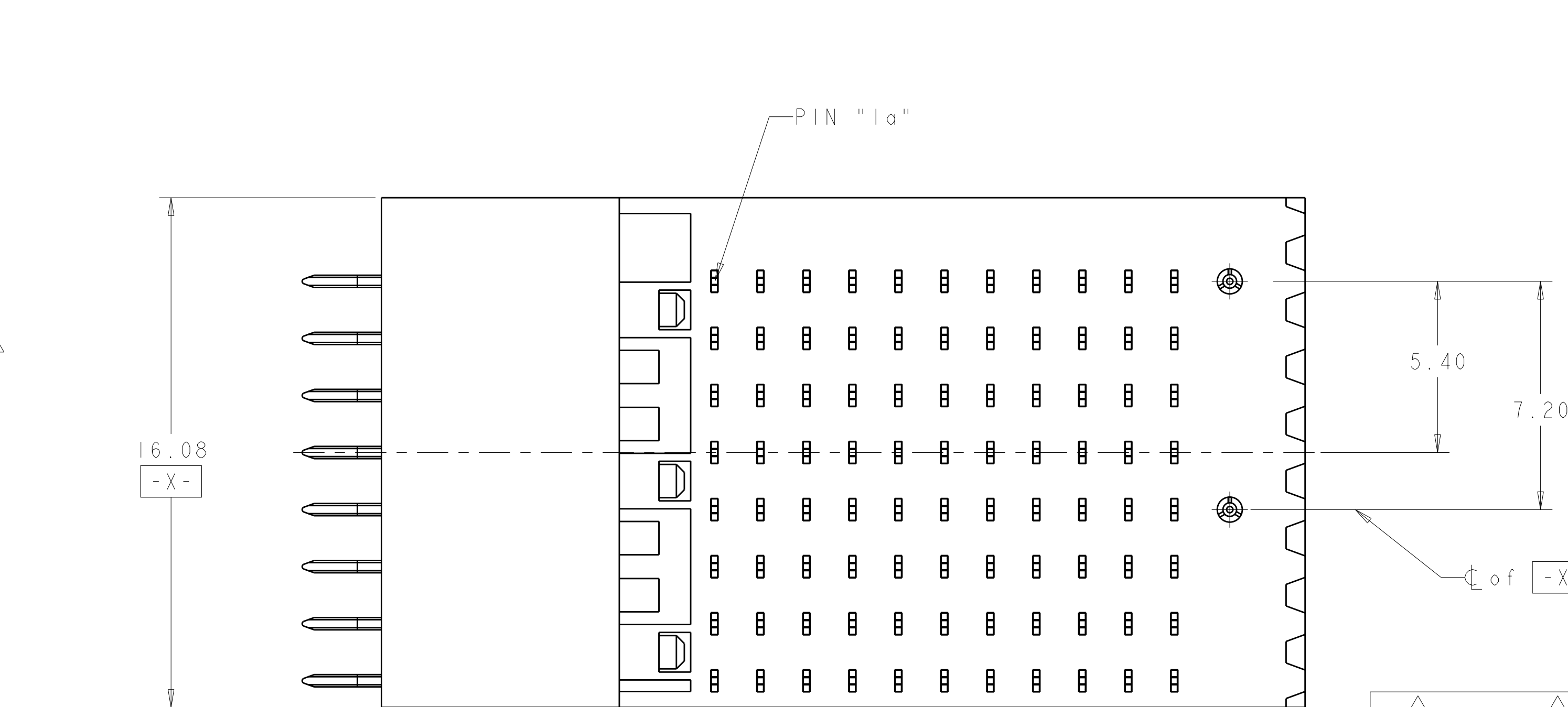
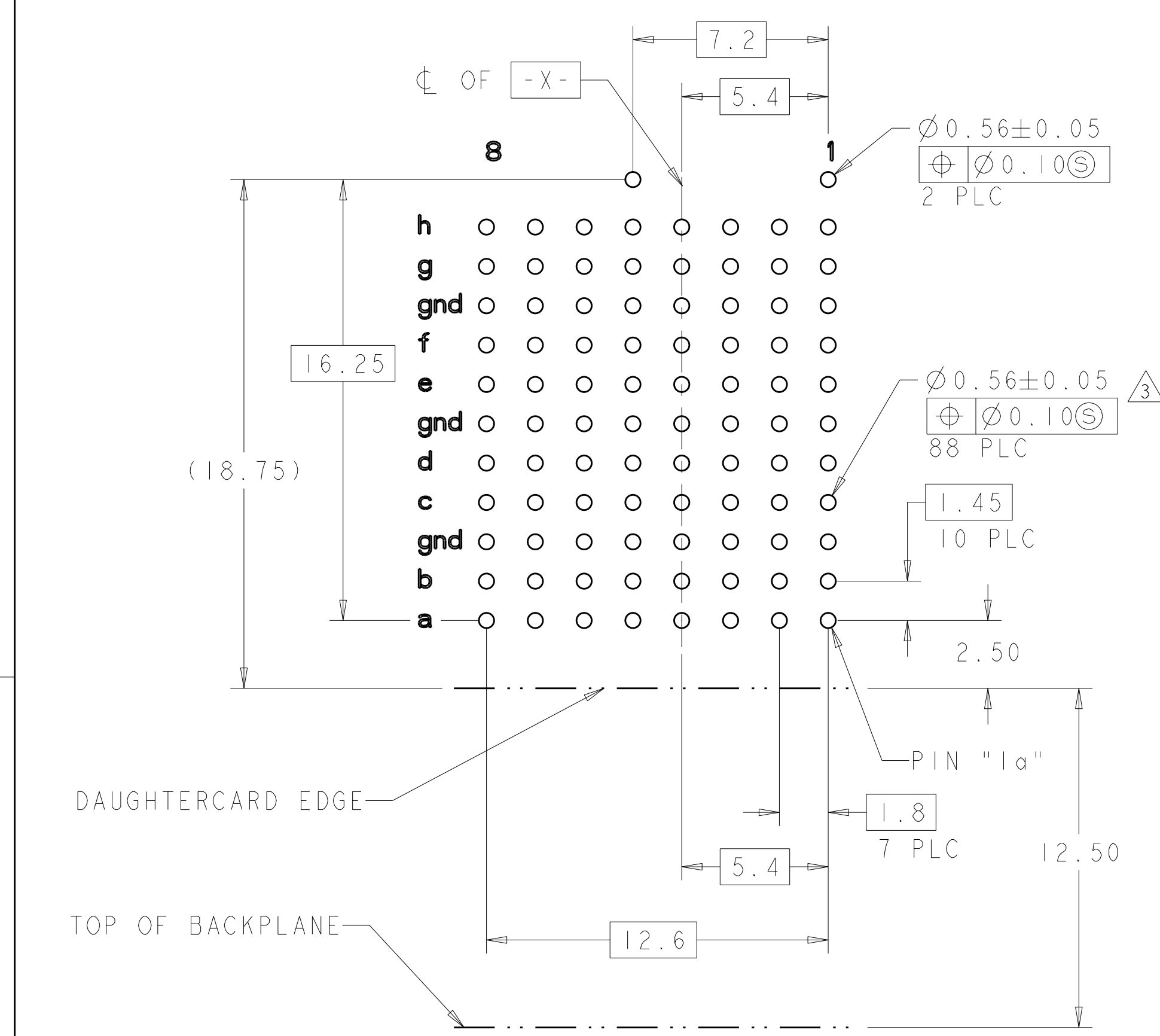


LOC		DIST		REVISIONS			
AD	00	P	LYR	DESCRIPTION	DATE	DWN	APVD
		A		EC 0S12-0341-04	29JUL2004	BW	GG
		A1		REVISED PER ECO-09-025699	27JAN10	KK	HMR



- △ HOUSING: LCP, UL94V-0, BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE.
- △ 2 CONTACT: 0.76 µm GOLD PER ASTM B 488 IN CONTACT AREA,
1.27 µm MIN TIN-LEAD
ON PCB TAIL,
OVER 1.27 µm MIN NICKEL PER
SAE AMS-QQ-N-290 OVER ALL.
POST: 1.27 µm MIN NICKEL.
- △ 3 MANUFACTURING TOLERANCE
FOR A Ø 0.56 ± 0.05 FINISHED HOLE.
DRILLED HOLE: Ø 0.65 ± 0.02
COPPER PLATE: 0.050-0.025
TIN/LEAD PLATE: 0.0124-0.0038
- △ 4 CONTACT: 0.76 µm GOLD PER ASTM B 488 IN CONTACT AREA,
1.27 µm MIN MATTE TIN PER ASTM B 545
ON PCB TAIL,
OVER 1.27 µm MIN NICKEL PER
SAE AMS-QQ-N-290 OVER ALL.
POST: 1.27 µm MIN NICKEL.
- △ 5 MANUFACTURING TOLERANCE FOR A
Ø 0.575 ± 0.045 FINISHED HOLE WITHOUT
TIN LEAD PLATING:
DRILLED HOLE: Ø 0.65 ± 0.02
COPPER PLATING: 0.025 - 0.050
- △ 6 OBSOLETE PARTS: OBSOLETE CIS STREAMLINING
PER D.RENAUD/D.SINISI



△ 6 OBSOLETE	△ 4	△ 5	DIFFERENTIAL, NONSEQUENTIAL	1410228-4
△ 6 OBSOLETE	△ 4	△ 5	SINGLE END, NONSEQUENTIAL	1410228-3
△ 6 OBSOLETE	△ 2	△ 3	DIFFERENTIAL, NONSEQUENTIAL	1410228-2
△ 6 OBSOLETE	△ 2	△ 3	SINGLE END, NONSEQUENTIAL	1410228-1
CONTACT TAIL/ HOLE FINISH			DESCRIPTION	PART NUMBER

PC BOARD LAYOUT
(CONNECTOR SIDE)
SCALE 6:1

DIMENSIONS:		TOLERANCES UNLESS OTHERWISE SPECIFIED:		DWN F. MORANA 05OCT2001		Tyco Electronics Corporation Harrisburg, PA 17105-3608	
mm		9 PLC 2 PLC 3 PLC 4 PLC ANGLES		CHK D. DEFIBAUGH 23OCT2001		NAME	
MATERIAL		FINISH		APVD F. P. MORANA 23OCT2001		PRODUCT SPEC	
1		2		108-2072		APPLICATION SPEC	
114-13056		WEIGHT		114-13056		SIZE	
CUSTOME DRAWING		SCALE		8:1		SHEET	
1		OF		1		REV	
A1							