

6

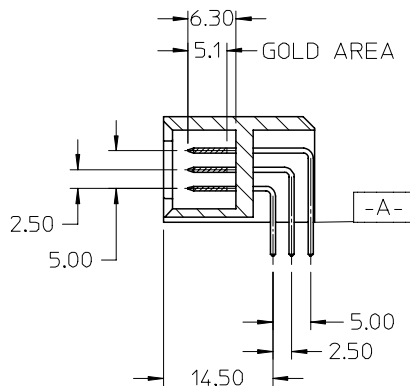
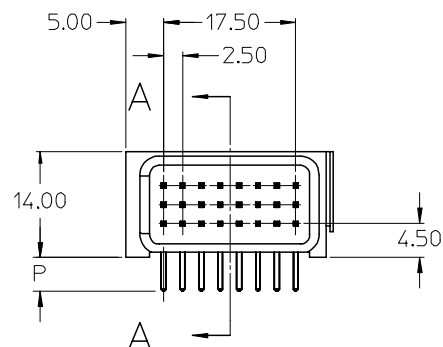
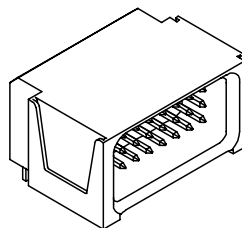
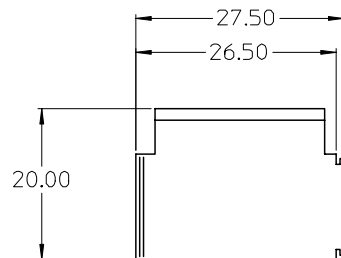
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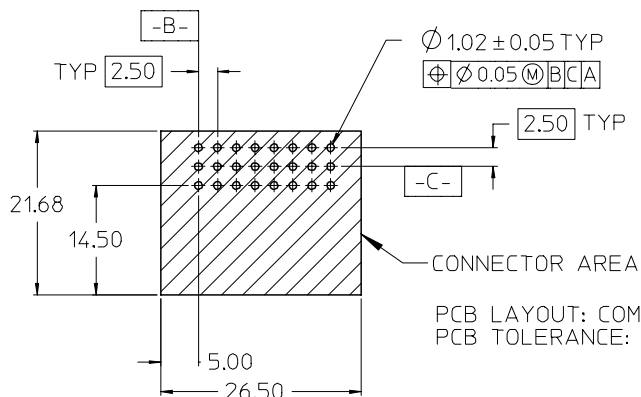
45518



SECTION A-A

NOTES:

- HOUSING MAT'L: LCP, GLASS FILLED, UL94V-0, BLACK.
CONTACT MAT'L: COPPER ALLOY
- FINISH: SELECT GOLD: 30 MI MIN.
SELECT TIN: 150 MI MIN.
BOTH OVER NICKEL
- PRODUCT SPECIFICATION: PS-45424-001
- PACKAGING SPECIFICATION: PK-45485-001
- THIS ASSEMBLY INTERLOCKS WITH ALL STANDARD MOLEX POWER DOCK MODULES.
- DATUM -A- IS THE CONNECTOR MOUNTING SURFACE.
- THE PRIMARY SHIPPING CARTON WILL BE LABELED "COMPLIANT TO RoHS DIRECTIVE 2000/95/EC AND ELV ANNEX II OF DIRECTIVE 2000/53/EC".
CARTONS WITHOUT THIS LABEL MAY CONTAIN PRODUCT WITH TIN/LEAD PLATING.
- PC TAILS MAY BE BLUNT CUT TO LENGTH.



Ø 1.02 ± 0.05 TYP
 Ø 0.05 (M) B C A

2.50 TYP

-C-

CONNECTOR AREA

PCB LAYOUT: COMPONENT SIDE
 PCB TOLERANCE: ± 0.05

PCB THICKNESS	DIM P	MATERIAL NUMBER
.125 3.18	.177 4.50	45518-0001
.093 2.36	.143 3.60	45518-0002
.062 1.57	.115 2.92	45518-0003

STANDARDIZE EC NO: UCP2006-0797 DRN: ADRA TNL 2005/11/29 CHKD: KSAMEC 2005/11/29 APPR: APATEL 2005/11/30	DESCRIPTION	GENERAL TOLERANCES (UNLESS SPECIFIED)		SCALE 1:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY	
REV	DESCRIPTION	mm		INCH		TITLE		
		4 PLACES ± ---		± ---		POWER DOCK MODULE		
		3 PLACES ± ---		± ---		SIGNAL 24 PIN		
		2 PLACES ± 0.25		± ---		RIGHT ANGLE SOLDER TAIL		
		1 PLACE ± 0.38		± ---		MOLEX INCORPORATED		
REV	DESCRIPTION	ANGULAR ± 1/2°		DATE		MATERIAL NO.		
		DRAFT WHERE APPLICABLE		DATE		DOCUMENT NO.		
		MUST REMAIN WITHIN DIMENSIONS		DATE		SHEET NO.		