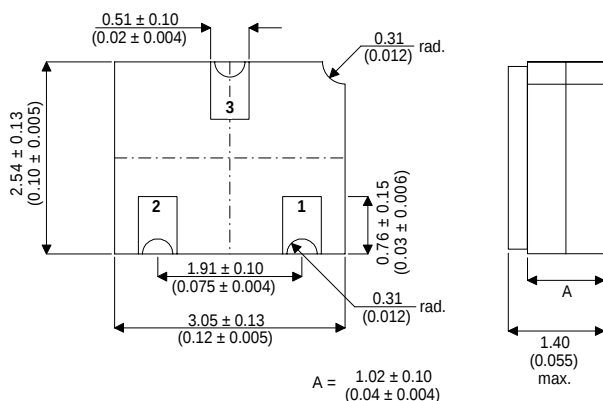


MECHANICAL DATA

Dimensions in mm(inches)



LCC1 PACKAGE (SOT23 Compatible)

Underside View

Pad 1 – Anode

Pad 2 – N/C

Pad 3 – Cathode

VOLTAGE REGULATOR DIODE IN A CERAMIC SURFACE MOUNT PACKAGE FOR HI-REL APPLICATIONS

FEATURES

- HERMETIC CERAMIC SURFACE MOUNT PACKAGE
- SCREENING OPTIONS AVAILABLE

ABSOLUTE MAXIMUM RATINGS

P_{TOT}	Power Dissipation	$T_{AMB} = 25^{\circ}C$	400mW
	Derate above 25°C		3.2mW/°C
T_{OP}	Maximum Operating Ambient Temperature		–65 to +150°C
T_{STG}	Storage Temperature Range		–65 to +175°C
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		312°C/W

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_Z Zener Voltage	I_{PINCH} (mA) @ $V_S = 25.0V$	3.87	4.30	4.73	V
V_L Maximum Limiting Voltage	$I_L = 0.8 I_{PINCH(min)}$			2.75	
Z_S^* Minimum Dynamic Impedance	$V_S = 25.0V$	0.245			mΩ
Z_K^* Minimum Knee Impedance	$V_K = 6.0V$	0.014			

* Z_S is derived by superimposing a 90Hz RMS signal equal to 10% of V_S onto V_S .

Similarly, Z_K is derived by superimposing a 90Hz RMS signal equal to 10% of V_S onto V_K .

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