# **74AHCT244A**

# Octal buffer/line driver; 3-state Rev. 1 — 23 November 2016

**Product data sheet** 

#### **General description** 1.

The 74AHCT244A is an 8-bit buffer/line driver with 3-state outputs and TTL inputs. The device features two output enables (1OE and 2OE). A HIGH on nOE causes the associated outputs to assume a high-impedance OFF-state.

Designed to operate over a V<sub>CC</sub> range from 4.5 V to 5.5 V, the inputs are TTL compatible, which allows the device to be used to translate from 3.3 V to 5 V.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device is fully specified for partial Power-down applications using I<sub>OFF</sub>. The I<sub>OFF</sub> circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

#### 2. **Features and benefits**

- Direct interface with TTL levels
- Supply voltage range from 4.5 V to 5.5 V
- Typical t<sub>pd</sub> of 2.8 ns at 5 V
- Typical  $V_{OL(p)}$  < 0.8 V at  $V_{CC}$  = 5 V,  $T_{amb}$  = 25 °C
- Typical  $V_{OH(v)} > 2.3 \text{ V at } V_{CC} = 5 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$
- Supports mixed-mode voltage operation on all ports
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
  - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
  - MM JESD22-A115-A exceeds 150 V
  - CDM JESD22-C101E exceeds 2 kV
- Specified from −40 °C to +85 °C and from −40 °C to +125 °C

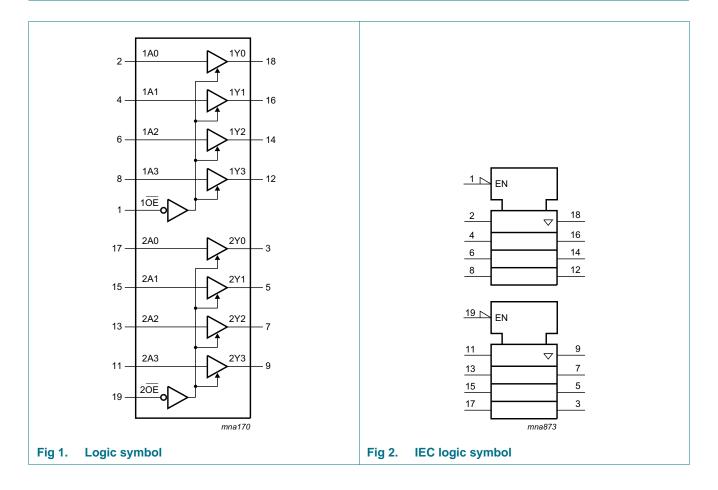


## 3. Ordering information

Table 1. Ordering information

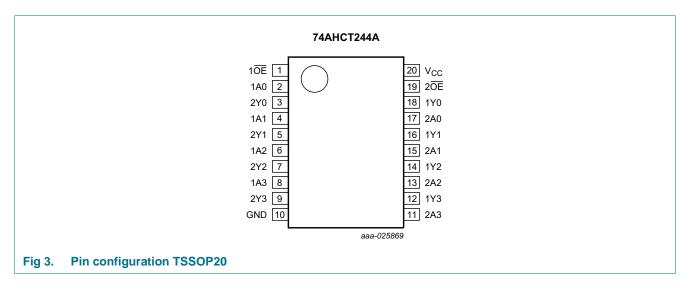
Type number	Package	Package									
	Temperature range	Name	Description	Version							
74AHCT244APW	–40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package; 20 leads; body width 4.4 mm	SOT360-1							

## 4. Functional diagram



## 5. Pinning information

## 5.1 Pinning



## 5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
1 <del>OE</del> , 2 <del>OE</del>	1, 19	output enable input (active LOW)
1A0, 1A1, 1A2, 1A3	2, 4, 6, 8	data input
2Y0, 2Y1, 2Y2, 2Y3	3, 5, 7, 9	data output
GND	10	ground (0 V)
2A0, 2A1, 2A2, 2A3	17, 15, 13, 11	data input
1Y0, 1Y1, 1Y2, 1Y3,	18, 16, 14, 12	data output

## 6. Functional description

Table 3. Function table [1]

Control	Input	Output
nOE	nAn	nYn
L	L	L
L	Н	Н
Н	X	Z

<sup>[1]</sup> H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

## 7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CC}$	supply voltage			-0.5	+7.0	V
VI	input voltage		<u>[1]</u>	-0.5	+7.0	V
Vo	output voltage	active mode	[2][3]	-0.5	V <sub>CC</sub> + 0.5	V
		power-down or 3-state mode	[2]	-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-20	-	mA
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V		-20	-	mA
Io	output current	$V_O = 0 \text{ V to } V_{CC}$		-	±25	mA
I <sub>CC</sub>	supply current			-	75	mA
I <sub>GND</sub>	ground current			-75	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$	<u>[4]</u>	-	500	mW

<sup>[1]</sup> The minimum input voltage ratings may be exceeded if the input current ratings are observed.

<sup>[2]</sup> The output voltage ratings may be exceeded if the output current ratings are observed.

<sup>[3]</sup> This value is limited to 7.0 V maximum.

<sup>[4]</sup> For TSSOP20 package: above 100  $^{\circ}$ C the value of P<sub>tot</sub> derates linearly with 10 mW/K.

## 8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		4.5	5.5	V
VI	input voltage		0	5.5	V
Vo	output voltage	active mode	0	V <sub>CC</sub>	V
		power-down or 3-state mode	0	5.5	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	-	20	ns/V

## 9. Static characteristics

#### Table 6. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C			-40 °C	to +85 °C	-40 °C t	Unit	
			Min	Тур	Max	Min	Max	Min	Max	
V <sub>IH</sub>	HIGH-level input voltage	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$	2	-	-	2	-	2	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 4.5 V to 5.5 V	-	-	0.8	-	0.8	-	0.8	V
V <sub>OH</sub> HIGH-level	$V_I = V_{IH}$ or $V_{IL}$ ; $V_{CC} = 4.5 \text{ V}$									
	output voltage	$I_{O} = -50 \ \mu A$	4.4	4.5	-	4.4	-	4.4	-	V
	/ LOW lovel	$I_O = -8 \text{ mA}$	3.94	-	-	3.8	-	3.7	-	V
V <sub>OL</sub> LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$ ; $V_{CC} = 4.5 \text{ V}$									
	output voltage	I <sub>O</sub> = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 8 mA	-	-	0.36	-	0.44	-	0.55	V
l <sub>OZ</sub>	OFF-state output current	$V_{CC}$ = 5.5 V; $V_I$ = $V_{IH}$ or $V_{IL}$ ; $V_O$ = GND to 5.5 V	-	-	±0.25	-	±2.5	-	±2.5	μА
I <sub>OFF</sub>	power-off leakage current	$V_I$ or $V_O$ = GND to 5.5 V; $V_{CC}$ = 0 V	-	-	0.5	-	5	-	5	μА
I <sub>I</sub>	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0$ V to 5.5 V	-	-	±0.1	-	±1	-	±1	μА
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	2	-	20	-	20	μА
Δl <sub>CC</sub>	additional supply current	per input pin; $V_I = 3.4 \text{ V}$ ; $I_O = 0 \text{ A}$ ; other pins at $V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	1.35	-	1.5	-	1.5	mA

## 10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V. For test circuit see Figure 6.

Symbol	Parameter	Conditions			25 °C		-40 °C	to +85 °C	-40 °C t	Unit	
				Min	Typ[1]	Max	Min	Max	Min	Max	=
t <sub>pd</sub>	propagation	nAn to nYn; see Figure 4	[2]								
	delay	V <sub>CC</sub> = 4.5 V to 5.5 V									
		C <sub>L</sub> = 15 pF		-	2.8	7.4	1	8.5	1	9.5	ns
		C <sub>L</sub> = 50 pF		-	4.4	8.4	1	9.5	1	10.5	ns
t <sub>en</sub> enable time	nOE to nYn; see Figure 5										
		V <sub>CC</sub> = 4.5 V to 5.5 V									
		C <sub>L</sub> = 15 pF		-	3.8	10.4	1	12	1	13	ns
	C <sub>L</sub> = 50 pF		-	5.4	11.4	1	13	1	14.5	ns	
t <sub>dis</sub>	disable time	nOE to nYn; see Figure 5	[2]								
		V <sub>CC</sub> = 4.5 V to 5.5 V									
		C <sub>L</sub> = 15 pF		-	2.9	8	1	11	1	11	ns
		C <sub>L</sub> = 50 pF		-	5.1	11.4	1	13	1	14.5	ns
t <sub>sk(o)</sub>	skew	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V};$ $C_L = 50 \text{ pF}$		-	-	1	-	1	-	1	ns
Cı	input capacitance	$V_I = V_{CC}$ or GND; $V_{CC} = 5 \text{ V}$		-	2	6	-	6	-	6	pF
Co	output capacitance	$V_O = V_{CC}$ or GND; $V_{CC} = 5 \text{ V}$		-	5	-	-	-	-	-	pF
C <sub>PD</sub>	power dissipation capacitance	per buffer; $C_L = 0$ pF; $f = 10$ MHz; $V_I = GND$ to $V_{CC}$	[3]	-	8	-	-	-	-	-	pF

<sup>[1]</sup> Typical values are measured at  $T_{amb}$  = 25 °C and  $V_{CC}$  = 5 V.

 $t_{\text{en}}$  is the same as  $t_{\text{PZL}}$  and  $t_{\text{PZH}}.$ 

 $t_{\mbox{\scriptsize dis}}$  is the same as  $t_{\mbox{\scriptsize PLZ}}$  and  $t_{\mbox{\scriptsize PHZ}}.$ 

[3]  $C_{PD}$  is used to determine the dynamic power dissipation  $P_D$  ( $\mu W$ ).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

 $V_{CC}$  = supply voltage in Volts.

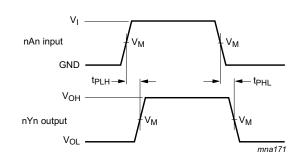
<sup>[2]</sup>  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

 Table 8.
 Noise characteristics

GND = 0 V. For test circuit see Figure 6.

Symbol	Parameter	Conditions	Т	Unit		
			Min	Тур	Max	
$V_{CC} = 5 V$	; C <sub>L</sub> = 50 pF					
$V_{OL(p)}$	LOW-level output voltage (peak)		-	0.5	1.5	V
$V_{OL(v)}$	LOW-level output voltage (valley)		-1.5	-0.3	-	V
V <sub>OH(v)</sub>	HIGH-level output voltage (valley)		-	4.5	-	V
V <sub>IH(AC)</sub>	AC HIGH-level input voltage (dynamic)		2	-	-	V
V <sub>IL(AC)</sub>	AC LOW-level input voltage (dynamic)		-	-	0.8	V

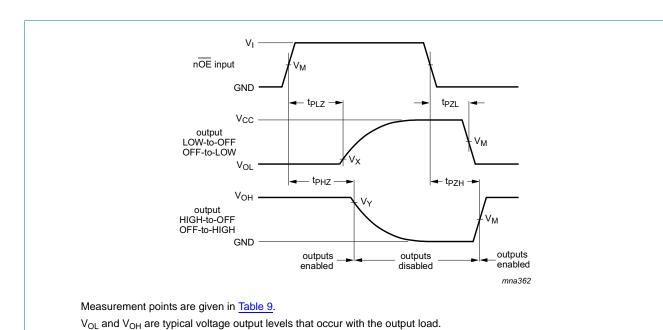
### 11. Waveforms



Measurement points are given in Table 9.

 $V_{\text{OL}}$  and  $V_{\text{OH}}$  are typical voltage output levels that occur with the output load.

Fig 4. Propagation delay input (nAn) to output (nYn)



74AHCT244A

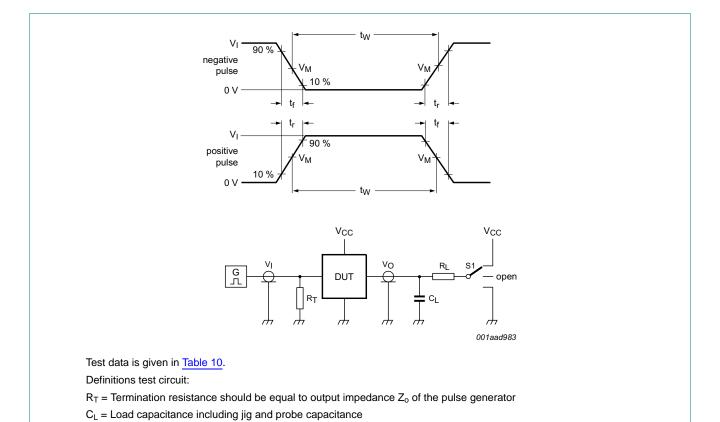
Fig 5.

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enable and disable times

Table 9. Measurement points

Input	Output		
V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>
1.5 V	0.5V <sub>CC</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> – 0.3 V



R<sub>L</sub> = Load resistor S1 = Test selection switch

Fig 6. Test circuit for measuring switching times

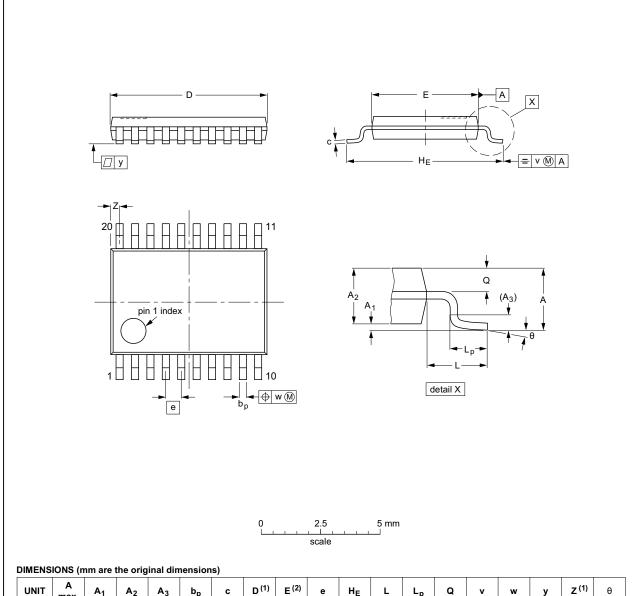
Table 10. Test data

Input		Load		S1 position			
$V_{I}$	t <sub>r</sub> , t <sub>f</sub>	C <sub>L</sub> R <sub>L</sub> t <sub>F</sub>		t <sub>PHL</sub> , t <sub>PLH</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>	
GND to 3.0 V	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V <sub>CC</sub>	

## 12. Package outline

#### TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1



UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	<b>A</b> <sub>3</sub>	bp	C	D <sup>(1)</sup>	E (2)	е	HE	L	Lp	Q	>	w	у	Z <sup>(1)</sup>	θ
mm	1.1	0.15 0.05	0.95 0.80	0.25	0.30 0.19	0.2 0.1	6.6 6.4	4.5 4.3	0.65	6.6 6.2	1	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.5 0.2	8° 0°

#### Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

DUTLINE		REFER	ENCES		EUROPEAN	ISSUE DATE
ERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT360-1		MO-153				<del>-99-12-27</del> 03-02-19
•	ERSION	/ERSION IEC	/ERSION IEC JEDEC	/ERSION IEC JEDEC JEITA	/ERSION IEC JEDEC JEITA	VERSION IEC JEDEC JEITA PROJECTION

Fig 7. Package outline SOT360-1 (TSSOP20)

74AHCT244A

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## 13. Abbreviations

#### Table 11. Abbreviations

Acronym	Description
CDM	Charge Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

## 14. Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHCT244A v.1	20161123	Product data sheet	-	-

## 15. Legal information

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Octal buffer/line driver; 3-state

## 17. Contents

1	General description 1
2	Features and benefits 1
3	Ordering information
4	Functional diagram 2
5	Pinning information
5.1	Pinning
5.2	Pin description
6	Functional description 4
7	Limiting values 4
8	Recommended operating conditions 5
9	Static characteristics 5
10	Dynamic characteristics 6
11	Waveforms
12	Package outline 9
13	Abbreviations
14	Revision history
15	Legal information
15.1	Data sheet status
15.2	Definitions
15.3	Disclaimers
15.4	Trademarks
16	Contact information
17	Contents