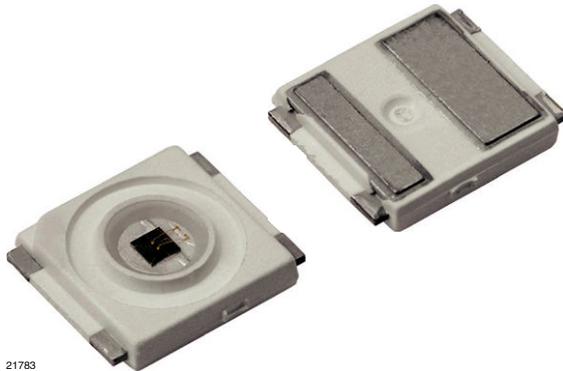




High Power Infrared Emitting Diode, 850 nm, Surface Emitter Technology



21783

DESCRIPTION

As part of the [SurfLight™](#) portfolio, the VSMY7850X01 is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant power and high speed, molded in low thermal resistance Little Star package. A 42 mil chip provides outstanding low forward voltage and allows DC operation of the device up to 1 A.

FEATURES

- Package type: surface mount
- Package form: Little Star®
- Dimensions (L x W x H in mm): 6.0 x 7.0 x 1.5
- Peak wavelength: $\lambda_p = 850$ nm
- High reliability
- High radiant power
- High radiant intensity
- Angle of half intensity: $\phi = \pm 60^\circ$
- Low forward voltage
- Designed for high drive currents: Up to 1 A_{DC} and up to 5 A pulses
- Low thermal resistance: R_{thJP} = 10 K/W
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



APPLICATIONS

- Infrared illumination for CMOS cameras (CCTV)
- Driver assistance systems
- Machine vision IR data transmission
- 3D TV

PRODUCT SUMMARY				
COMPONENT	I _e (mW/sr)	φ (deg)	λ _p (nm)	t _r (ns)
VSMY7850X01	170	± 60	850	15

Note

- Test conditions see table “Basic Characteristics”

ORDERING INFORMATION			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY7850X01-GS08	Tape and reel	MOQ: 2000 pcs, 2000 pcs/reel	Little Star

Note

- MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V _R	5	V
Forward current		I _F	1	A
Peak forward current	t _p /T = 0.5, t _p = 100 μs	I _{FM}	2	A
Surge forward current	t _p = 100 μs	I _{FSM}	5	A
Power dissipation		P _V	2.5	W
Junction temperature		T _j	125	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature	Acc. figure 7, J-STD-20	T _{sd}	260	°C
Thermal resistance junction/pin	Acc. J-STD-051, soldered on PCB	R _{thJP}	10	K/W

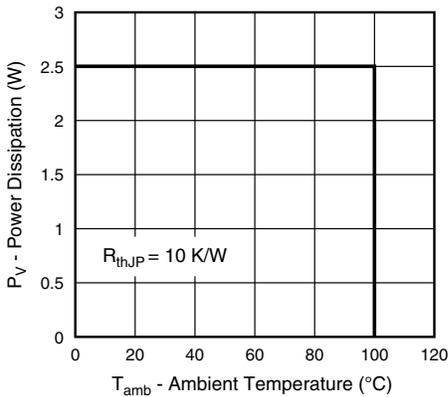


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

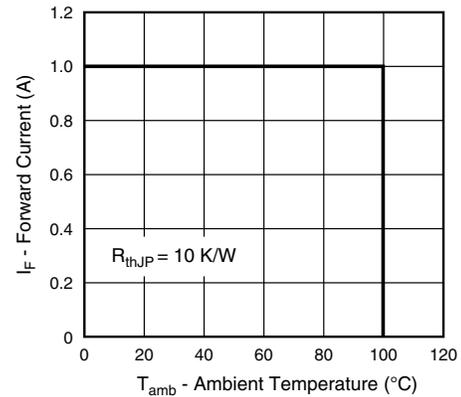


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 1\text{ A}$, $t_p = 20\text{ ms}$	V_F		2.0	2.5	V
	$I_F = 5\text{ A}$, $t_p = 100\text{ }\mu\text{s}$	V_F		3.5		V
Temperature coefficient of V_F	$I_F = 1\text{ A}$	TK_{V_F}		- 0.2		mV/K
Reverse current	$V_R = 5\text{ V}$	I_R	not designed for reverse operation			μA
Radiant intensity	$I_F = 1\text{ A}$, $t_p = 20\text{ ms}$	I_e	130	170	390	mW/sr
	$I_F = 5\text{ A}$, $t_p = 100\text{ }\mu\text{s}$	I_e		780		mW/sr
Radiant power	$I_F = 1\text{ A}$, $t_p = 20\text{ ms}$	ϕ_e		520		mW
Temperature coefficient of ϕ_e	$I_F = 1\text{ A}$	TK_{ϕ_e}		- 0.5		%/K
Angle of half intensity		ϕ		± 60		deg
Peak wavelength	$I_F = 1\text{ A}$	λ_p		850		nm
Spectral bandwidth	$I_F = 1\text{ A}$	$\Delta\lambda$		30		nm
Temperature coefficient of λ_p	$I_F = 1\text{ A}$	TK_{λ_p}		0.2		nm/K
Rise time	$I_F = 1\text{ A}$	t_r		15		ns
Fall time	$I_F = 1\text{ A}$	t_f		18		ns

BASIC CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

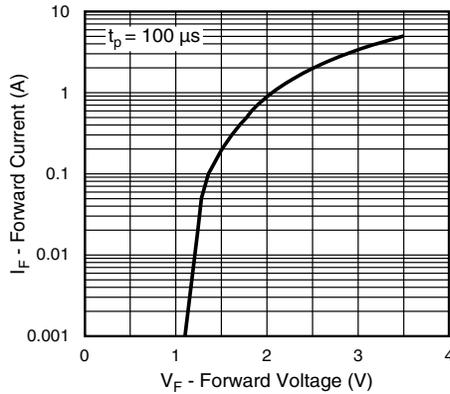


Fig. 3 - Forward Current vs. Forward Voltage

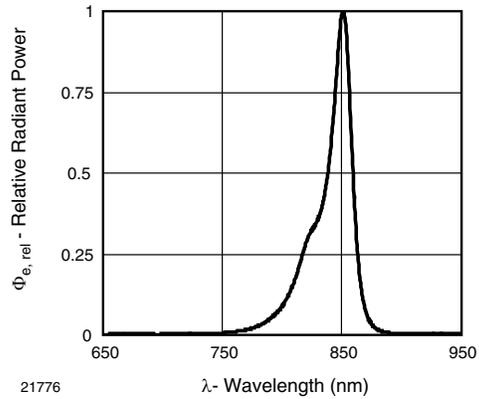


Fig. 5 - Relative Radiant Power vs. Wavelength

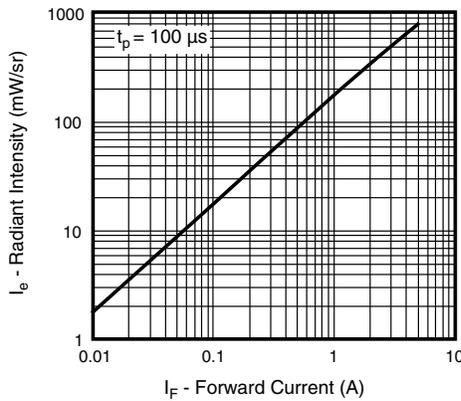


Fig. 4 - Radiant Intensity vs. Forward Current

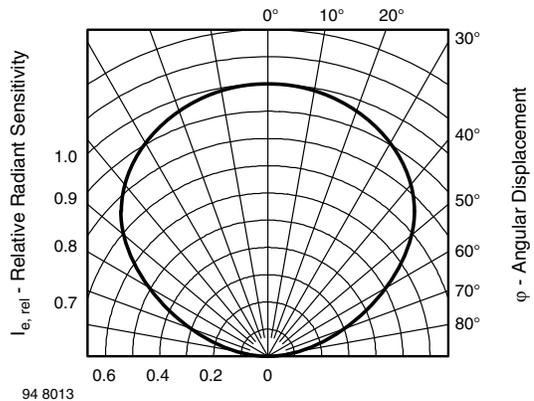
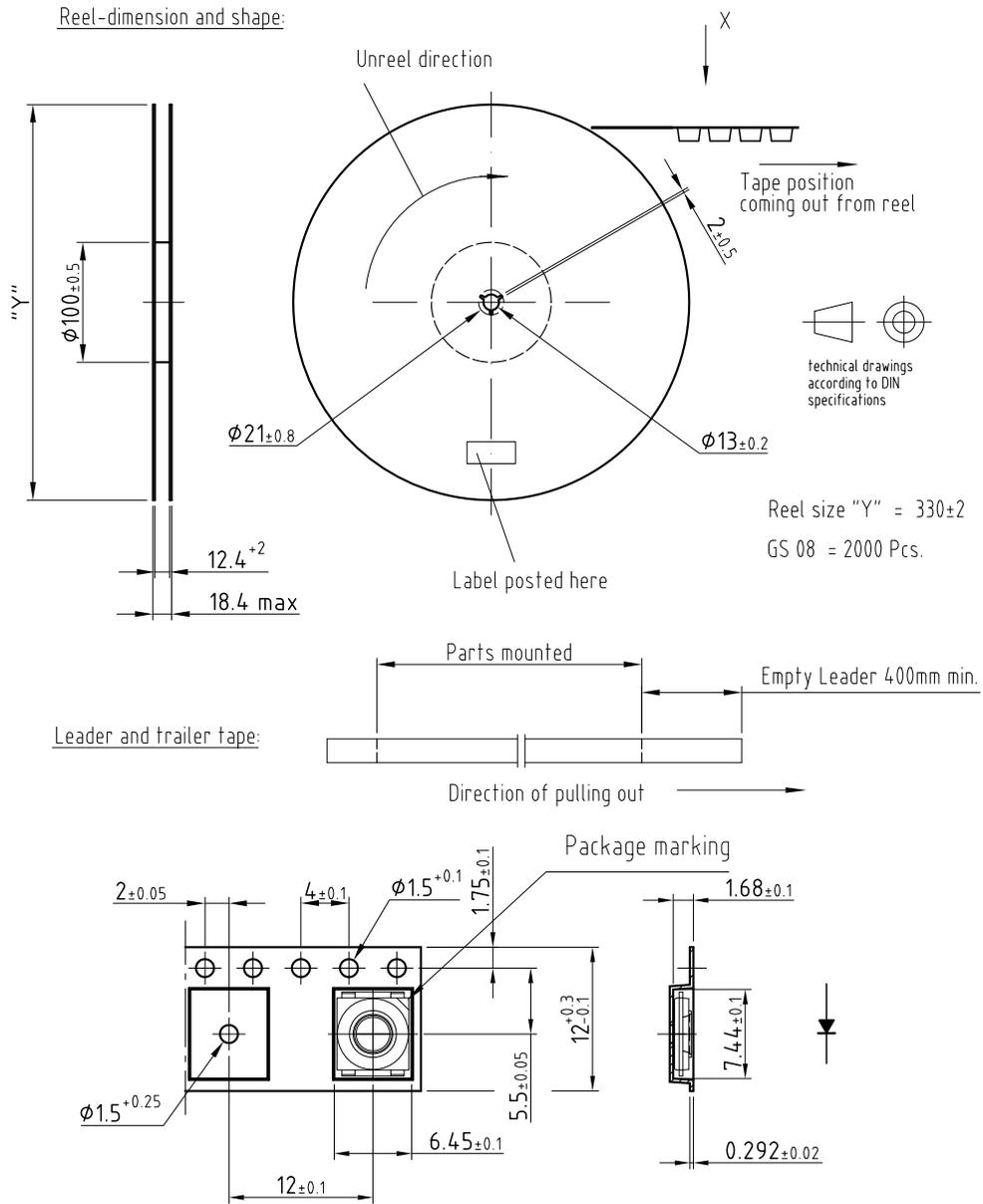


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

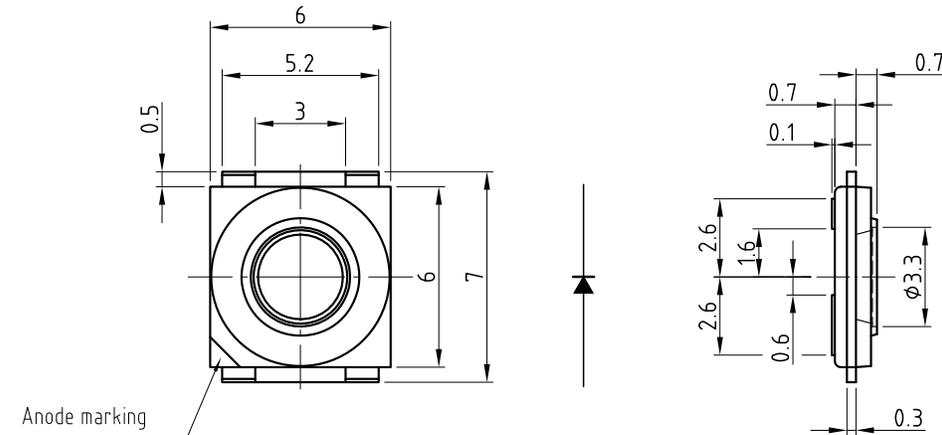
TAPING DIMENSIONS in millimeters



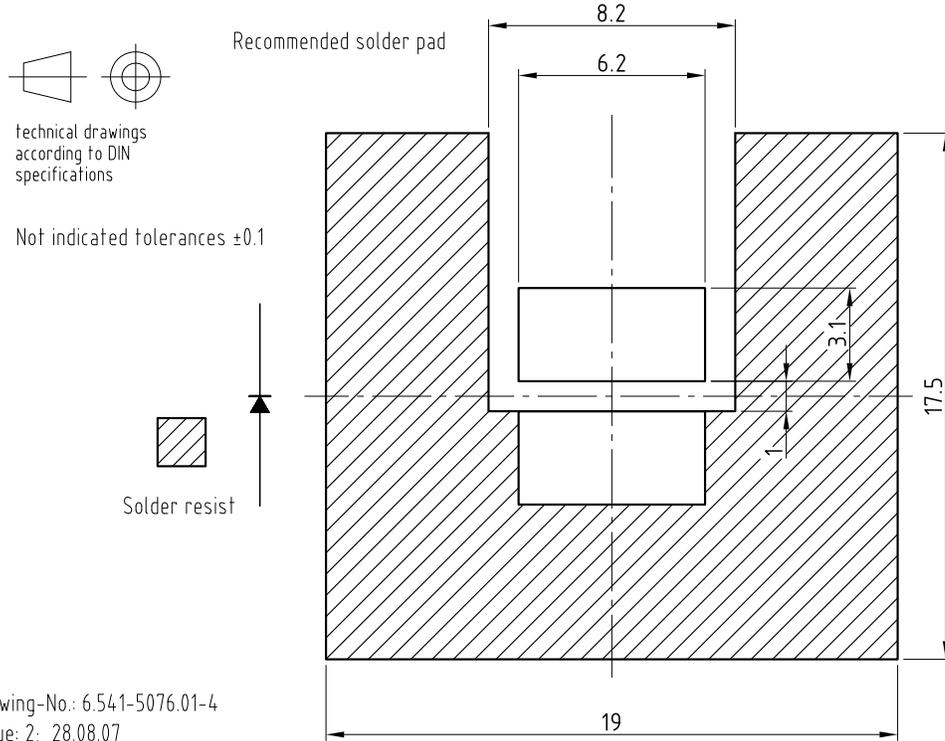
Drawing-No.: 9.800-5094.01-4
Issue: 3; 22.01.08
20846



PACKAGE DIMENSIONS in millimeters



Anode marking



Drawing-No.: 6.541-5076.01-4
Issue: 2; 28.08.07
20848

SOLDER PROFILE

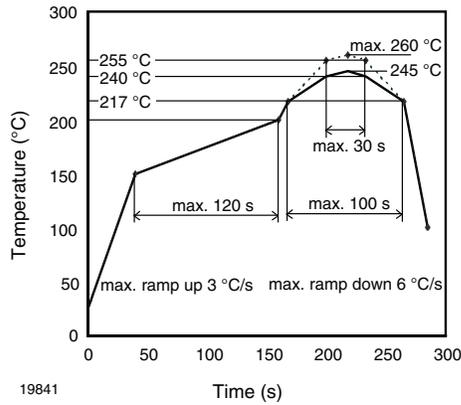


Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020 for Preconditioning acc. to JEDEC, Level 2a

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

Conditions: $T_{amb} < 30\text{ }^{\circ}\text{C}$, $RH < 60\%$

Moisture sensitivity level 2a, acc. to J-STD-020B

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at $40\text{ }^{\circ}\text{C}$ ($+ 5\text{ }^{\circ}\text{C}$), $RH < 5\%$.



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.