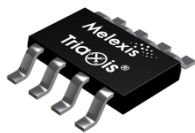


# MLX90380 - Triaxis<sup>®</sup> Resolver

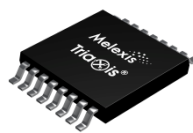
## Datasheet

### 1. Features and Benefits

- Triaxis<sup>®</sup> Hall Technology
- Sine and Cosine Analog Outputs
- Output Refresh Rate 4 $\mu$ s
- 3.3V and 5V Operating Supply Voltage
- Operating Temperature Range from -40°C to 150°C
- Selectable Magnetic Field Axis (X/Y - X/Z - Z/Y)
- Selectable Sensitivity Range (10mT ... 70mT)
- End-of-Shaft / Through-Shaft Operation
- Ratiometric Outputs
- Programming Through the Connector
- RoHS Compliant Packages:
  - Single Die – SOIC-8
  - Dual Die Fully Redundant – TSSOP-16



SOIC-8



TSSOP-16

### 2. Application Examples

- Absolute Rotary Position Sensor
- Brushless Motor Control
  - Permanent Magnet Synchronous Motor
  - Brushless DC Motor (BLDC)
- Field-oriented Motor Control

### 3. Description

The MLX90380 is a monolithic contactless sensor IC sensitive to the flux density applied orthogonally and parallel to the IC surface.

High-speed dual analog outputs allow the MLX90380 to deliver accurate sine/cosine signals when used with a rotating permanent magnet.

With a wide range of operating temperature, supply voltage and magnetic flux density, the MLX90380 can resolve the angular position of a rotating axis over 360 degrees in many industrial and automotive applications.

Selectable magnetic axes ensure compatibility with end-of-shaft and through-shaft configurations.

Sensitivity and filter bandwidth can be programmed through the 4 application pins to optimally use the ADC input range of the companion MCU.

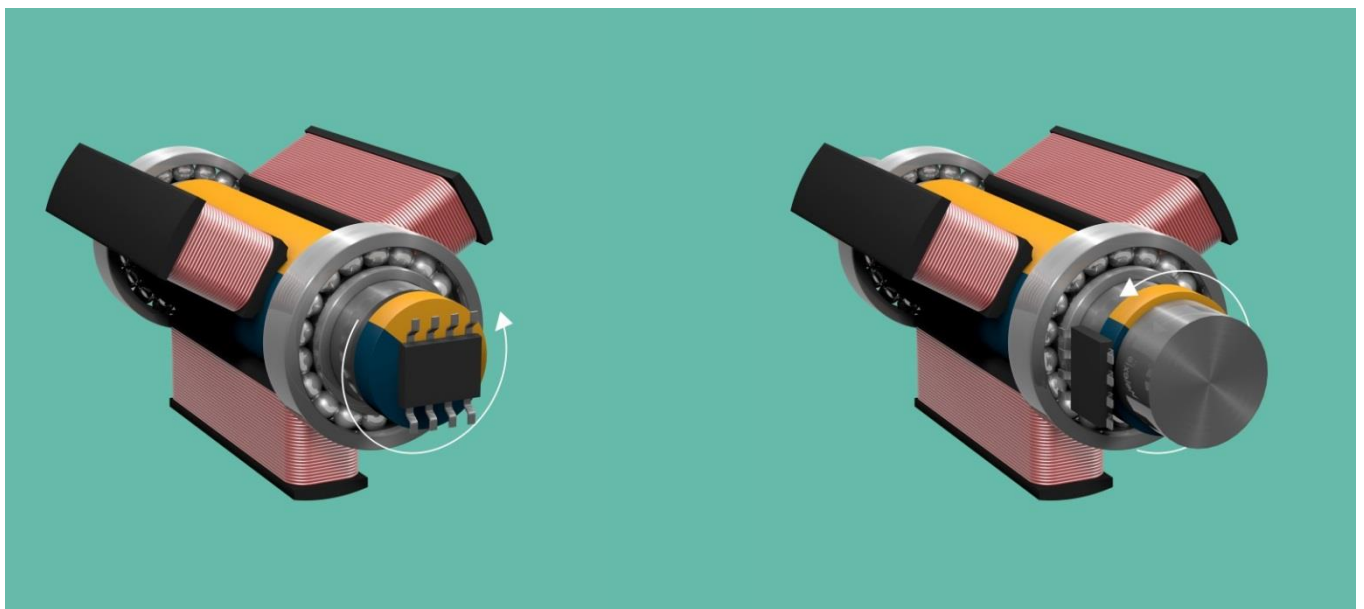


Figure 1: End of Shaft

Figure 2: Through Shaft

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## 4. Ordering Information <sup>(1)</sup>

Product Code	Temperature Code	Package Code	Option Code	Packing Form Code
MLX90380	L (-40°C to 150°C)	DC	BAB-003	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-005	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-023	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-025	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-043	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-045	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-103	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-105	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-123	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-125	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-143	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-145	RE
MLX90380	L (-40°C to 150°C)	DC	BAB-506	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-003	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-005	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-023	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-025	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-043	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-045	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-103	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-105	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-123	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-125	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-143	RE
MLX90380	L (-40°C to 150°C)	GO	BAB-145	RE

*Table 1: Ordering Codes available. All option codes of Table 2 can be made.*

<sup>1</sup> Other option codes and packing forms are available upon request. Every combination in Table 2 can be made pending on yearly volume. Please contact your sales representative to see what combinations are available.

## Legend:

Temperature Code:	L: from -40°C to 150°C
Package Code:	DC: for SOIC-8 package GO: for TSSOP-16 package (dual die)
Option Code:	<p>BAX-123</p> <p><b>Sensitivity Temperature Coefficient:</b></p> <p>BAA-xxx: SmCo magnet BAB-xxx: NdFeB magnet BAC-xxx: Ferrite magnet</p> <p><b>Sensitivity:</b></p> <p>BAX-xx0: 40mT magnetic range<sup>(2)</sup>          BAX-xx1: 10mT magnetic range<sup>(2)</sup> for 3.3V operating supply voltage only          BAX-xx2: 20mT magnetic range<sup>(2)</sup>          BAX-xx3: 30mT magnetic range<sup>(2)</sup>          BAX-xx4: 50mT magnetic range<sup>(2)</sup>          BAX-xx5: 60mT magnetic range<sup>(2)</sup>          BAX-xx6: 70mT magnetic range<sup>(2)</sup>          BAX-xx7: 15mT magnetic range<sup>(2)</sup> for 5V operating supply voltage only</p> <p><b>Magnetic axis:</b></p> <p>BAX-x0x: X/Y magnetic axis – equal sensitivity<sup>(3)</sup>          BAX-x1x: X/Z magnetic axis – equal sensitivity<sup>(3)</sup>          BAX-x2x: Z/Y magnetic axis – equal sensitivity<sup>(3)</sup>          BAX-x3x: X/Z magnetic axis – sensitivity mismatch<sup>(4)</sup>          BAX-x4x: Z/Y magnetic axis – sensitivity mismatch<sup>(4)</sup></p> <p><b>Operating voltage and Bandwidth:</b></p> <p>BAX-0xx: 3.3V operating supply voltage – low BW          BAX-1xx: 5V operating supply voltage – low BW          BAX-2xx: 3.3V operating supply voltage – medium BW          BAX-3xx: 5V operating supply voltage – medium BW          BAX-4xx: 3.3V operating supply voltage – high BW          BAX-5xx: 5V operating supply voltage – high BW</p>
Packing Form:	RE: for Reel
Ordering Example:	MLX90380LDC-BAA-000-RE: SOIC-8 150, SmCo magnet, 3.3V operating supply voltage, Low bandwidth, X/Y magnetic axis, 40mT full scale magnetic range, Reel

Table 2: Order code Description

<sup>2</sup> The magnetic range is the in-plane (X or Y) flux density, that results in a  $\pm 40\%$ VDD output swing with a  $V_{OQ}$  of 50%VDD with  $T_A=25^\circ\text{C}$ ,  $V_{DD}=V_{DDNOM}$

<sup>3</sup> Magnetic axis for: two pole magnet end of shaft / on axis and multi-pole magnet off-axis / through-shaft application.

<sup>4</sup> Magnetic axis for: two pole magnet off-axis / through-shaft application. The magnetic flux norm for the Z axis is double the magnetic flux norm for the X or Y axis:  $B_{xy}/B_z = 50\%$ .

## 5. Functional Diagram

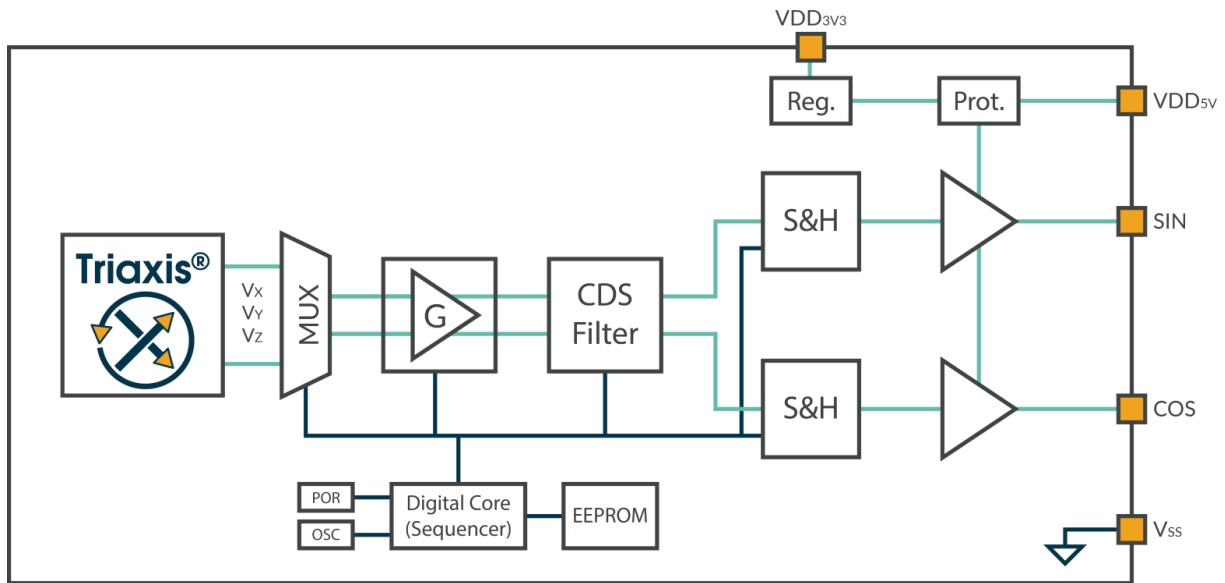


Figure 3: Functional Diagram

## 6. Glossary of Terms

Gauss (G), Tesla (T)	Units for the magnetic flux density – 1 mT = 10 G
TC	Temperature Coefficient (in ppm/°C.)
NC	Not Connected
EMC	Electro-Magnetic Compatibility

Table 3: Glossary of Terms

## 7. Detailed General Description

The MLX90380 is a monolithic sensor IC sensitive to the flux density applied orthogonally and parallel to the IC surface. High-speed dual analog outputs allow the MLX90380 to deliver accurate, contact-less, true 360Deg. sine/cosine signals when used with a rotating permanent magnet.

OUT<sub>1</sub> and OUT<sub>2</sub> output voltages are proportional to the applied magnetic field along 2 axes. Those 2 axes are specified by the option code. For example, option code BAA-000 is a chip where B<sub>x</sub> and B<sub>y</sub> components of the magnetic field are such that  $OUT_1 = V_{oq1} + S_1 \times B_x$  and  $OUT_2 = V_{oq2} + S_2 \times B_y$ .

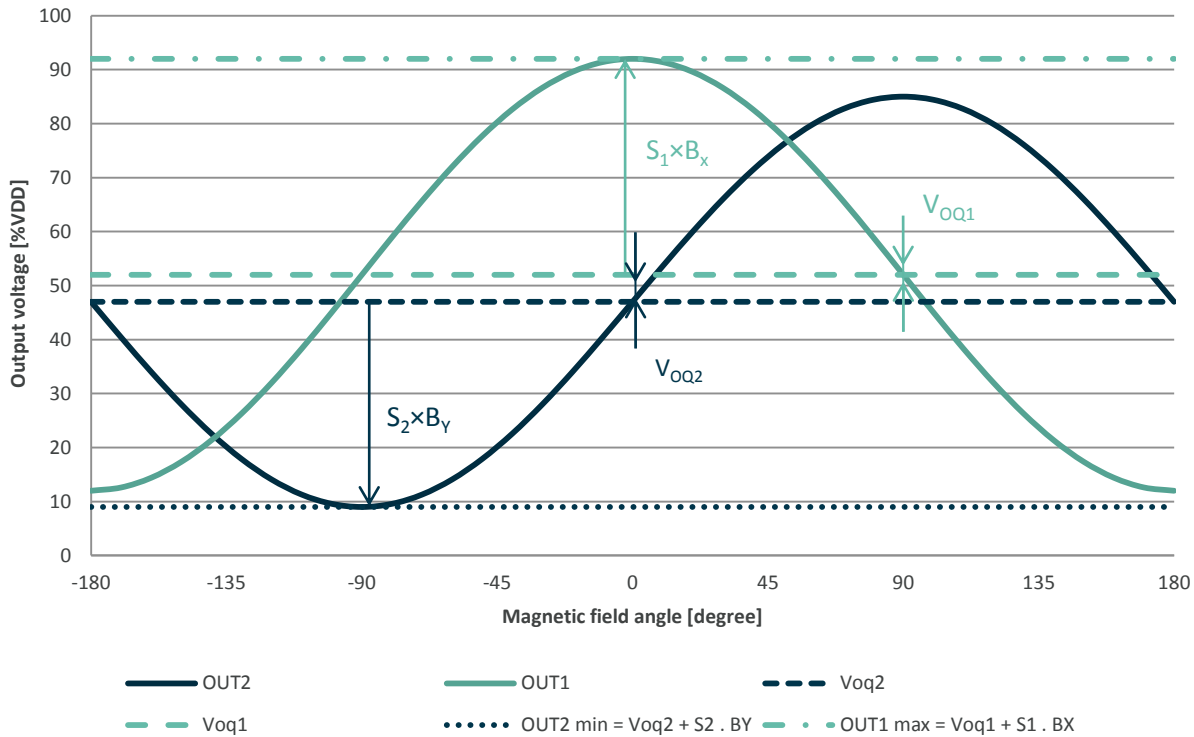


Figure 4: Output characteristics

Customers can order pre-programmed devices. End of line calibration is optional to achieve higher position accuracy.

## 8. Intrinsic Magnetic Axis



Figure 5: Axis configuration

Figure 5 gives the axis convention. Thus, take for example an IC in Z/Y configuration. Then, OUT1 will give Bz & OUT2 will give By.

The following sections list a detailed explanation of the intrinsic axis. It explains clock wise and counter clock wise behavior of the angle calculation.

CW – Clockwise turn: The magnet needs to turn in the CW direction to get a positive slope of the angle calculated by ATAN.

CCW – Counterclockwise turn: The magnet needs to turn in the CCW direction to get a positive slope of the angle calculated by ATAN.

The CW - CCW rotation direction can be modified by changing the COS / SIN assignment to SIN / COS with an angle correction of 90 Deg.

$$\alpha = ATAN\left(\frac{SIN}{COS}\right)$$

The convention of preprogrammed parts is such that OUT2 is seen as SIN and OUT1 as COS.



### 8.1. X/Y magnetic axis

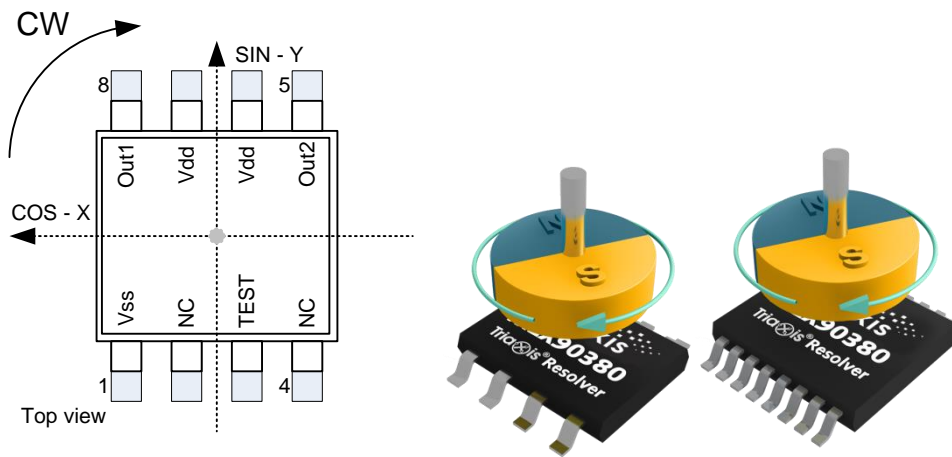


Figure 6: X/Y Magnetic Axis

### 8.2. X/Z magnetic axis

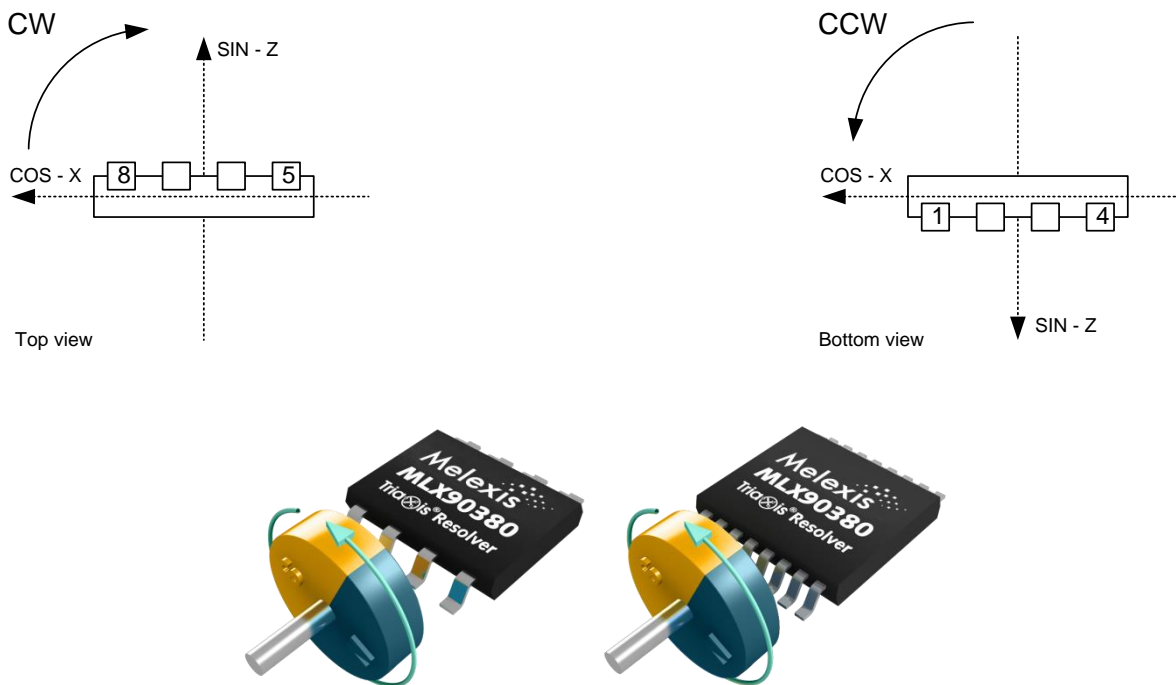


Figure 7: X/Z Magnetic Axis

### 8.3. Z/Y magnetic axis

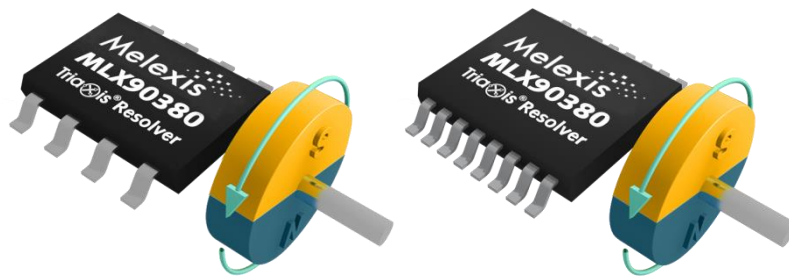


Figure 8: Z/Y Magnetic Axis

## 9. Pin Definitions and Descriptions

### 9.1. Pin Definition for SOIC-8

Pin #	Name	Description
1	VSS	Supply Ground
2	NC	Not Used
3	TEST	Test Pin
4	NC	Not Used
5	OUT2	Analog Output SIN
6	VDD5V	Supply 5V
7	VDD3V3	Supply 3V3
8	OUT1	Analog Output COS

Table 4: Pin Definition SOIC-8

For optimal EMC behavior connect the test pin and unused pins (Not Used) to the Ground.

Important: for 5V-mode applications, leave the VDD3V3 floating. Check the section 13 “Recommended Application Diagrams” for more details.

## 9.2. Pin Definition for TSSOP-16

Pin #	Name	Description
1	VSS_A	Supply Ground Die A
2	NC	Not Used
3	NC	Not Used
4	TEST_A	Test Pin Die A
5	OUT2_B	Analog Output SIN Die B
6	VDD5V_B	Supply 5V Die B
7	VDD3V3_B	Supply 3V3 Die B
8	OUT1_B	Analog Output COS Die B
9	VSS_B	Supply Ground Die B
10	NC	Not Used
11	TEST_B	Test Pin Die B
12	NC	Not Used
13	OUT2_A	Analog Output SIN Die A
14	VDD5V_A	Supply 5V Die A
15	VDD3V3_A	Supply 3V3 Die A
16	OUT1_A	Analog Output COS Die A

*Table 5: Pin Definition TSSOP-16*

For optimal EMC behavior connect the test pin and unused pins (Not Used) to the Ground.

Important: for 5V applications, leave the VDD3V3 floating. Check the section “Recommended Application Diagrams” for more details.

## 10. Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Condition
Supply Voltage (5V)	V <sub>DD5V</sub>	-16	24	V	
Supply Voltage (3.3V)	V <sub>DD3V3</sub>	-0.3	7	V	
Supply Current <sup>(5)</sup>	I <sub>DD</sub>	-10	10	mA	Excluding external load on OUT1&2.
Negative Output Voltage	V <sub>OUT</sub>	-0.3	-	V	
Positive Output Voltage (5V Application Diagram)	V <sub>OUT</sub>	-	18	V	
Positive Output Voltage (3.3V Application Diagram)	V <sub>OUT</sub>	-	17	V	
Output Current	I <sub>OUT</sub>	-20	20	mA	
Operating Temperature Range	T <sub>A</sub>	-40	150	°C	
Maximum Junction Temperature	T <sub>J</sub>	-	165	°C	
Storage Temperature Range	T <sub>S</sub>	-55	165	°C	
ESD Sensitivity (Human Body Model) AEC-Q100-002 standard	ESD <sub>HBM</sub>	-	2	kV	Valid for SOIC-8. TSSOP-16 cross die ESD limit is 1.5kV.
ESD Sensitivity (Charged Device Model) AEC-Q100-011 standard	ESD <sub>CDM</sub>	-	500	V	
Maximum Flux Density	B	-1	1	T	

Table 6: Absolute Maximum Ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute maximum-rated conditions for extended periods may affect device reliability.

## 11. General Electrical Specifications

Operating Characteristics, VDD=3.135V to 3.6V or VDD=4.5V to 5.5V (depending on the option code), T<sub>A</sub> = -40°C to 150°C (unless otherwise specified)

Electrical Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Supply Voltage (operating)	V <sub>DD3V3</sub>	3.135	3.3	3.6	V	SOIC-8 and TSSOP-16 packages
Supply Voltage (operating)	V <sub>DD5V</sub>	4.5	5	5.5	V	SOIC-8 and TSSOP-16 packages
Supply Current	I <sub>DD</sub>	5	-	9	mA	Excluding external load on OUT1&2 3.135 ≤ VDD ≤ 3.6
		6	-	10	mA	4.5 ≤ VDD ≤ 5.5

<sup>5</sup> Note I<sub>DD</sub> may exceed 10mA with a pull-down on OUT1&2.

Electrical Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Power On Reset Voltage Rising	VPOR_H	2.4	2.75	3	V	OUT HiZ → operating mode
		2.9	3.4	3.9	V	Application Diagram VDD3V3 Application Diagram VDD5V
Power On Reset Voltage Falling	VPOR_L	2.1	2.4	2.7	V	Operating mode → OUT HiZ
		2.5	3.05	3.5	V	Application Diagram VDD3V3 Application Diagram VDD5V
Programming Voltage (PTC)	MT7V	6.1	6.5	7	V	Rising Entry Level on VDD
Load Current Range	IOUT	-1	-	1	mA	
Load Resistance Range <sup>(6)</sup>	RL	10	-	∞	kΩ	Connected between OUT and GND
		5	-	∞	kΩ	Application Diagram VDD3V3 Application Diagram VDD5V
Load Capacitor Range	CL	1.5	2.2	10	nF	Connected between OUT and GND
Output Voltage Range	Vo	5	-	95	%VDD	Linearity better than 1.5%
		10	-	90	%VDD	Linearity better than 0.5%
Output Resistance	ROUT	-	25	-	Ω	IOUT= ±1mA
Power-On Time <sup>(7)</sup>	ton	-	-	200	μs	High bandwidth programming VPOR_H ≤ VDD ≤ VDD5V_Max VPOR_H ≤ VDD ≤ VDD3V3_Max
Chopping Frequency	FCHOP	0.9	1	1.1	MHz	
Output Update Period	Ts	-	4	-	μs	Each field component takes 2μs, measured sequentially
Output Noise Voltage <sup>(8)</sup>	Vnrms	-	-	3.1	mV	RMS noise, B=0mT, VDD = typical value S=1 %VDD/mT (50mV/mT at VDD=5V)
		-	-	4.6	mV	Low bandwidth programming
		-	-	10	mV	Medium bandwidth programming High bandwidth programming
Nyquist Frequency	Fnyq	-	125	-	kHz	
Tracking Delay <sup>(9)</sup>		-	12	17.5	μs	High bandwidth
		-	29.5	38	μs	Medium bandwidth
		-	74	85.5	μs	Low bandwidth

Table 7 General Electrical Specifications

<sup>6</sup> A Pull down enables EEPROM error detection when the output goes in HiZ mode.

<sup>7</sup> Lower bandwidth programming increases the Power-On Time by the tracking delay (see footnote 9).

<sup>8</sup> Higher sensitivity programming may increase the output noise voltage.

<sup>9</sup> Tracking Delay is defined as the time delay between a rotating magnetic stimuli and the change on both outputs, SIN and COS. This delay includes the sample and hold filter which can be programmed by the customer according to the following equation:  $V_o = V_i/n + n z^{-1} V_o/n$  with  $\text{Bandwidth} = 1/(2\pi n T_s)$  and  $n=1, 2, \dots, 16$ .

## 12. Magnetic Specifications<sup>(10)</sup>

Operating Characteristics, VDD=3.135V to 3.6V or VDD=4.5V to 5.5V (depending on the option code), T<sub>A</sub> = -40°C to 150°C (unless otherwise specified).

Magnetic Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Full Scale Magnetic Range (programmable)	Bfs	10	-	70	mT	
Magnetic Flux Density	Bz	-	-	140	mT	
Sensitivity <sup>(11)</sup>	S	-	1 <sup>(12)</sup>	-	%VDD/mT	xx0: 40mT magnetic range
		-	4 <sup>(12)</sup>	-	%VDD/mT	xx1: 10mT magnetic range <sup>(13)</sup>
		-	2 <sup>(12)</sup>	-	%VDD/mT	xx2: 20mT magnetic range
		-	1.33 <sup>(12)</sup>	-	%VDD/mT	xx3: 30mT magnetic range
		-	0.8 <sup>(12)</sup>	-	%VDD/mT	xx4: 50mT magnetic range
		-	0.66 <sup>(12)</sup>	-	%VDD/mT	xx5: 60mT magnetic range
		-	0.57 <sup>(12)</sup>	-	%VDD/mT	xx6: 70mT magnetic range
		-	2.66 <sup>(12)</sup>	-	%VDD/mT	xx7: 15mT magnetic range <sup>(14)</sup>
Sensitivity Accuracy		-5	-	5	%	X/Y magnetic axis
		-8	-	8	%	Z magnetic axis T <sub>A</sub> =25°C, V <sub>DD</sub> =V <sub>DDNOM</sub> , S=1%VDD/mT
Sensitivity Temperature Coefficient <sup>(15)</sup>	TCS	-	350	-	ppm/°C	First Order approximation SmCo
		-	1100	-	ppm/°C	NdFeB
		-	2000	-	ppm/°C	Ferrite
Sensitivity Temperature Coefficient Drift	TCSdrift	-500	-	500	ppm/°C	Temperature drift around programmed TCS
Sensitivity Ratiometry		-2.5	-	2.5	% / V	
Linearity		-0.5	-	0.5	%	V <sub>DD</sub> =5V, output voltage within [0.5V;4.5V]
		-0.5	-	0.5	%	V <sub>DD</sub> =3.3V, output voltage within [0.33V;2.97V]
		-1.5	-	1.5	%	V <sub>DD</sub> =5V, output voltage within [0.25V;4.75V]
		-1.5	-	1.5	%	V <sub>DD</sub> =3.3V, output voltage in [0.165V;3.135V]
Sensitivity Mismatch		-5	-	5	%	X vs. Y, with/without gain adjustment
		-	-20	-	%	Z vs. X, Z vs. Y, without gain adjustment
		-5	-	5	%	Z vs. X, Z vs. Y, with gain adjustment

<sup>10</sup> Please refer to the [User\\_Interface\\_description\\_MLX90380\\_PSF\\_sample.xls.pdf](#) for customer programmable parameters.

<sup>11</sup> Sensitivity and associated magnetic full scale range are factory programmed (see Option Codes).

<sup>12</sup> For option code x3x and x4x the sensitivity for the Z axis is half of the sensitivity for the X or Y axis. The given figure is for the X or Y axis.

<sup>13</sup> for 3.3V operating supply voltage only

<sup>14</sup> for 5V operating supply voltage only

<sup>15</sup> See section 12.1 for second order behavior.

Magnetic Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Output Offset Level	Voq	47.5	50	52.5	%VDD	B=0mT, T <sub>A</sub> =25°C
Output Offset Temperature Drift <sup>(16)</sup>		-1.5 -2.2	0	1.5 2.2	%VDD %VDD	V <sub>DD</sub> =5V V <sub>DD</sub> =3.3V B=0mT, S=1 %VDD/mT
Output Offset Ratiometry		-2.5	-	2.5	%	B=0mT (dV <sub>OQ</sub> / V <sub>OQ</sub> ) - (dV <sub>DD</sub> / V <sub>DD</sub> ) -10% < dV <sub>DD</sub> /V <sub>DD</sub> < 10%
Magnetic Angular Speed		0	-	25000	rpm	
Signal Phase Shift	PHI	-	1.8	-	Degree	At 25000 rpm, high bandwidth programming
Output Update Rate		-	1.6	-	Sample/ Degree	At 25000 rpm, high bandwidth programming
Total Angular Error without Correction <sup>(17)</sup>		-10 -15	-	10 15	Degree Degree	T=25°C -40°C ≤ T ≤ 150°C
Total Angular Error with Dynamic Compensation <sup>(18)</sup>		-1	-	1	Degree	

Table 8: Magnetic Specifications

<sup>16</sup> See section 12.2 for the full Output Offset Temperature Drift Characteristics

<sup>17</sup> Total angle error with an external homogeneous magnetic field stimulus. System design and manufacturing (magnet eccentricity and off-axis) may degrade the accuracy.

<sup>18</sup> This system error budget is explained in more detail in a separate application note. It is dependent on the magnetic system and the correction algorithm of the ECU. A very popular method to reduce total angle error is offset and amplitude correction (e.g. min/max COS/SIN scaling by the system ECU after one full magnet revolution). The total angle error refers only to the linearity error associated to the MLX90380. The linearity error associated to the magnetic and mechanical (off-axis, eccentricity) design is not included and should be considered as an additional contribution. The total error can be reduced either statically (room temperature compensation) or dynamically (continuous compensation) through the off-chip signal processing performing the angular computation i.e.  $\arctan(\text{OUT2}/\text{OUT1})$ . The compensation is applied prior to computing the arctangent function: offset, gain and phase correction can be applied.

## 12.1. Sensitivity Temperature Coefficient Characteristic

The sensitivity temperature coefficient of the MLX90380 is a first order sensitivity compensation to counter the degrading of the magnets field strength over temperature. The TCS is foreseen with a target TCS of SmCo, NdFeB and Ferrite. The actual TCS of the sensor will show a small 2nd order on the target as illustrated in the figure below.

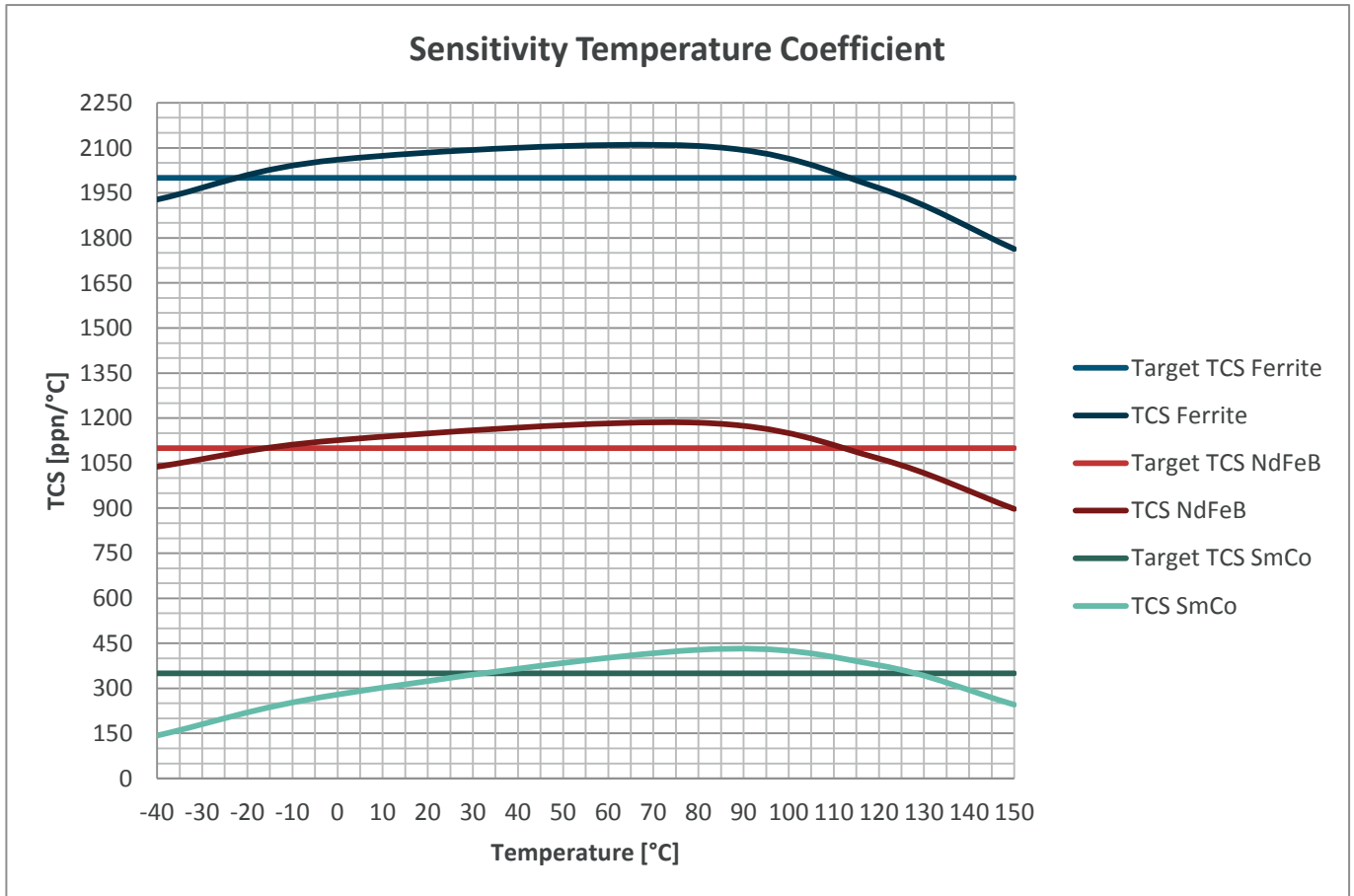


Figure 9: Target TCS vs. 2<sup>nd</sup> order TCS

## 12.2. Output Offset Temperature Drift Characteristic

In the Magnetic Specifications, the Output Offset Temperature Drift of the MLX90380 is specified for B=0mT, VDD=VDDNOM, S=1 %VDD/mT  $-40^{\circ}\text{C} \leq T \leq 150^{\circ}\text{C}$ . The charts below list the Output Offset Temperature Drift characteristics for the full magnetic range.



12.2.1. Output Offset Temperature Drift Characteristic



Figure 10: Output Offset Temperature Drift per Magnet Type

### 12.2.2. Output Offset Temperature Drift Characteristic vs. Sensitivity

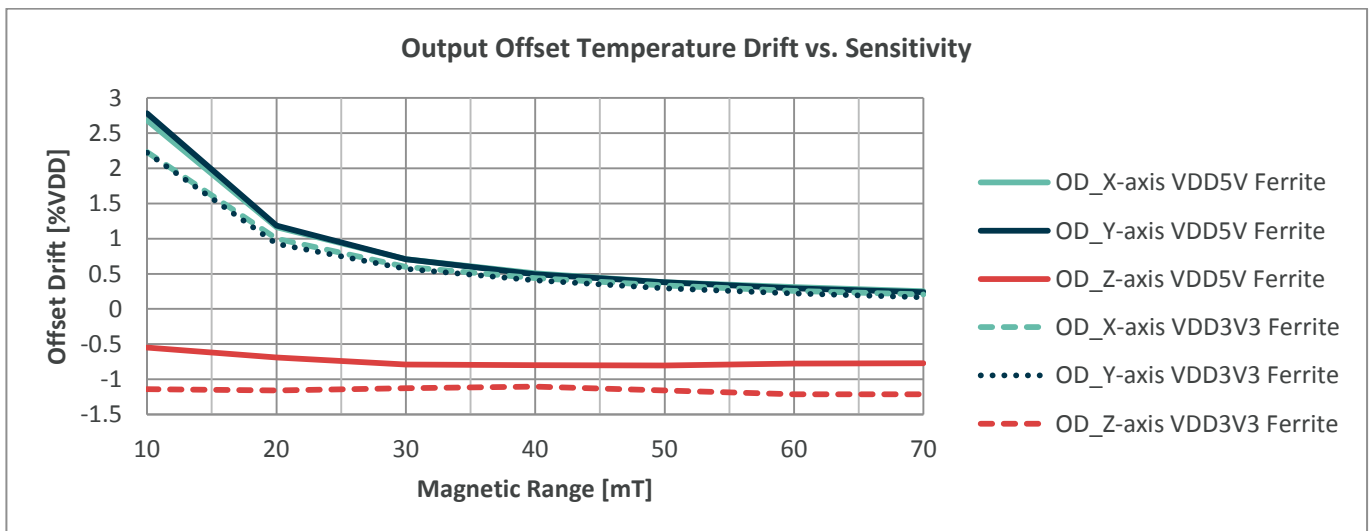
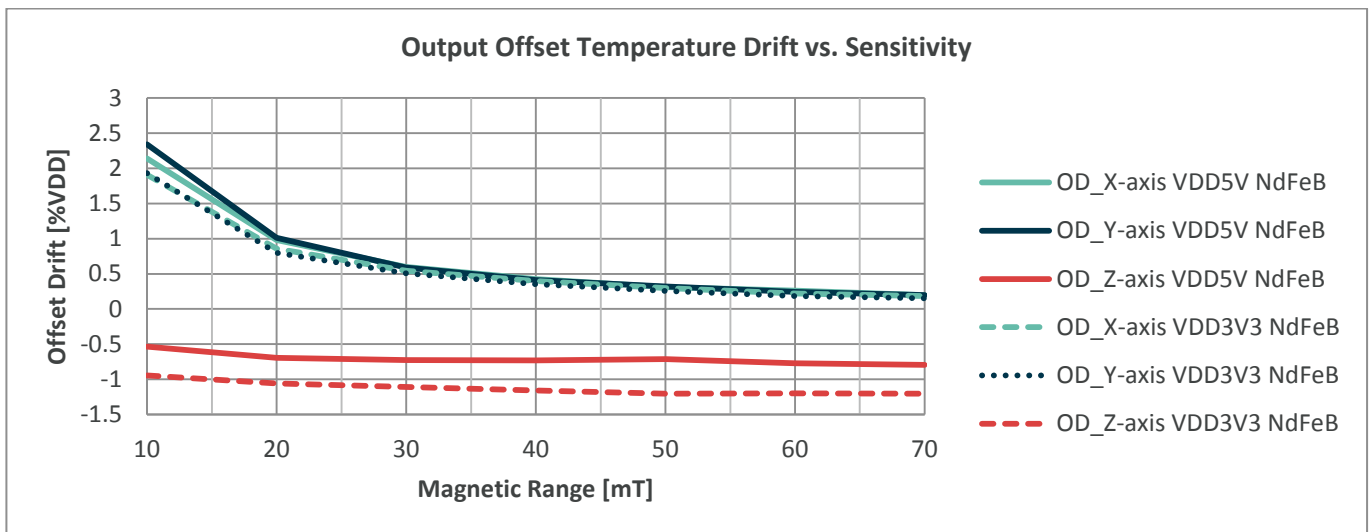
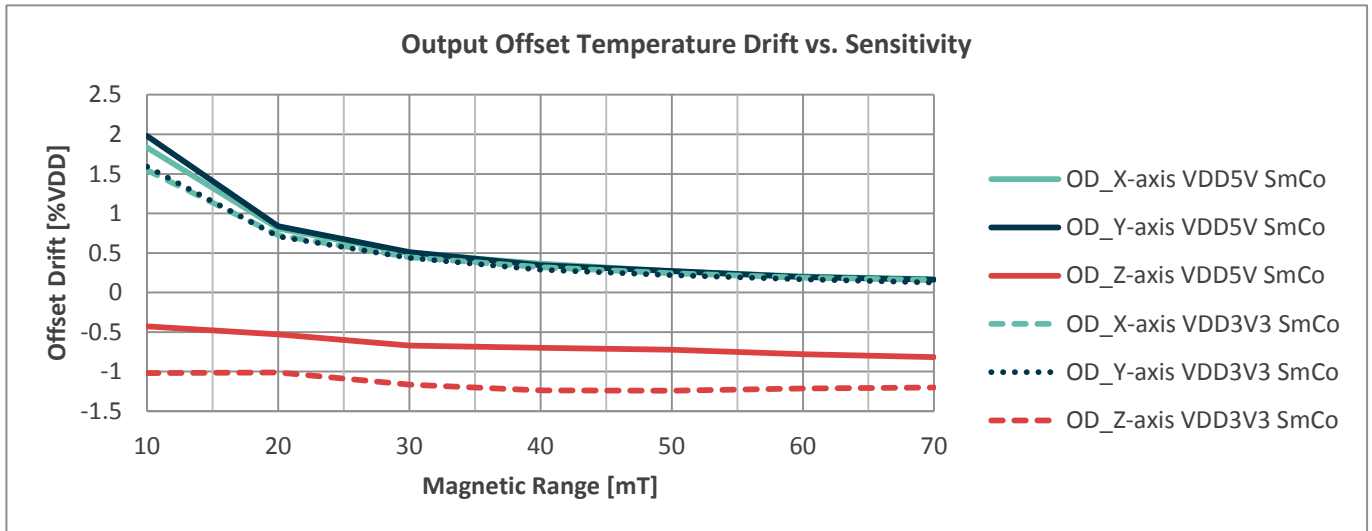


Figure 11: Output Offset Temperature Drift vs Sensitive Range per Magnet Type

## 13. Recommended Application Diagrams

### 13.1. MLX90380 in SOIC-8 Package

#### 13.1.1. Application Diagram VDD3V3

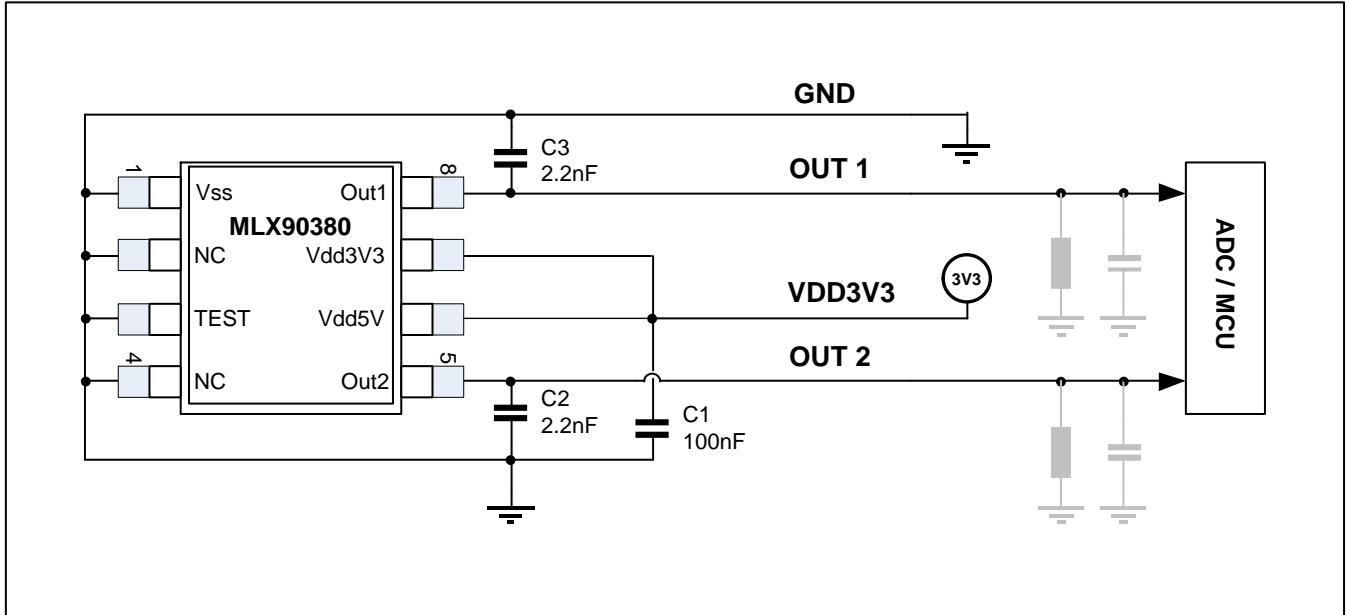


Figure 12: Application Diagram VDD3V3

#### 13.1.2. Application Diagram VDD5V

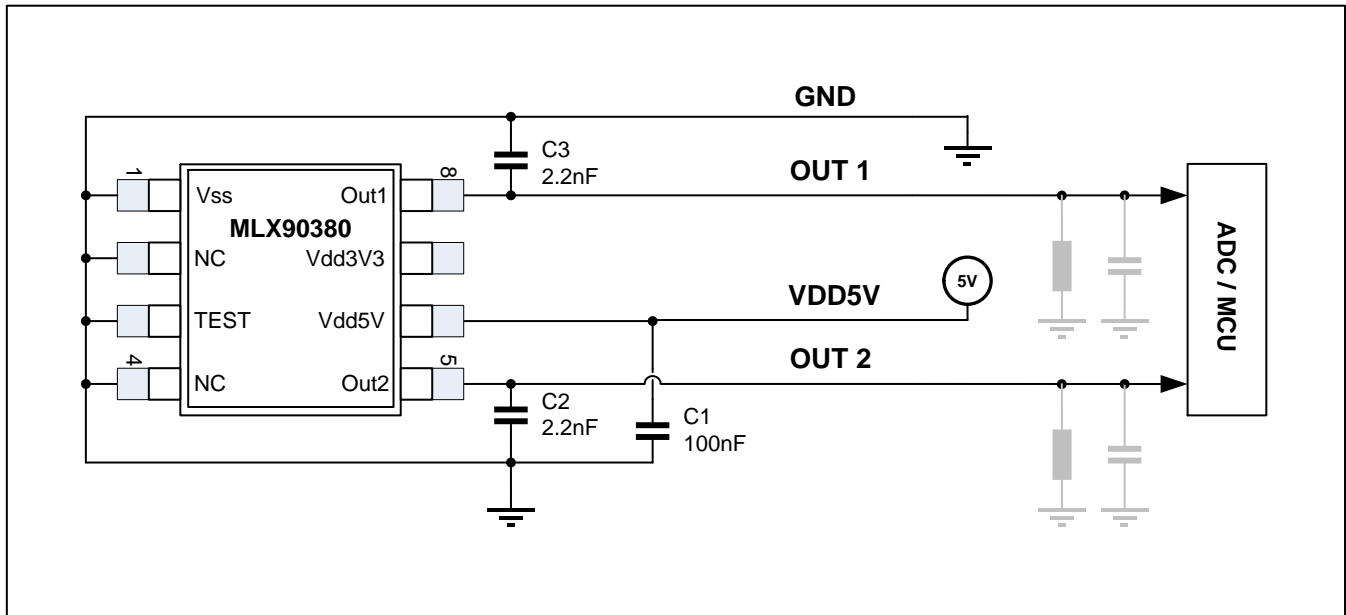


Figure 13: Application Diagram VDD5V

## 13.2. MLX90380 in TSSOP-16 Package

### 13.2.1. Application Diagram VDD3V3

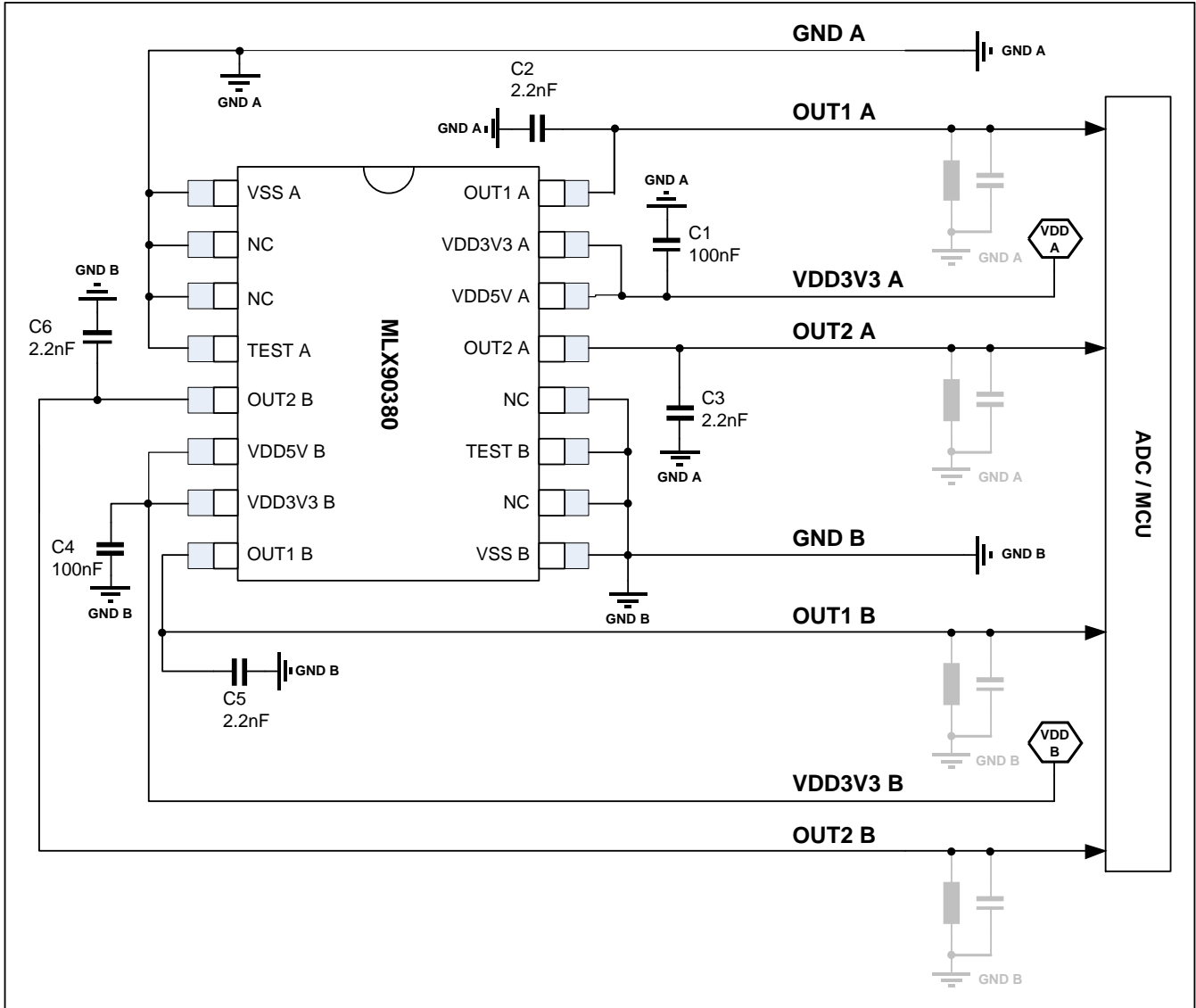


Figure 14: Application Diagram VDD3V3

### 13.2.2. Application Diagram VDD5V



Figure 15: Application Diagram VDD5V

## 14. Package Information

### 14.1. SOIC-8 Package Dimensions



Figure 16: SOIC-8 Package Dimensions

### 14.2. SOIC-8 Pin out and Marking

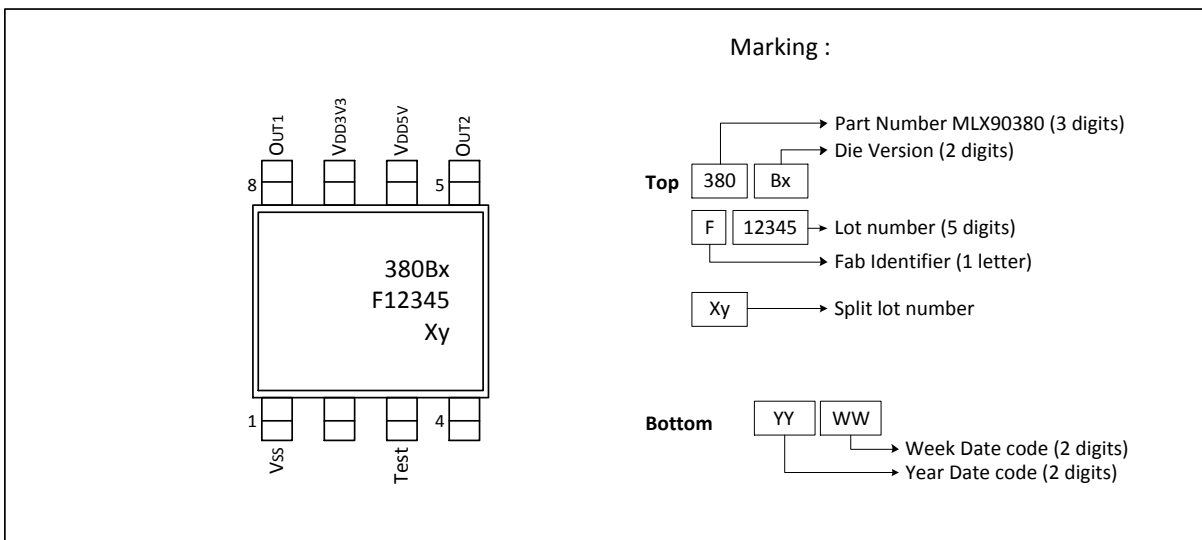


Figure 17: SOIC-8 Pin out and Marking

### 14.3. SOIC-8 IMC Positioning



Figure 18: SOIC-8 IMC positioning



Figure 19: Angular detection MLX90380 SOIC-8

### 14.4. TSSOP-16 Package Dimensions



Figure 20: TSSOP-16 Package Dimensions



### 14.5. TSSOP-16 Pin out and Marking

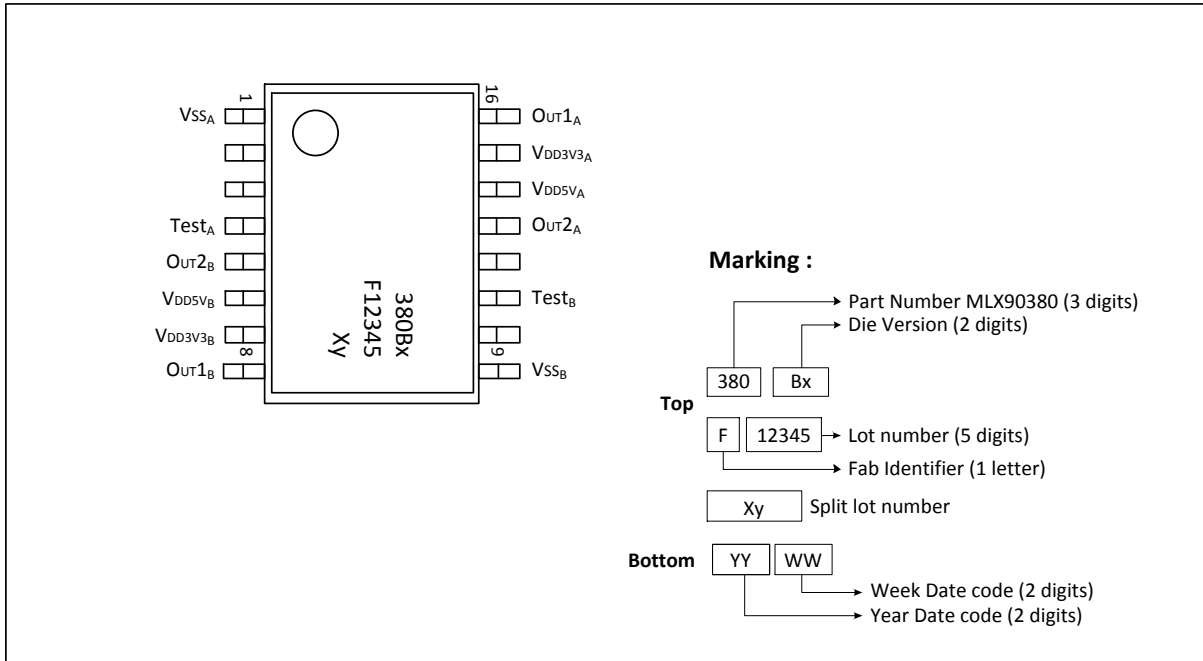


Figure 21: TSSOP-16 Pin out and Marking

### 14.6. TSSOP-16 IMC positioning

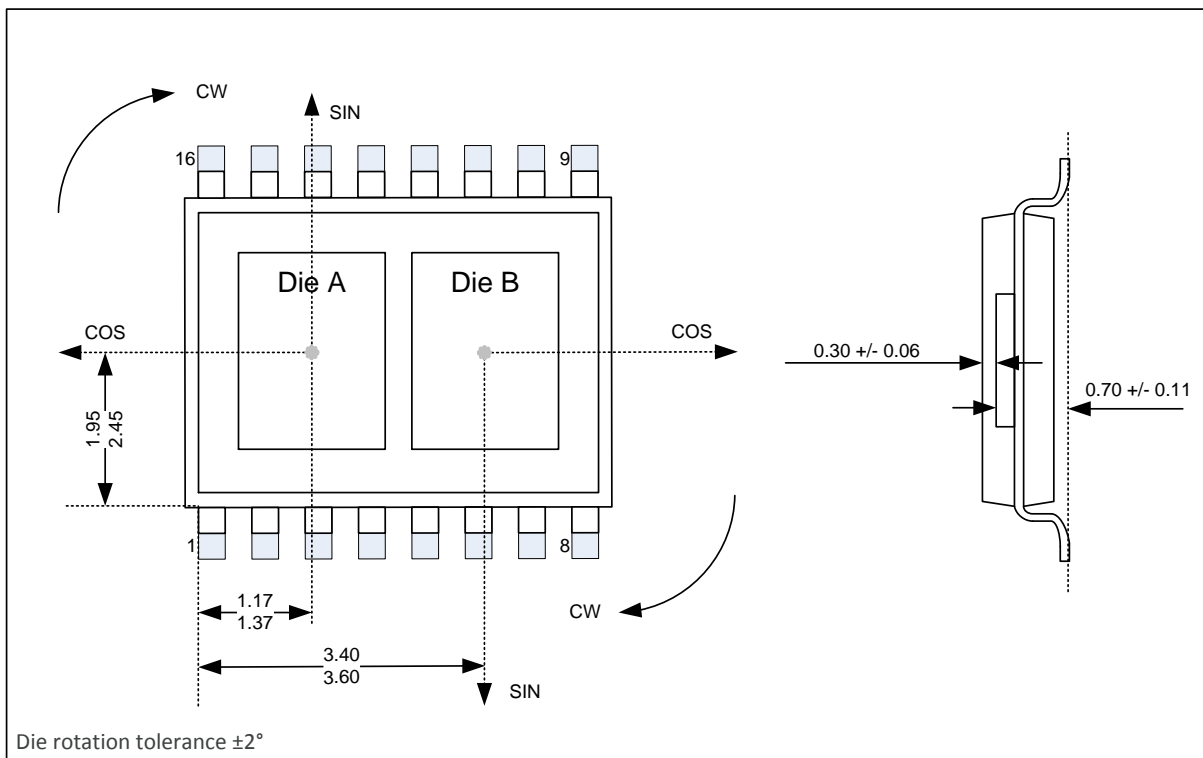


Figure 22: TSSOP-16 IMC positioning

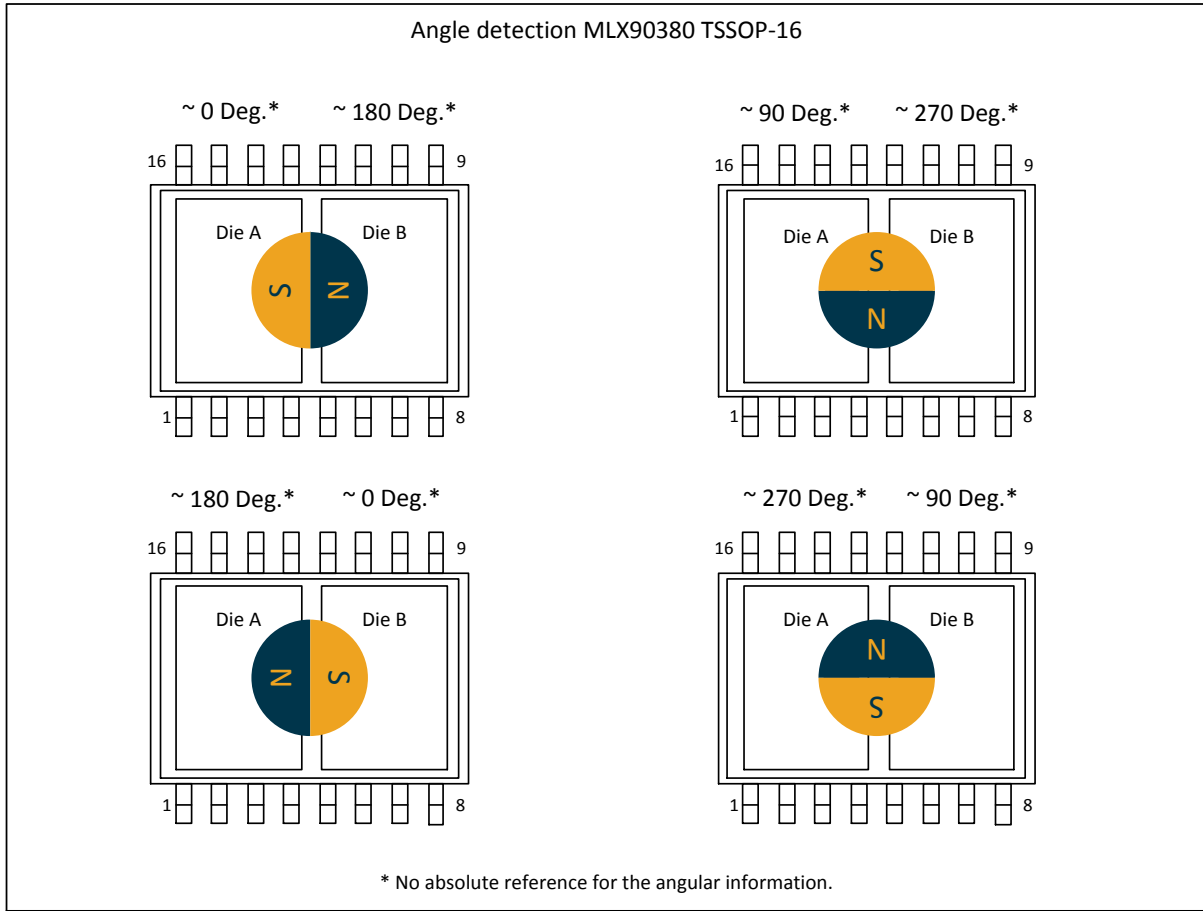


Figure 23: Angular detection MLX90380 TSSOP-16

As illustrated above the magnetic axis of die B in the package are shifted 180° towards the magnetic axis of die A. When a magnetic angle of ~0° is applied to die A, die B will report an angle of ~180°.

## 15. Standard Information

Our products are classified and qualified regarding soldering technology, solder ability and moisture sensitivity level according to standards in place in Semiconductor industry.

For further details about test method references and for compliance verification of selected soldering method for product integration, Melexis recommends reviewing on our web site the General Guidelines [soldering recommendation](#). For all soldering technologies deviating from the one mentioned in above document (regarding peak temperature, temperature gradient, temperature profile etc.), additional classification and qualification tests have to be agreed upon with Melexis.

For package technology embedding trim and form post-delivery capability, Melexis recommends consulting the dedicated trim&form recommendation application note: [lead trimming and forming recommendations](#).

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