

ISL5216EVAL Schematics

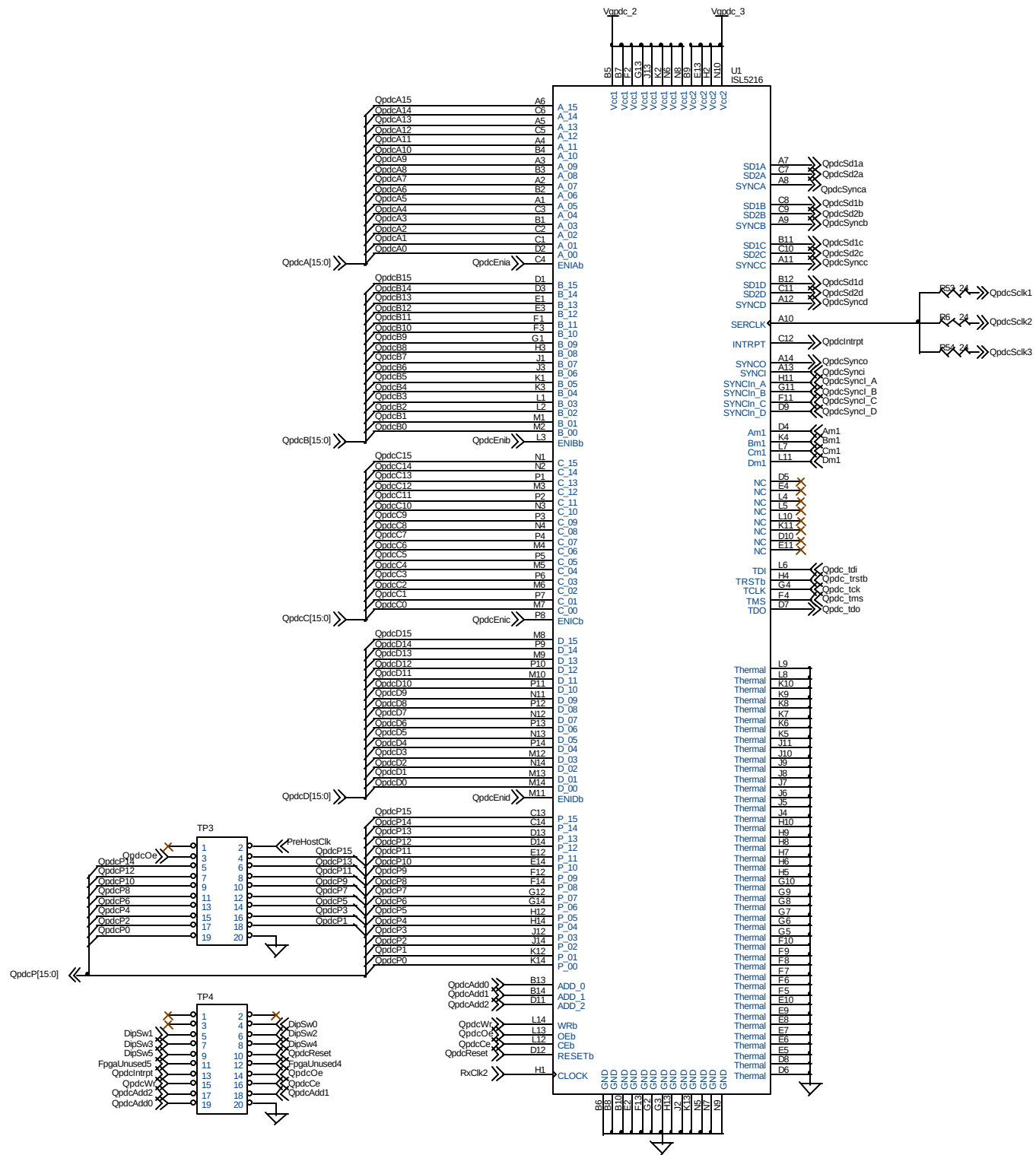
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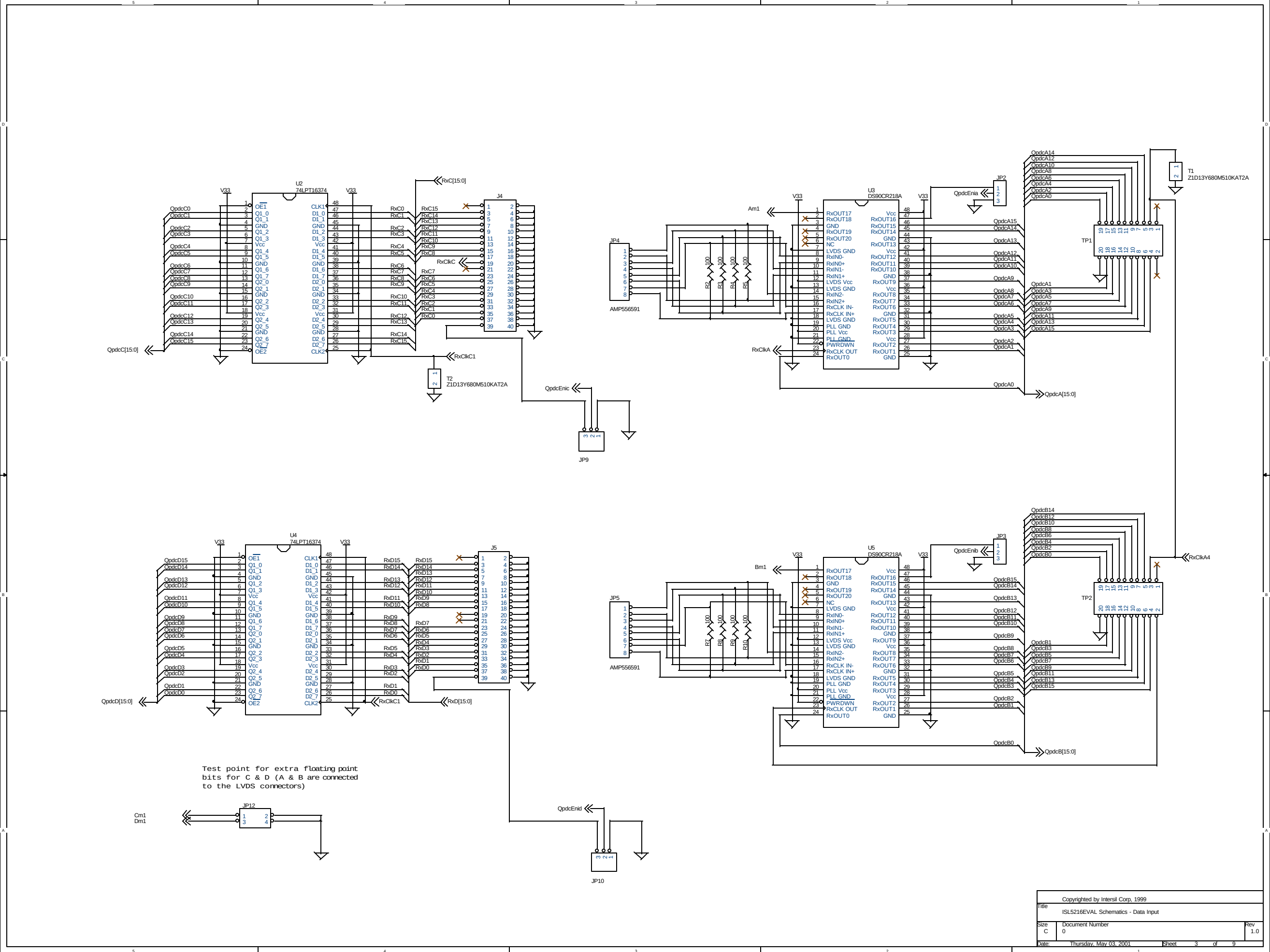
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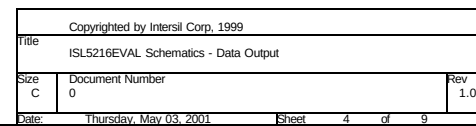
Highest Referenced Designators:

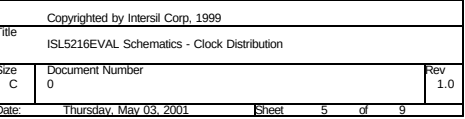
C109
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JP12
L1
R61
TP11
T9
U17
Y1

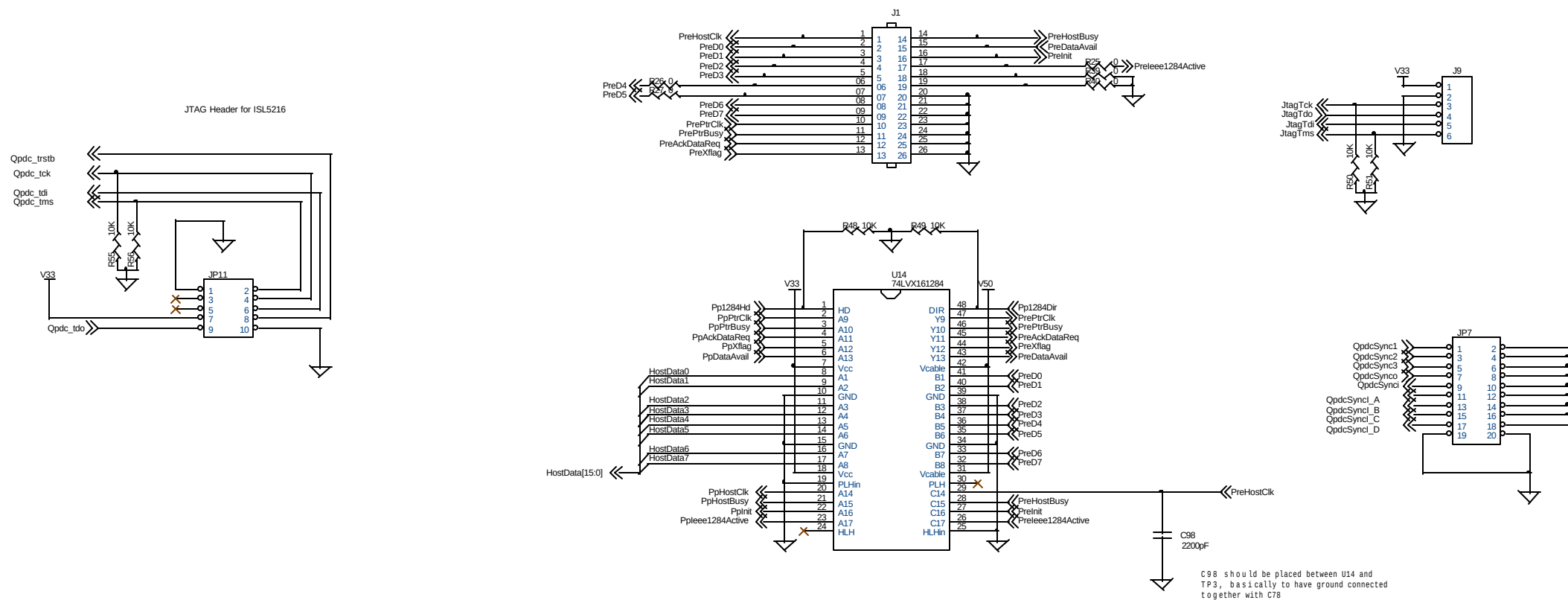
Unused Designators:

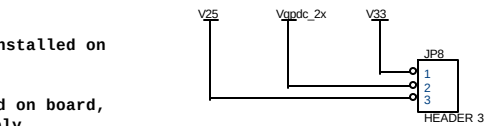




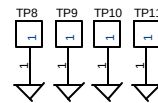
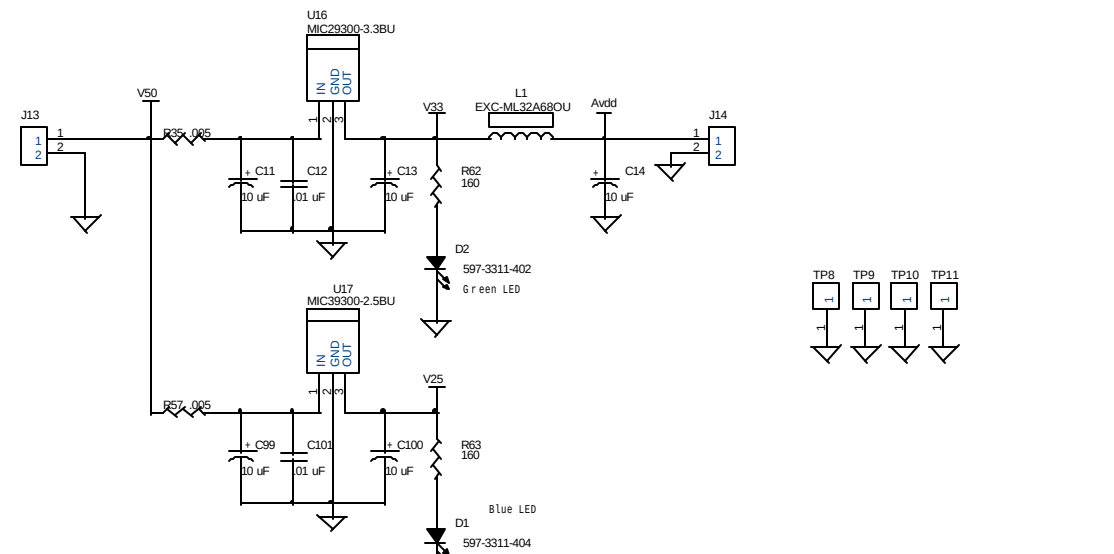








NOTE: This header should NOT be installed on the board. Instead, a zero Ohm resistor, or a piece of wire will go between the supply plane and Vqpd_c_2, depending if ISL5216 or HSP50216 are on board.



The diagram shows two identical decoupling capacitor circuits. Each circuit consists of a capacitor (C34 and C35) connected between a power supply rail (V33) and ground. The capacitors are labeled with a value of .1 uF.

The figure shows three identical circuit diagrams, each representing a different capacitor used to connect the V33 signal to ground. In each diagram, the V33 signal line is connected to a vertical line. This vertical line is connected to a horizontal line that branches into three parallel capacitors. The capacitors are labeled C36, C37, and C38 in the first diagram; C39, C40, and C41 in the second; and C42, C43, and C44 in the third. Each capacitor has a value of 11 uF, .01 uF, and .001 uF respectively. The bottom terminals of all capacitors are connected to a common ground symbol.

V33
C74 .1 uF

V33
C87 .1 uF

Figure 10-10 shows four parallel branches connected to a common V33 supply. Each branch contains a 10 uF capacitor and a diode (C88, C89, C90, C91) connected to ground.