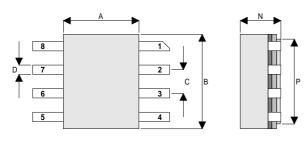


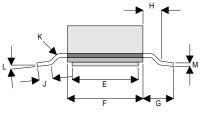
### **D2220UK**

#### ROHS COMPLIANT METAL GATE RF SILICON FET

#### **MECHANICAL DATA**



# **GOLD METALLISED MULTI-PURPOSE SILICON DMOS RF FET** 5W - 12.5V - 1GHz SINGLE ENDED



#### **SO8 PACKAGE**

PIN 1 – SOURCE	PIN 5 - SOURCE
PIN 2 – DRAIN	PIN 6 – GATE
PIN 3 – DRAIN	PIN 7 – GATE
PIN 4 - SOURCE	PIN 8 - SOURCE

Dim.	mm	Tol.	Inches	Tol.	
Α	4.06	±0.08	0.160	±0.003	
В	5.08	±0.08 0.200		±0.003	
С	1.27	±0.08	±0.08 0.050		
D	0.51	±0.08	0.020	±0.003	
E	3.56	±0.08	0.140	±0.003	
F	4.06	±0.08	0.160	±0.003	
G	1.65	±0.08	0.065	±0.003	
Н	0.76	+0.25	0.030	+0.010	
	0.76	-0.00	0.030	-0.000	
J	0.51	Min.	0.020	Min.	
'	1.02	Max.	0.040	Max.	
K	45°	Max.	45°	Max.	
	, 0° Min.		0°	Min.	
-	7°	Max.	7°	Max.	
М	0.20	±0.08	0.008	±0.003	
N	2.18	Max.	0.086	Max.	
Р	4.57	±0.08 0.180		±0.003	

#### **FEATURES**

- SIMPLIFIED AMPLIFIER DESIGN
- SUITABLE FOR BROAD BAND APPLICATIONS
- VERY LOW C<sub>rss</sub>
- SIMPLE BIAS CIRCUITS
- LOW NOISE
- HIGH GAIN 10 dB MINIMUM

#### **APPLICATIONS**

 HF/VHF/UHF COMMUNICATIONS from 1 MHz to 2 GHz

## **ABSOLUTE MAXIMUM RATINGS** (T<sub>case</sub> = 25°C unless otherwise stated)

$\overline{P_D}$	Power Dissipation	17.5W
$BV_{DSS}$	Drain – Source Breakdown Voltage	40V
$BV_GSS$	Gate – Source Breakdown Voltage	±20V
I <sub>D(sat)</sub>	Drain Current	4A
T <sub>stg</sub>	Storage Temperature	−65 to 150°C
T <sub>j</sub>	Maximum Operating Junction Temperature	200°C

Semelab PIc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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# **D2220UK**

Issue 1

### **ELECTRICAL CHARACTERISTICS** (T<sub>case</sub> = 25°C unless otherwise stated)

	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
R\/	Drain-Source	V <sub>GS</sub> = 0	I <sub>D</sub> = 10mA	40			V
BV <sub>DSS</sub>	Breakdown Voltage	VGS - 0	ID - IOIIIV	40			\ \ \
	Zero Gate Voltage	V <sub>DS</sub> = 12.5V V <sub>GS</sub> = 0	V 0			2	mΛ
IDSS	Drain Current				2	mA	
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> = 20V	V <sub>DS</sub> = 0			1	μΑ
V <sub>GS(th)</sub>	Gate Threshold Voltage*	I <sub>D</sub> = 10mA	$V_{DS} = V_{GS}$	0.5		7	V
9 <sub>fs</sub>	Forward Transconductance*	V <sub>DS</sub> = 10V	I <sub>D</sub> = 0.4A	0.36			S
G <sub>PS</sub>	Common Source Power Gain	P <sub>O</sub> = 5W		10			dB
η	Drain Efficiency	V <sub>DS</sub> = 12.5V	$I_{DQ} = 0.2A$	40			%
VSWR	Load Mismatch Tolerance	f = 1GHz		20:1			_
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 0V \qquad V_{GS}$	S = -5V $f = 1MHz$			24	pF
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 12.5V V_{G}$	S = 0 $f = 1MHz$			20	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	$V_{DS} = 12.5V V_{GS}$	S = 0 $f = 1MHz$			2	pF

<sup>\*</sup> Pulse Test: Pulse Duration = 300  $\mu s$  , Duty Cycle  $\leq 2\%$ 

#### THERMAL DATA

R <sub>THj-case</sub>	Thermal Resistance Junction – Case	Max. 6°C / W
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