

Model No: PO⊞ €FZÓÓÓO₽€ËE

Approv	ved By

- Tel: 1 (888) 499-8477
- Fax: (407) 273-0771
- E-mail: mtusainfo@microtipsusa.com
- Web: www.microtipsusa.com



RECORD OF REVISION

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1. GENERAL DESCRIPTION

1.1 Description

HA-101ZIEBCAHO-A is a 10.1 (16:10) inch diagonally measured active display with high resolution WXGA 1280x800 display and high brightness. This model is composed of a TFT LCD panel, backlight system, a projected capacitive touch panel and HDMI included Stereo D/A Converter . It is designed to make Raspberry Pi usage easy. You can simply use this TFT display with your Raspberry Pi, or also you can use this as computer display with any device which has HDMI output. This 10.1" TFT model comes in 1280x800 resolution that would be great for embedded computing usage too.

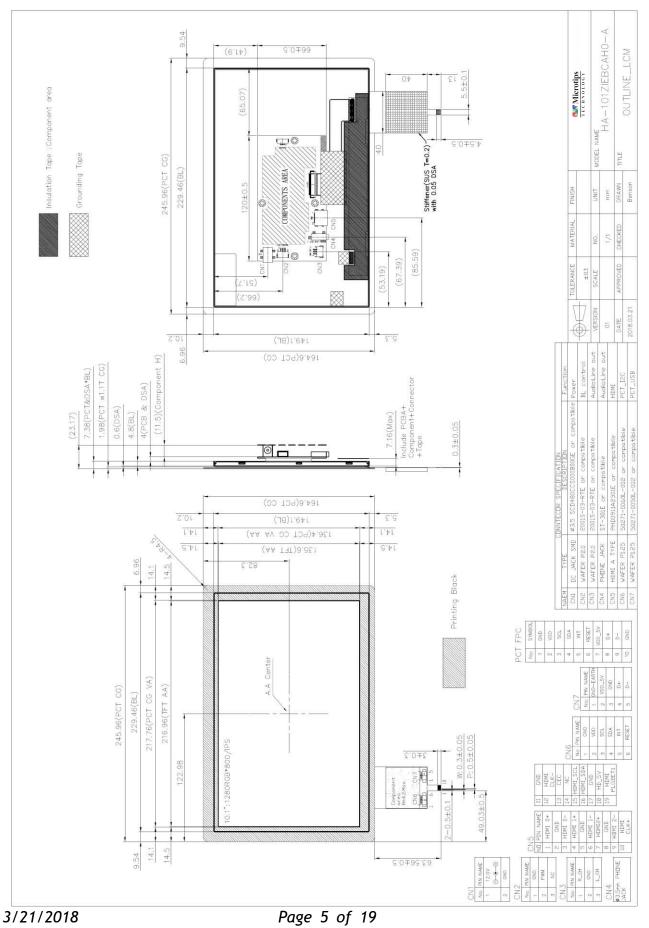
No.	Item	Specification	Unit
1	Panel Size	10.1"	Inch
2	Number of Pixels	1280 (W) x RGB x 800 (H)	Pixels
3	Active Area	216.96 (W) × 135.6 (H)	mm
4	Pixel Pitch	0.1695 (W) x 0.1695 (H)	mm
5	Outline Dimension	245.96 (W) × 164.6 (H) × 23.17 (T)	mm
6	Number of Colors	16.7M	
7	Display Mode	IPS / Normally Black / Transmissive	
8	View Direction	ew Direction Free direction	
9	Display Format	RGB vertical stripe	
10	Surface Treatment	Surface Treatment Clear (7H)	
11	Contrast Ratio	Contrast Ratio 800 (Typ.)	
12	Luminance (cd/m^2)	500 (Typ.)	cd/m2
13	Video Input Interfaco	HDMI	
IJ	Video Input Interface	(Compliance HDMI V1.4 and include HDCP decryption)	
14	Audio Output Interface	Analog Output	
15	Backlight	White LED	
16	Operation Temperature	-20 ~ 70	°C
17	Storage Temperature	ure -30 ~ 80	
18	Weight	(TBD)	g

1.2 Features:

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2. MECHANICAL SPECIFICATION



MICROTIPS TECHNOLOGY USA 3504 Lake Lynda Dr, Suite 110, Orlando, Florida, 32817, TEL: 407-273-0204, FAX: 407-273-0771

3. PIN DESCRIPTION

3.1 Power Input(CN1)

[DC JACK:SCD480CCS000B00GE or compatible]

Pin No.	Symbol	1/0	Function	Note
1	12V	Ρ	Power Supply +12V	12.0V
2	GND	Ρ	Ground	

3.2 Back-light Control(CN2)

[WAFER P2.0mm:2001S-03-RTE or compatible]

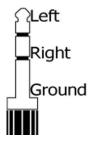
Pin No.	Symbol	I/O	Function	Note
1	GND	Р	Ground	
2	PWM	I	Back-light Dimming control (internal pull up to 3.3V)	*1
3	LED_EN		No connection. (internal control)	

*1: When PWM, LED_EN not connected, back-light defult is typical brightness.

3.3 Audio line out(CN3) [WAFER P2.0mm:2001S-03-RTE or compatible]

Pin No.	Symbol	1/0	Function	Note
1	R_CH	Α	HDMI Audio:Right Channel Analog Output	
2	GND	Р	Ground	
3	L_CH	Α	HDMI Audio:Left Channel Analog Output	

3.4 Standard 3.5mm Phone Jack (CN4) [PHONE JACK:ST-301E or compatible] HDMI Audio Analog Output



3.5 HDMI (CN5)

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[HDMI A TYPE:PHD0911A2301E or compatible]

Pin No.	Symbol	1/0	Function	Note		
1	TMDS 2+		MDS Data2+			
2	GND	Р	TMDS Data2 Shield			
3	TMDS 2-	I	TMDS Data2-			
4	TMDS 1+	I	TMDS Data1+			
5	GND	Р	TMDS Data1 Shield			
6	TMDS 1-	I	TMDS Data1-			
7	TMDS 0+		TMDS Data0+			
8	GND	Р	TMDS Data0 Shield			
9	TMDS 0-		TMDS Data0-			
10	TMDS CLK+	I	TMDS Clock+			
11	GND	Р	TMDS Clock Shield			
12	TMDS CLK-	I	TMDS Clock-			
13	CEC	I	CEC			
14	N.C.	-	N.C.			
15	DDC_SCL		IIC SCL to EDID ROM			
16	DDC_SDA	1/0	IIC SDA to EDID ROM			
17	GND	Р	DDC/CEC Ground			
18	HD_5V	Р	+5V Power			
19	HPD	0	Hot Plug Detect			

3.6 PCT Control:IIC (CN6) [WAFER P1.25mm:50271-0060L-002 or compatible]

Pin No.	Symbol	I/0	Function	Note
1	GND	Р	Ground	
2	VDD	Р	Power supply for I2C	
3	SCL	I	IIC SCL to PCT Controller	
4	SDA	1/0	IIC SDA to PCT Controller	
5	INT	0	Interrupt	
6	RESET		Reset	

3.7 PCT Control:USB (CN7) [WAFER P1.25mm:50271-0050L-002 or compatible]

Pin No.	Symbol	I/0	Function	Note
1	GND -EARTH	Ρ	Earth Ground(Shield)	
2	VDD_5V	Р	Power supply for USB I/F	
3	GND	Р	Power Ground	
4	D+	1/0	USB data +	
5	D-	1/0	USB data -	

3.8 PCT Control:IIC and USB (FPC)

Pin No.	Symbol	1/0	Function	Note
1	GND	Р	Ground	
2	VDD	Р	Power supply for I2C	
3	SCL		IIC SCL to PCT Controller	
4	SDA	1/0	IIC SDA to PCT Controller	
5	INT	0	Interrupt signal to inform the host processor that touch data is ready for read	
6	RESET	I	External low signal reset the chip.	
7	VDD_5V	Р	Power supply for USB I/F	
8	D+	1/0	USB data +	
9	D-	1/0	USB data -	
10	GND	Р	Ground	

4. ABSOLUTE MAXIMUM RATINGS

4.1 Electrical Absolute Rating

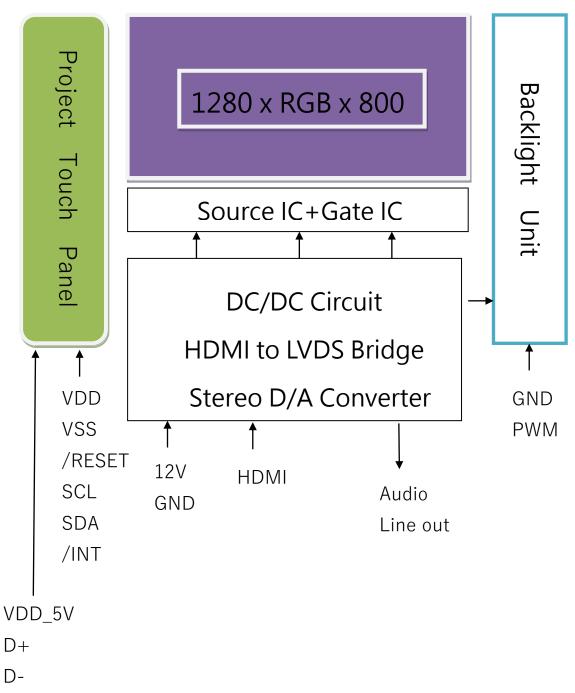
4.1.1 HDMI TFT LCD Module

ltom	Symbol	Val	ues	Unit	Note
ltem	Symbol	Min	Max.		
Power supply voltage	12V	TBD	14	V	

4.1.2 Environment Absolute Rating

ltom	Symbol	Values			Unit	Note	
ltem	Symbol	Min	Тур	Max.	Unit	NOLE	
Operating Temperature	Тор	-20		70	°C	Ambient	
Storage Temperature	Tst	-30		80	°C	temperature	

- 5. BLOCK DIAGRAM
 - 5.1 TFT LCD Module



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6. ELECTRICAL CHARACTERISTICS

6.1 HDMI TFT LCD Module

ltem	Symbol		Values	Unit	Note		
item	Symbol	Min	Тур.	Max.	Unit	Note	
Supply Voltage	12V	TBD	12	13	V		
PWM frequency		100	-	10K	Hz		
PWM Duty		17	-	100	%	<17%=0FF	
PWM Dimming	Vpwm-ih	3.3	-	8	V		
Voltage	VPWM-IL	-	0.3	-	۷		
LED Enable Control	VLED_EN-IH	3.3	-	12	V		
Voltage	VLED_EN-IL	-	-	0.5	۷		
Supply Current	ICC(12V)	TBD	TBD	TBD	mA		
LED life time		70000	-	-	Hr	(1)	

Note 1:

The "LED life time" is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25° C 60% RH.

7. POROJECTED CAPACITIVE TOUCH PANEL SPECIFICATION

7.1 Main Feature

Item	Specification	Unit
Screen Size	10.1 inches	Diagonal
Туре	Transparent Type Projected Capacitive Touch Panel	
Input Mode	Human's Finger	
Interface	I2C or USB	
Touch number	5 points	
Cover glass pencil-hardness	7H	
Response time	\$ 25ms	ms
Controller IC	ILI2511	

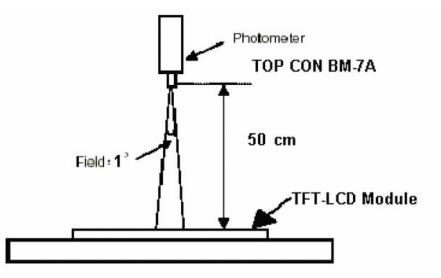
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8. OPTICAL CHARACTERISTICS

ltem		Symbol	Condition	Min.	Тур.	Max.	Unit
Brightness Uniformity				400	500		cd/m2
		B-uni	Note1,	75	80	-	%
Contrast Ratio		CR	Note 3,	600	800		
Response Time		Tr	$(\theta = 0^{\circ},$ Normal		10	20	ms
		Tf	Viewing		15	30	ms
Color	White	Wx	Angle)	0.260	0.310	0.360	
Chromaticity		Wy		0.280	0.330	0.380	
	Horizontal	heta x+	Center CR≥10	75	85		
View angle		θ x-		75	85		
	Vertical	θ Y +		75	85		
		θ Υ-		75	85		

Note : The following optical specifications shall be measured in a darkroom or equivalent state(ambient luminance ≤ 1 lux, and at room temperature). The operation temperature is 25°C±2°C. The measurement method is shown in Note1.

Note1: The method of optical measurement:

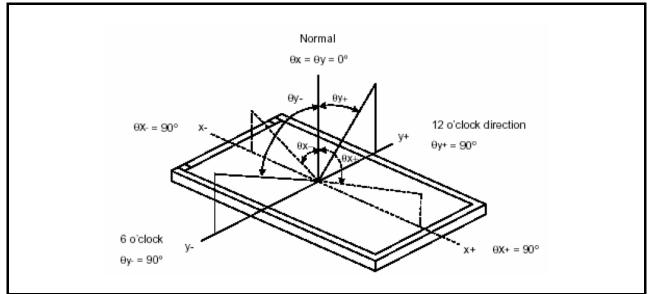


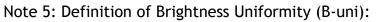
Note2: Measured at the center area of the panel and at the viewing angle of the $\theta x = \theta y$ =0°

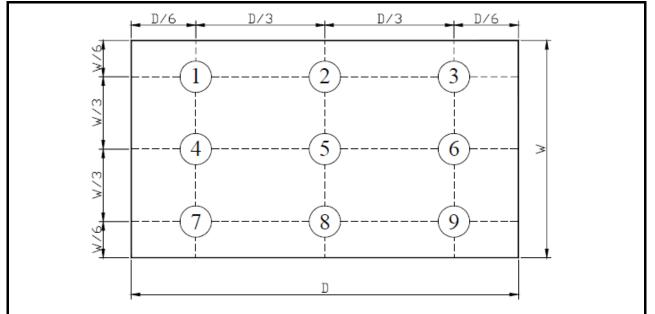
Note3: Definition of Contrast Ratio (CR):

CR = Luminance with all pixels in white state \div Luminance with all pixels in Black state

Note 4: Definition of Viewing Angle:





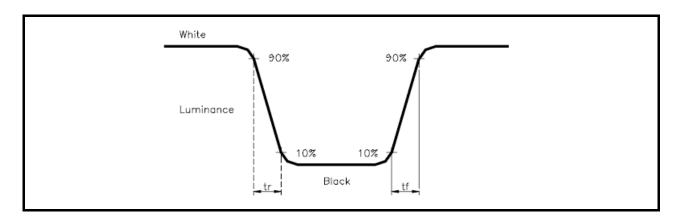


B-uni = (Minimum luminance of 9 points \div Maximum luminance of 9 points)X100%



Note 6: Definition of Response Time:

The Response Time is set initially by defining the "Rising Time (Tr)" and the "Falling Time (Tf)" respectively. Tr and Tf are defined as following figure



Note 7: Definition of Chromaticity:

The color coordinates (Wx,Wy),(Rx,Ry),(Gx,Gy),and (Bx,By) are obtained with all pixels in the viewing field at white, red, green, and blue states, respectively.

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9. RELIABILITY

9.1 Test Condition

9.1.1 Temperature and Humidity(Ambient Temperature) Temperature : $25 \pm 5^{\circ}$ C Humidity : $65 \pm 5^{\circ}$

9.1.2 OperationUnless specified otherwise, test will be conducted under function state.

- 9.1.3 ContainerUnless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.
- 9.1.4 Test Frequency In case of related to deterioration such as shock test. It will be conducted only once.

9.2 TESTS

No.	ITEM	CONDITION CRITERION			
1	High Temperature Storage	80°C, 120 hrs			
2	Low Temperature Storage	-30°C, 120 hrs			
3	High Temperature Operating	70°C, 120 hrs			
4	Low Temperature Operating	-20°C, 120 hrs			
5	High Temperature/Humidity	40°C, 90%RH, 120 hrs			
	Non-Operating				
6	Temperature Shock Non-Operating	$-30^{\circ}C \longleftrightarrow 80^{\circ}C$			
0	Temperature shock non operating	(0.5hr each), 100 cycles			
		Frequency:0 ~ 55 Hz Amplitude:1.5 mm			
7	Vibration Test Non-Operating	Sweep Time:11min			
/		Test Period:6 Cycles for each Direction of			
		X,Y,Z			
8	Electro-static Discharge	\pm 2KV, Human Body Mode, 100pF/1500 Ω			

Note1: The test sample have recovery time for 24 hours at room temperature before the function check. In the standard conditions, there is no any touch panel function NG issue occurred.



9.3 JUDGMENT STANDARD

The judgment of the above test should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect. Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.

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9.4 INCOMING INSPECTION STANDARDS

No.	Parameter	Criteria						
110.	l'ulumotor	Display function: No	Displa			ior)		
		Display function: No Display malfunction (Major) Contrast ratio (Black, White):						
		Does not meet spec			ne spec. (I	Major) (N	lote:3)	
		Line Defect: No obvious Vertical and Horizontal line defect in bright						right.
		dark and colored. (Major) (Note:1)						.g,
		Point Defect : Active area ≤ 5 dots (Minor) (Note:1)						
				eptable r				
		ltem		Active A		Total		
			_		lea		_	
		Bright		2		5		
		Dark		4		5		
					I			
1	Operating							
	Operating	Non-uniformity: \/ioit	la the			(Miner)		
		Non-uniformity: Visik					(41.)	
		Foreign material in		or white			/+L)	
		Zon	Acc	eptable	Class	S	AQL	
				umber	Of		Level	
		Dimension			Defec	ts		
		D> 0.5		0				
		0.3 < D ≤ 0.5	5	5	Mino	r	1.5	
		D ≤ 0.3		*				
		D = (Long + S	short)	/2 *:	Disregard	· · · ·		
		Foreign Material in			-		ote: 4)	
			Zone			Class	l í	
				AC	ceptable	Of	AQL	
		L (mm) W(n	nm)	<u> </u>	number	Defects	Level	
			W>0.	1	0			
			3 < W		5	Minor	1.5	
			N≤0.0		*			
			: Widt)isregard			
		Dimension: Outline			Jareyaru			
		Bezel appearance:			.)			
		Scratch on the pola			/			
				Accepta	Clas	s	AQL	
		$ \rangle \rangle$		ble	Of Def		Level	
		L (mm) W(m	m	number			2010	
		= ()	·0.1		Mino		1.5	-
							1.5	
		L ≤ 3 W≤	0.1	3				
	_		140.0					
	External Inspection			th ∗:Di				
2	(non-operating)	Dent or bubble on th	e pola	arize (Not		1		
		Zone	Acc	ceptable	Class	AQL		
				umber	Of	Level		
		Dimension			Defects			
		D≤0.3	_	*	Minor	1.5		
		D≤0.5		3				
		D = (Long + Short) / 2 * : Disregard						

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 Class of defects
 Major
 AQL 0.65%
 Definition

 It is a defect that is likely to result in failure or to reduce materially the usability of the product for the intended function.
 It is a defect that will not result in functioning problem with deviation classified.

Note1:

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(a)Bright point defect is defined as point defect of R,G,B with area >1/2 pixel respectively (b)Dark point defect is defined as visible in full white pattern.

(c)Definition of distribution of point defect is as follows:

-minimum separation between dark point defects should be larger than 5mm.

-minimum separation between bright point defects should be larger than 5mm.

(d)Definition of joined bright point defect and joined dark point defect are as follows:

-Two or more joined bright point defects must be nil.

-Three joined dark point defects must be nil.

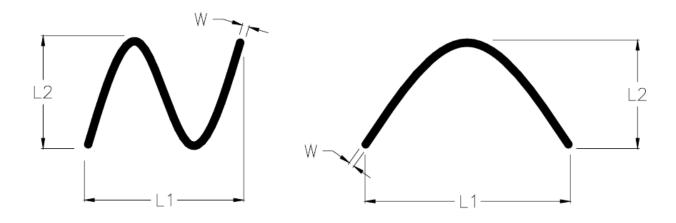
-Coupling of one dark and one bright point in junction is counted as one dark and bright spot with 1 pair maximum.

-Two Joined dark point is counted as two dark points with 2 pair maximum.

Note2: The external inspection should be conducted at the distance $30\pm$ 5cm between the eyes of inspector and the panel.

Note3: Luminance measurement for contrast ratio is at the distance $50\pm$ 5cm between the detective head and the panel with ambient luminance less than 1 lux. Contrast ratio is obtained at optimum view angle.

Note4: W-Width in mm , L-length of Max.(L1,L2) in mm.





9.5 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer.

Lot size: Quantity of shipment lot per model.

Sampling type: normal inspection, single sampling

Sampling table: MIL-STD-105E

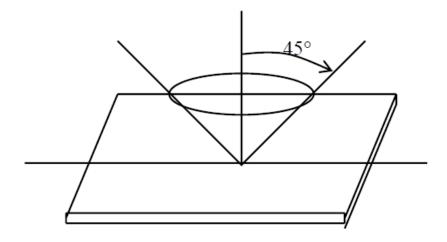
Inspection level: Level II

9.6 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light.

 $\theta \leq 45^{\circ}$ inspection under non-operating condition.

 $\theta \leq \mathbf{S}^{\circ}$ inspection under operating condition



10. PRECAUTION RELATING PRODUCT HANDLING

- 10.1 SAFETY
- 10.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 10.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.
- 10.2 HANDLING
- 10.2.1 Avoid any strong mechanical shock which can break the glass.
- 10.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 10.2.3 Do not remove the panel or frame from the module.
- 10.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, Do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 10.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 10.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 10.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 10.2.8 To control temperature and time of soldering is $280 \pm 10^{\circ}$ C and 3-5 sec.
- 10.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 10.3 STORAGE
- 10.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 10.3.2 Do not place the module near organics solvents or corrosive gases.
- 10.3.3 Do not crush, shake, or jolt the module.