

Diode

Silicon Carbide Schottky Diode

IDH02G120C5

5th Generation CoolSiC™ 1200 V SiC Schottky Diode

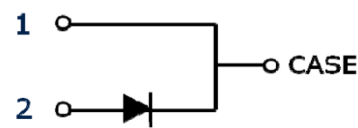
Final Datasheet

Rev. 2.1 2017-07-21

CoolSiC™ SiC Schottky Diode

Features:

- Revolutionary semiconductor material - Silicon Carbide
- No reverse recovery current / No forward recovery
- Temperature independent switching behavior
- Low forward voltage even at high operating temperature
- Tight forward voltage distribution
- Excellent thermal performance
- Extended surge current capability
- Specified dv/dt ruggedness
- Qualified according to JEDEC¹⁾ for target applications
- Pb-free lead plating; RoHS compliant



Benefits

- System efficiency improvement over Si diodes
- Enabling higher frequency / increased power density solutions
- System size / cost savings due to reduced heatsink requirements and smaller magnetics
- Reduced EMI
- Highest efficiency across the entire load range
- Robust diode operation during surge events
- High reliability
- RelatedLinks: www.infineon.com/sic



Applications

- Solar inverters
- Uninterruptable power supplies
- Motor drives
- Power Factor Correction

Package pin definitions

- Pin 1 and backside – cathode
- Pin 2 – anode



Key Performance and Package Parameters

Type	V _{DC}	I _F	Q _C	T _{j,max}	Marking	Package
IDH02G120C5	1200V	2A	14nC	175°C	D0212C5	PG-TO220-2-1

1) J-STD20 and JESD22

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Maximum ratings

Parameter	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	1200	V
Continues forward current for $R_{th(j-c,max)}$ $T_C = 168^{\circ}C$, $D=1$ $T_C = 135^{\circ}C$, $D=1$ $T_C = 25^{\circ}C$, $D=1$	I_F	2 5.7 11.8	A
Surge non-repetitive forward current, sine halfwave $T_C=25^{\circ}C$, $t_p=10ms$ $T_C=150^{\circ}C$, $t_p=10ms$	$I_{F,SM}$	37 31	A
Non-repetitive peak forward current $T_C = 25^{\circ}C$, $t_p=10 \mu s$	$I_{F,max}$	344	A
i^2t value $T_C = 25^{\circ}C$, $t_p=10 ms$ $T_C = 150^{\circ}C$, $t_p=10 ms$	$\int i^2 dt$	7 4.9	A ² s
Diode dv/dt ruggedness $V_R=0...960V$	dv/dt	80	V/ns
Power dissipation $T_C = 25^{\circ}C$	P_{tot}	75	W
Operating and storage temperature	$T_J; T_{stg}$	-55...175	$^{\circ}C$
Soldering temperature, wavesoldering only allowed at leads, 1.6mm (0.063 in.) from case for 10 s	T_{sld}	260	$^{\circ}C$
Mounting torque M3 and M4 screws	M	0.7	Nm

Thermal Resistances

Thermal Resistances						
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Characteristic						
Diode thermal resistance, junction – case	R _{th(j-c)}		-	1.54	2	K/W
Thermal resistance, junction – ambient	R _{th(j-a)}	leaded	-	-	62	K/W

Electrical Characteristics

Static Characteristics, at T_j=25°C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
DC blocking voltage	V_{DC}	$T_j = 25^{\circ}C$	1200	-	-	V
Diode forward voltage	V_F	$I_F = 2A, T_j = 25^{\circ}C$	-	1.4	1.65	V
		$I_F = 2A, T_j = 150^{\circ}C$	-	1.7	2.3	
Reverse current	I_R	$V_R = 1200V, T_j = 25^{\circ}C$		1.2	18	μA
		$V_R = 1200V, T_j = 150^{\circ}C$		6	90	

Dynamic Characteristics, at T_j=25°C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Dynamic Characteristics						
Total capacitive charge	Q _C	V _R =800V, T _j =150°C $Q_C = \int_0^{V_R} C(V)dV$	-	14	-	nC
Total Capacitance	C	V _R =1 V, f=1 MHz	-	182	-	pF
		V _R =400 V, f=1 MHz	-	13	-	
		V _R =800 V, f=1 MHz	-	10	-	

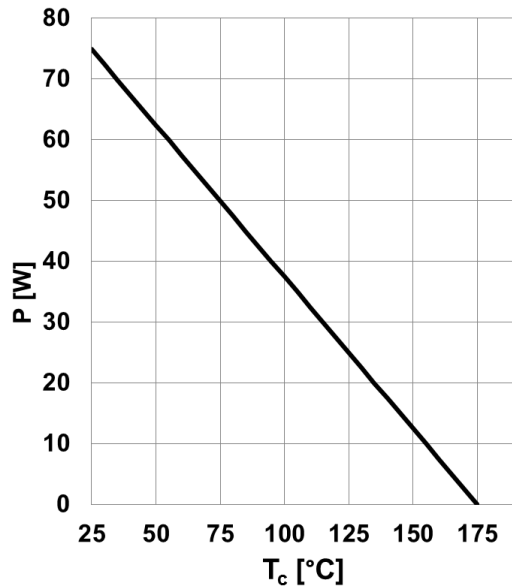


Figure 1. **Power dissipation as a function of case temperature, $P_{tot}=f(T_c)$, $R_{th(j-c),max}$**

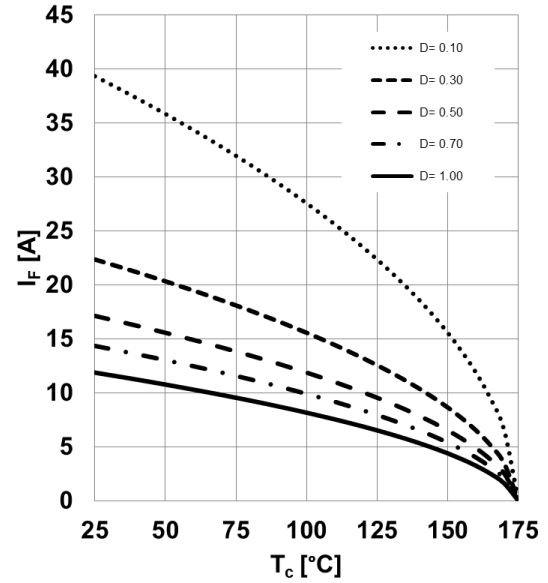


Figure 2. **Diode forward current as function of temperature, $T_j \leq 175^\circ\text{C}$, $R_{th(j-c),max}$, parameter D =duty cycle, V_{th} , R_{diff} @ $T_j=175^\circ\text{C}$**

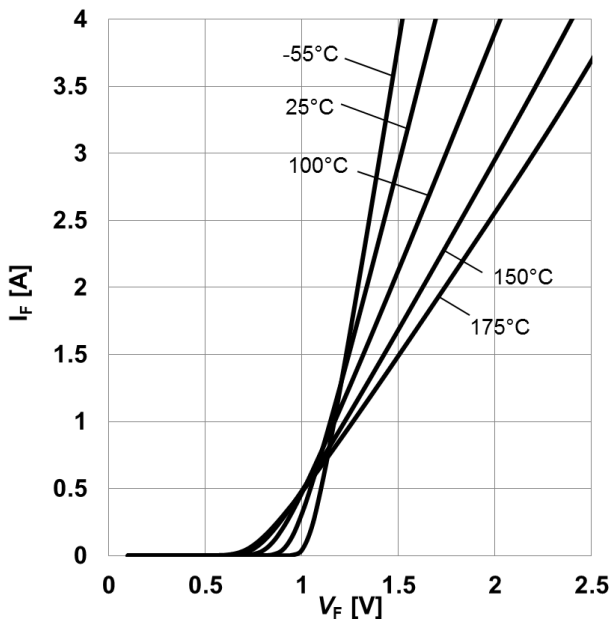


Figure 3. **Typical forward characteristics, $I_F=f(V_F)$, $t_p=10\text{ }\mu\text{s}$, parameter: T_j**

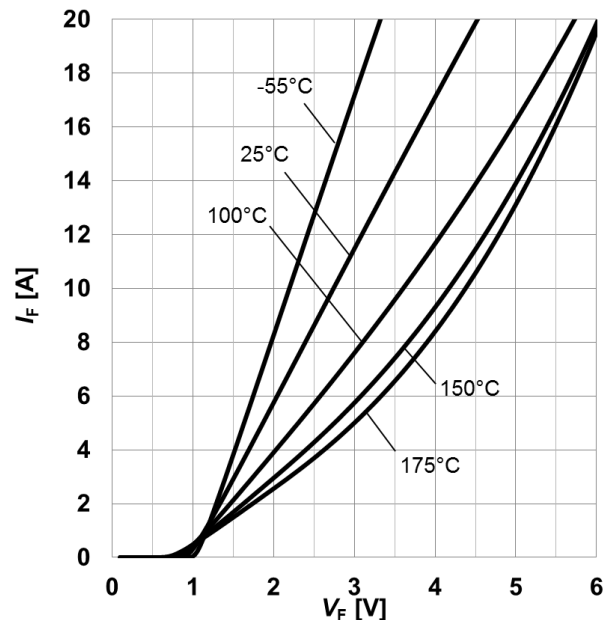


Figure 4. **Typical forward characteristics in surge current, $I_F=f(V_F)$, $t_p=10\text{ }\mu\text{s}$, parameter: T_j**

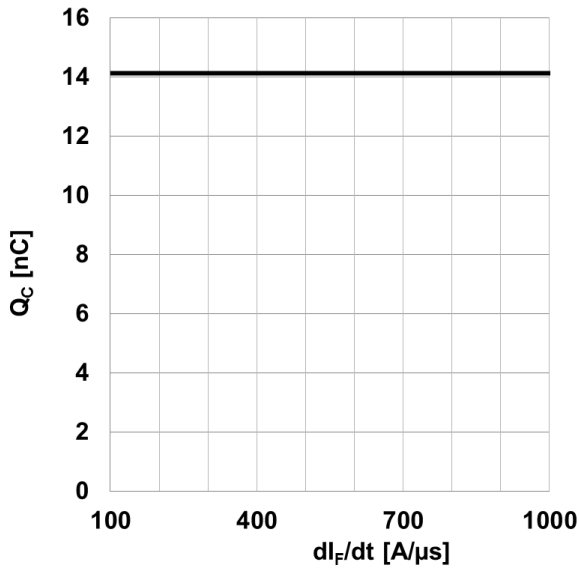


Figure 5. **Typical capacitive charge as function of current slope¹**, $Q_C=f(di_F/dt)$, $T_J=150^\circ\text{C}$
1) Only capacitive charge, guaranteed by design.

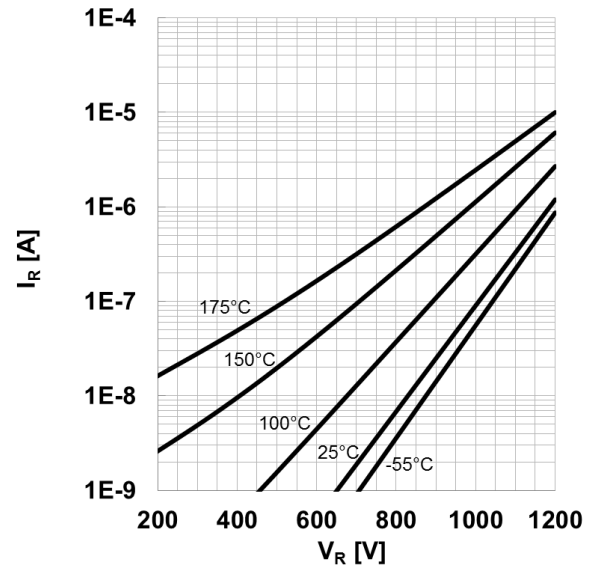


Figure 6. **Typical reverse current as function of reverse voltage**, $I_R=f(V_R)$, parameter: T_J

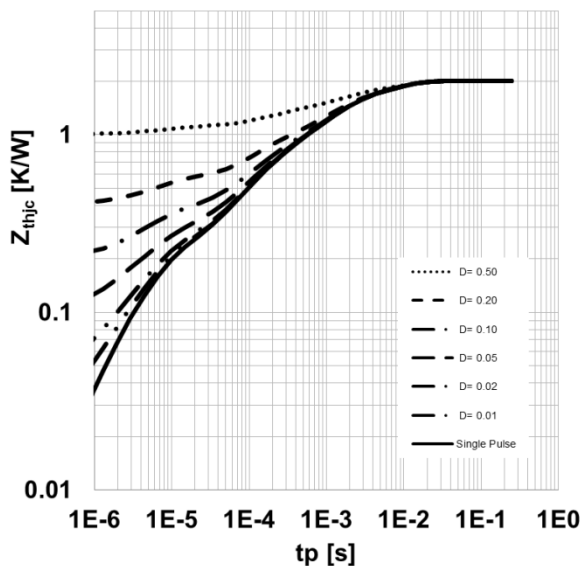


Figure 7. **Max. transient thermal impedance**, $Z_{th,jc}=f(t_p)$, parameter: $D=t_p/T$

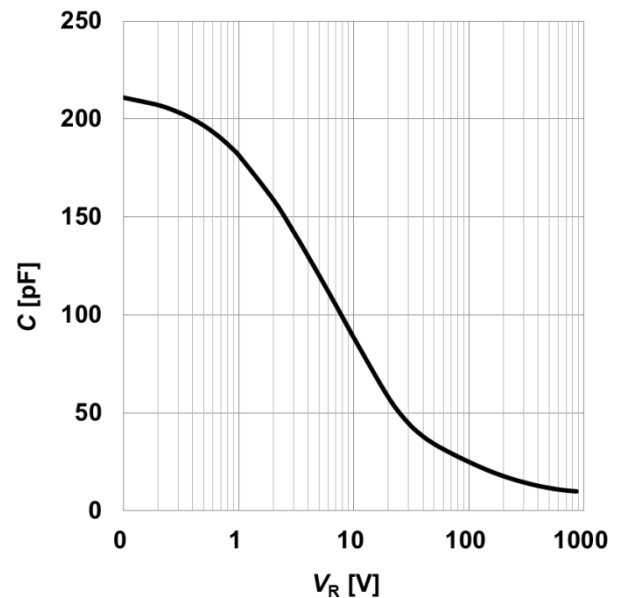


Figure 8. **Typical capacitance as function of reverse voltage**, $C=f(V_R)$; $T_J=25^\circ\text{C}$; $f=1\text{ MHz}$

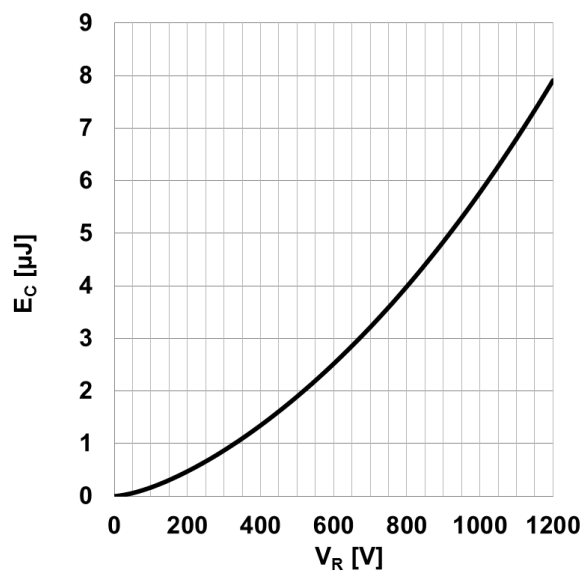
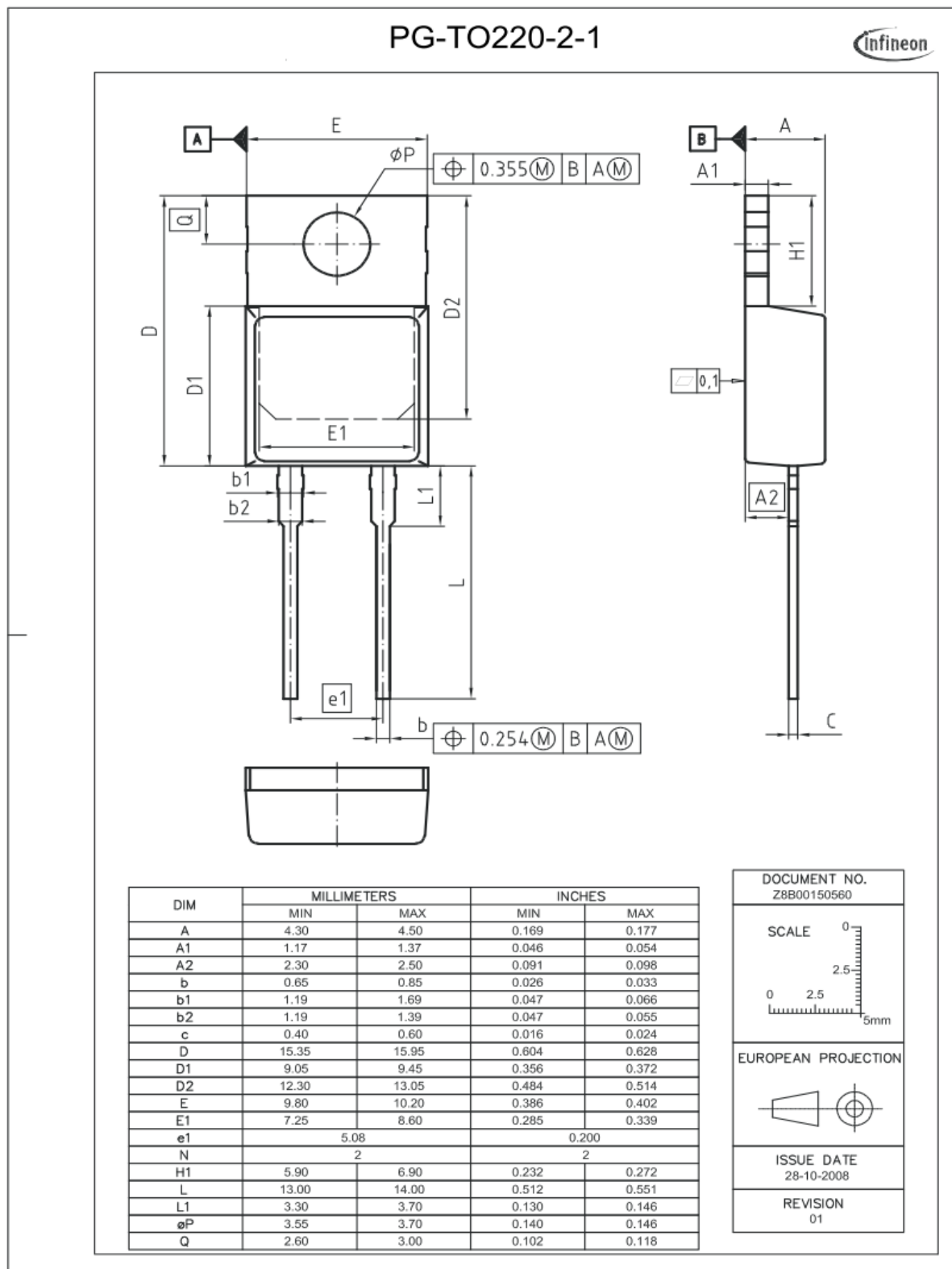


Figure 9. **Typical capacitively stored energy as function of reverse voltage,**

$$E_C = \int_0^{V_R} C(V) V dV$$



Revision HistoryIDH02G120C5

Revision: 2017-07-12, Rev. 2.1

Previous Revision:

Revision	Date	Subjects (major changes since last version)
2.0	2015-07-22	Final data sheet
2.1	-	Editorial change

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