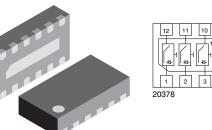
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**Vishay Semiconductors** 

# 6-Channel EMI-Filter with ESD-Protection



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#### **FEATURES**

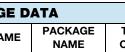
- Ultra compact LLP2513-13L package
- Low package profile of 0.6 mm
- 6-channel EMI-filter
- · Low leakage current
- Line resistance  $R_S = 100 \Omega$
- Typical cut off frequency f<sub>3dB</sub> = 100 MHz
- ESD-protection acc. IEC 61000-4-2 ± 30 kV contact discharge
  - ± 30 kV air discharge
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

ORDERING INFORMATI	ORDERING INFORMATION				
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
VEMI65AA-HCI	VEMI65AA-HCI-GS08	3000	15 000		

PACKAGE DATA								
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS		
VEMI65AA-HCI	LLP2513-13L	9P	5.5 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT			
Peak pulse current	All I/O pin to pin 13; acc. IEC 61000-4-5; $t_p = 8/20 \ \mu s$ ; single shot	I <sub>PPM</sub>	4	А			
ESD immunity	Contact discharge acc. IEC61000-4-2; 10 pulses	V	± 30	kV			
	Air discharge acc. IEC61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	κV			
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C			
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C			

# MARKING (example only)







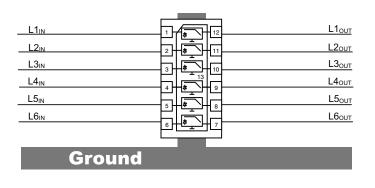
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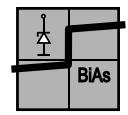
#### 6-Channel EMI-Filter with ESD-Protection

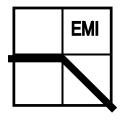


#### **APPLICATION NOTE**

With the VEMI65AA-HCI 6 different signal or data lines can be filtered and clamped to ground. Due to the different clamping levels in forward and reverse direction the clamping behaviour is <u>Bi</u>directional and <u>Asymmetric</u> (BiAs).







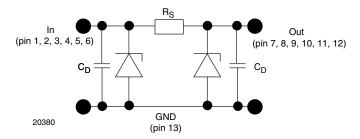
The 6 independent EMI-filter are placed between

pin 1 and pin 12, pin 2 and pin 11, pin 3 and pin 10, pin 4 and pin 9, pin 5 and pin 8 and pin 6 and pin 7.

They all are connected to a common ground pin 13 on the backside of the package.

The circuit diagram of one EMI-filter-channel shows two identical Z-diodes at the input to ground and the output to ground. These Z-diodes are characterized by the breakthrough voltage level ( $V_{BR}$ ) and the diode capacitance ( $C_D$ ). Below the breakthrough voltage level the Z-diodes can be considered as capacitors. Together with these capacitors and the line resistance  $R_S$  between input and output the device works as a low pass filter. Low frequency signals ( $f < f_{3dB}$ ) pass the filter while high frequency signals ( $f > f_{3dB}$ ) will be shorted to ground through the diode capacitances  $C_D$ .

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Each filter is symmetrical so that both ports can be used as input or output.



# VEMI65AA-HCI

### 6-Channel EMI-Filter with ESD-Protection

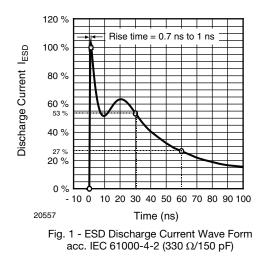
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ELECTRICAL CHARACTERISTICS VEMI65AA-HCI								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of channels which can be protected	N <sub>channel</sub>	-	-	6	channel		
Reverse stand off voltage	at I <sub>R</sub> = 1 μA	at $I_R = 1 \ \mu A$ $V_{RWM}$		-	-	V		
Reverse current	at $V_{R} = V_{RWM}$	I <sub>R</sub>	-	-	1	μA		
Reverse break down voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	6	-	-	V		
Pos. clamping voltage	at I <sub>PP</sub> = 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	-	-	7	V		
	at $I_{PP} = I_{PPM} = 4$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	-	-	8	V		
	at I <sub>PP</sub> = - 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	- 1	-	-	V		
Neg. clamping voltage	at $I_{PP} = I_{PPM} = -4$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	- 1.2	-	-	V		
Input capacitance	at $V_R = 0 V$ ; f = 1 MHz	C <sub>IN</sub>	-	60	-	pF		
	at V <sub>R</sub> = 2.5 V; f = 1 MHz	C <sub>IN</sub>	-	36	-	pF		
ESD-clamping voltage	at ± 30 kV ESD-pulse acc. IEC 61000-4-2	V <sub>CESD</sub>	-	7.5	-	V		
Line resistance	Measured between input and output; $I_S = 10 \text{ mA}$		90	100	110	Ω		
Cut-off frequency	$V_{IN}$ = 0 V; measured in a 50 $\Omega$ system	f <sub>3dB</sub>	-	100	-	MHz		

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. All inputs (pin 1, 2, 3 and 4) to ground (pin 9)

### **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25$ °C, unless otherwise specified)



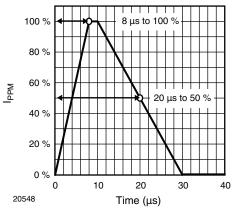


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

## VEMI65AA-HCI

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6-Channel EMI-Filter with ESD-Protection



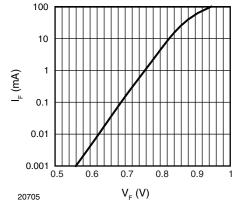


Fig. 3 - Typical Forward Current  $I_{\text{F}}$  vs. Forward Voltage  $V_{\text{F}}$ 

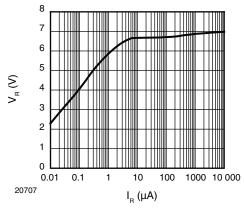


Fig. 4 - Typical Reverse Voltage  $V_{\rm R}$  vs. Reverse Current  $I_{\rm R}$ 

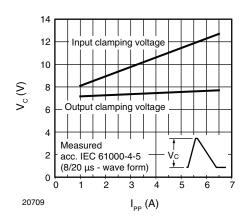


Fig. 5 - Typical Peak Clamping Voltage V\_C vs. Peak Pulse Current  $I_{PP}$ 

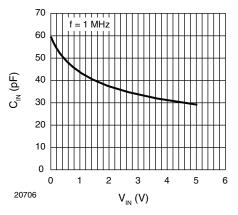


Fig. 6 - Typical Input Capacitance  $C_{IN}$  vs. Input Voltage  $V_{IN}$ 

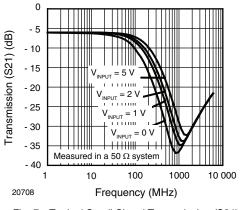


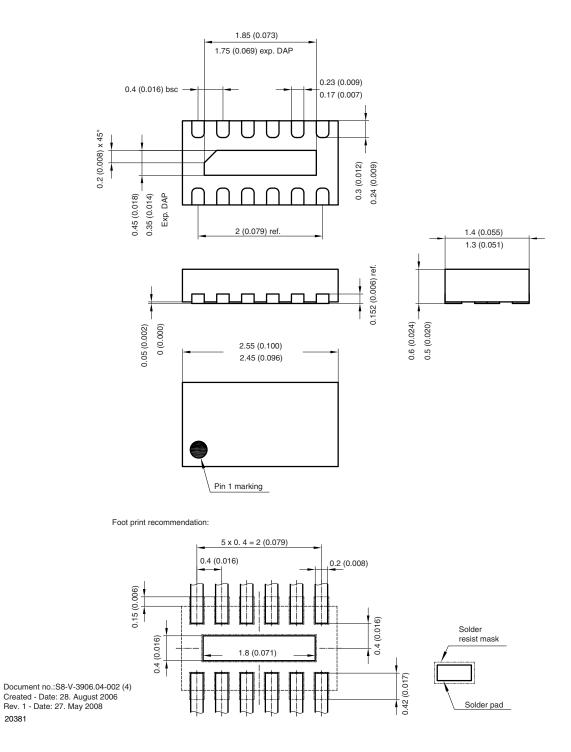
Fig. 7 - Typical Small Signal Transmission (S21) at  $~Z_{O}$  = 50  $\Omega$ 



### 6-Channel EMI-Filter with ESD-Protection

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#### PACKAGE DIMENSIONS in millimeters (inches): LLP2513-13L





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