

N-Channel Power MOSFET

500V, 5A, 1.38Ω

FEATURES

- 100% UIS and R_g tested
- Advanced planar process
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

KEY PERFORMANCE PARAMETERS

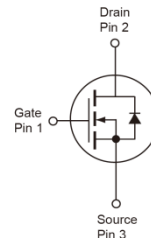
PARAMETER	VALUE	UNIT
V_{DS}	500	V
$R_{DS(on)}$ (max)	1.38	Ω
Q_g	15	nC

APPLICATIONS

- AC/DC LED Lighting
- Power Supply



ITO-220S



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	Limit	UNIT
Drain-Source Voltage	V_{DS}	500	V
Gate-Source Voltage	V_{GS}	±30	V
Continuous Drain Current (Note 1)	I_D	$T_C = 25^\circ\text{C}$	A
		$T_C = 100^\circ\text{C}$	
Pulsed Drain Current (Note 2)	I_{DM}	15	A
Total Power Dissipation @ $T_C = 25^\circ\text{C}$	P_{DTOT}	40	W
Single Pulse Avalanche Energy (Note 3)	E_{AS}	122.5	mJ
Single Pulse Avalanche Current (Note 3)	I_{AS}	3.5	A
Operating Junction and Storage Temperature Range	T_J, T_{STG}	- 55 to +150	°C

THERMAL PERFORMANCE

PARAMETER	SYMBOL	Limit	UNIT
Junction to Case Thermal Resistance	$R_{\theta JC}$	3.1	°C/W
Junction to Ambient Thermal Resistance	$R_{\theta JA}$	62	°C/W

Thermal Performance Note: $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistances. The case-thermal reference is defined at the solder mounting surface of the drain pins. $R_{\theta JA}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design. $R_{\theta JA}$ shown below for single device operation on FR-4 PCB in still air.

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	BV _{DSS}	500	--	--	V
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	V _{GS(TH)}	2.5	3.3	4.5	V
Gate Body Leakage	V _{GS} = ±30V, V _{DS} = 0V	I _{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V _{DS} = 500V, V _{GS} = 0V	I _{DSS}	--	--	1	μA
Drain-Source On-State Resistance (Note 4)	V _{GS} = 10V, I _D = 1.7A	R _{DS(on)}	--	1.1	1.38	Ω
Forward Transconductance (Note 4)	V _{DS} = 10V, I _D = 3.4A	g _{fs}	--	2.5	--	S
Dynamic (Note 5)						
Total Gate Charge	V _{DS} = 400V, I _D = 3.4A, V _{GS} = 10V	Q _g	--	15	--	nC
Gate-Source Charge		Q _{gs}	--	4	--	
Gate-Drain Charge		Q _{gd}	--	7	--	
Input Capacitance	V _{DS} = 50V, V _{GS} = 0V, f = 1.0MHz	C _{iss}	--	586	--	pF
Output Capacitance		C _{oss}	--	45	--	
Reverse Transfer Capacitance		C _{rss}	--	1	--	
Gate Resistance	f = 1.0MHz, open drain	R _g	0.9	3	6	Ω
Switching (Note 6)						
Turn-On Delay Time	V _{DD} = 250V, R _G = 5Ω, I _D = 3.4A, V _{GS} = 10V	t _{d(on)}	--	8.2	--	ns
Turn-On Rise Time		t _r	--	20.4	--	
Turn-Off Delay Time		t _{d(off)}	--	15.4	--	
Turn-Off Fall Time		t _f	--	20	--	
Source-Drain Diode						
Forward Voltage (Note 4)	I _S = 3.4A, V _{GS} = 0V	V _{SD}	--	--	1.3	V
Reverse Recovery Time	I _S = 3.4A	t _{rr}	--	205	--	ns
Reverse Recovery Charge	dI _F /dt = 100A/μs	Q _{rr}	--	1.5	--	μC

Notes:

1. Current limited by package
2. Pulse width limited by the maximum junction temperature
3. $L = 20mH, I_{AS} = 3.5A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
4. Pulse test: $PW \leq 300\mu s$, duty cycle $\leq 2\%$
5. For DESIGN AID ONLY, not subject to production testing.
6. Switching time is essentially independent of operating temperature.

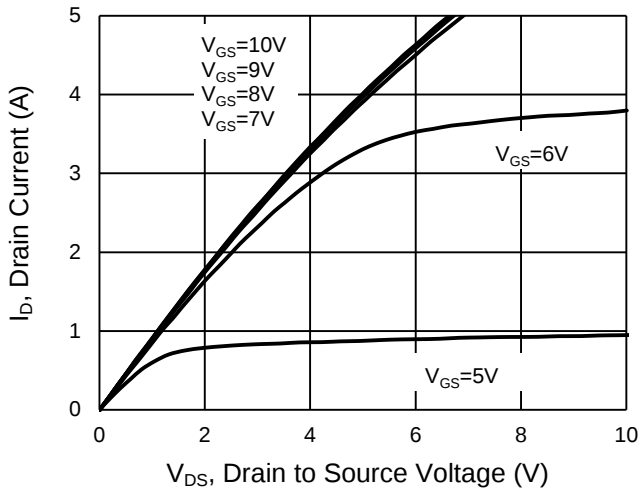
ORDERING INFORMATION

PART NO.	PACKAGE	PACKING
TSM5NC50CF C0G	ITO-220S	50pcs / Tube

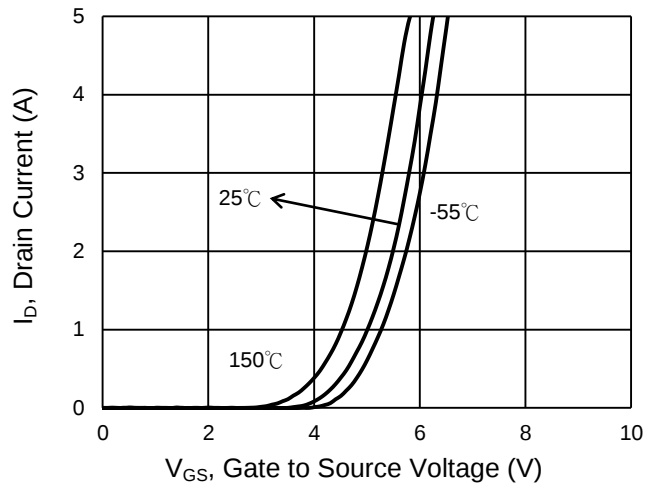
CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)

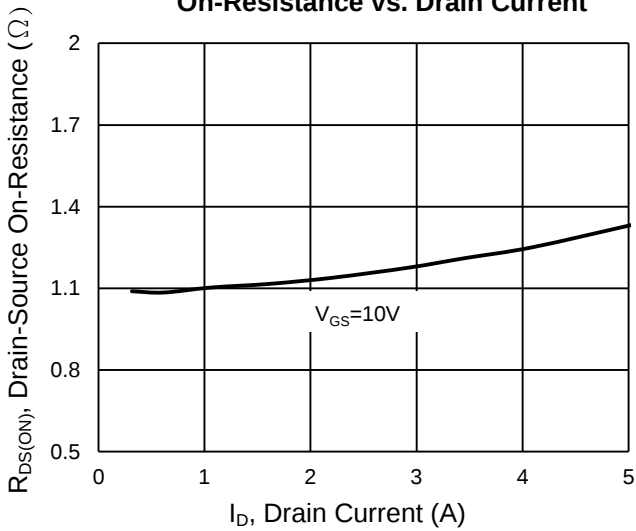
Output Characteristics



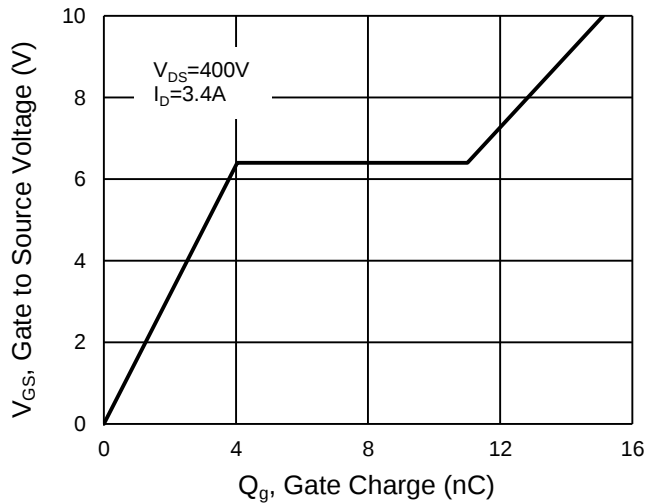
Transfer Characteristics



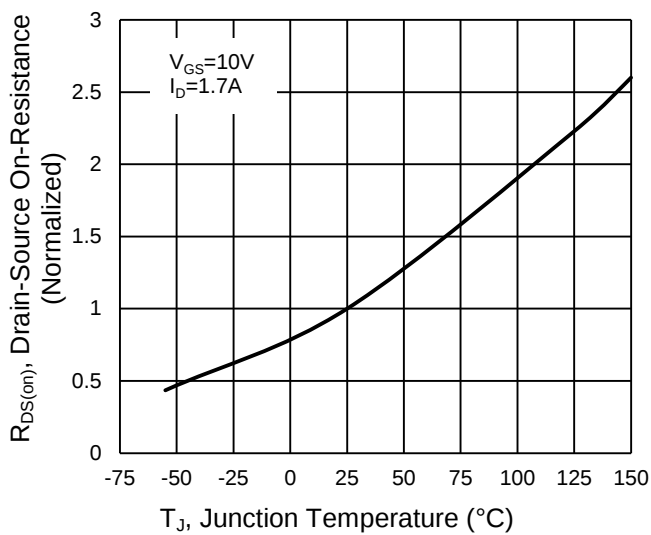
On-Resistance vs. Drain Current



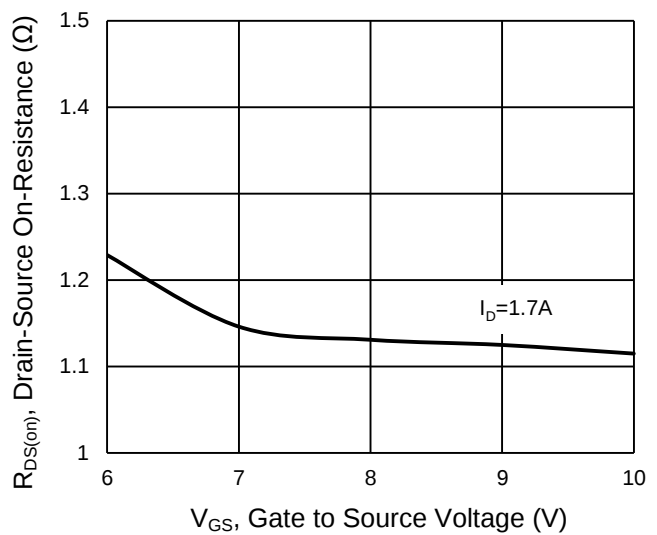
Gate-Source Voltage vs. Gate Charge



On-Resistance vs. Junction Temperature



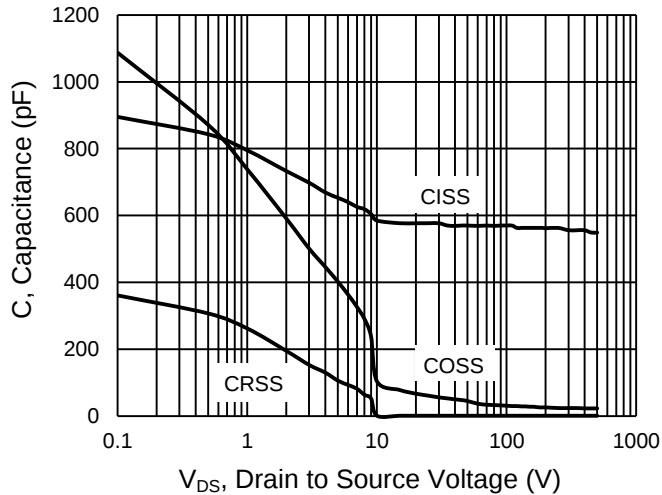
On-Resistance vs. Gate-Source Voltage



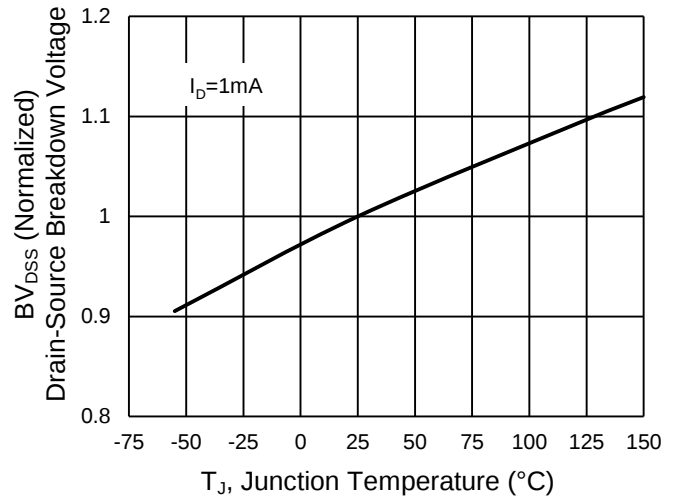
CHARACTERISTICS CURVES

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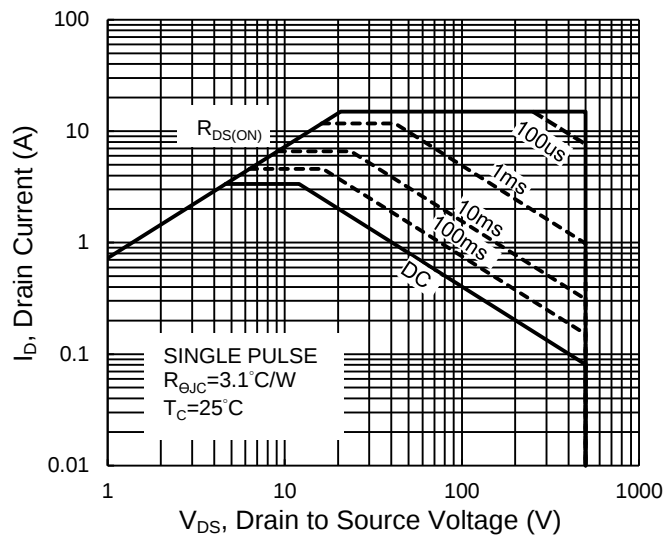
Capacitance vs. Drain-Source Voltage



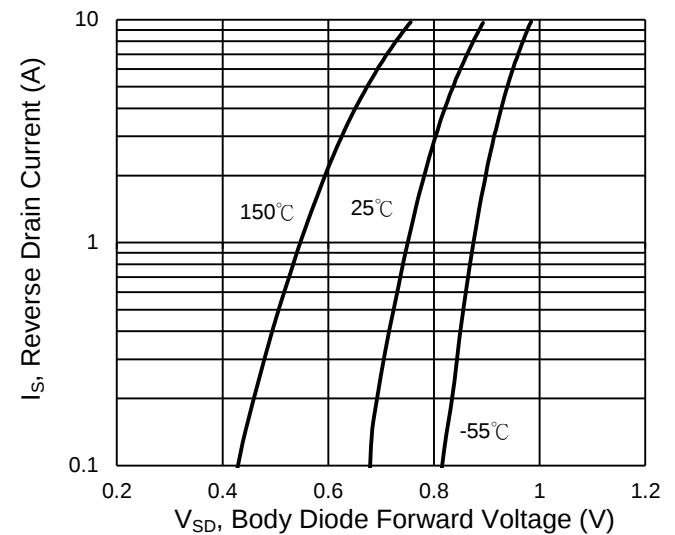
BV_{DSS} vs. Junction Temperature



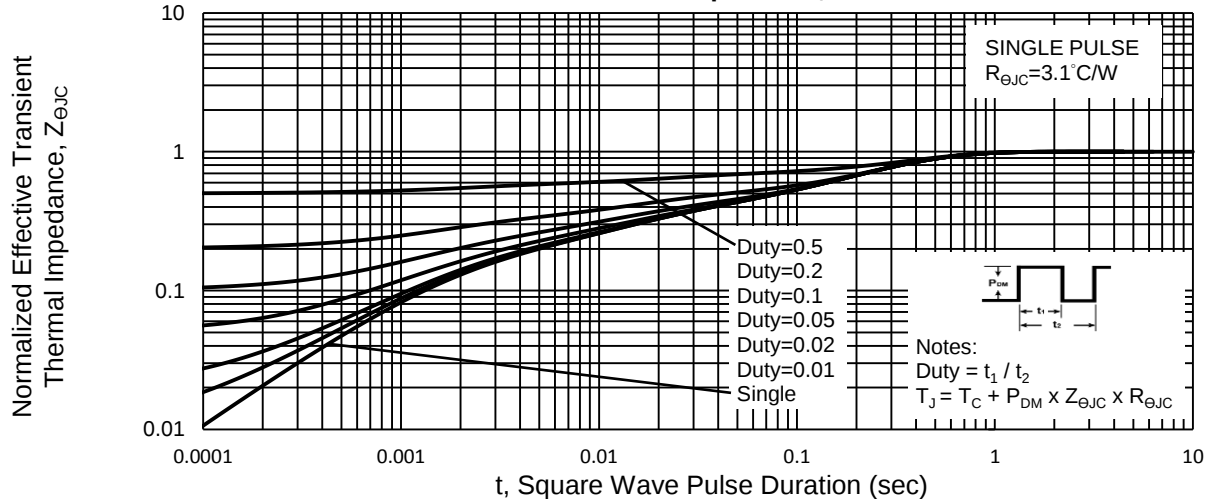
Maximum Safe Operating Area, Junction-to-Case



Source-Drain Diode Forward Current vs. Voltage

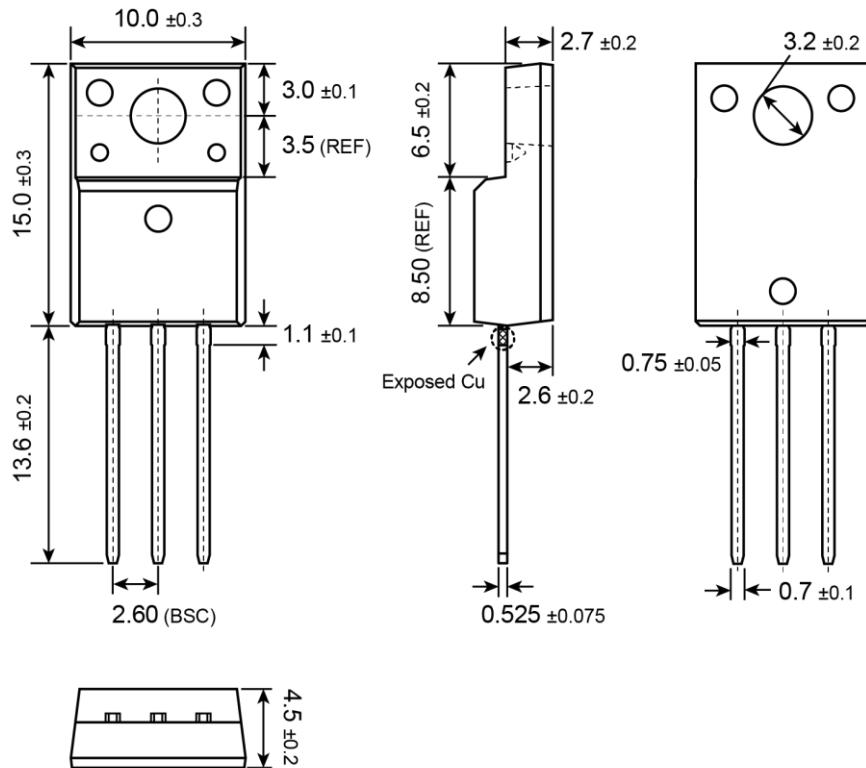


Normalized Thermal Transient Impedance, Junction-to-Case

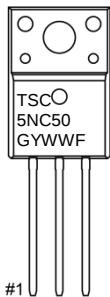


PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

ITO-220S



MARKING DIAGRAM



- G** = Halogen Free
- Y** = Year Code
- WW** = Week Code (01~52)
- F** = Factory Code

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