

FO transceiver - FOC-M12-MNNA-TC-650 - 1416716

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M12 FO transceiver, duplex, wavelength: 650 nm for 125 Mbps, for POF and PCF fiber, for PCB mounting, M16 fastening thread, with sealing cap, including I²C interface for transceiver monitoring and management

M12 OPTIC transceiver for POF and PCF

Product description

M12 OPTIC transceiver, 650 nm, 125 Mbps

Product Features

- ✓
- ✓ robust metal housing
- ✓ Pin connector pattern in accordance with DIN/IEC 61754-27
- ✓ I²C management interface
- ✓ Signal Detect (SD) output
- ✓ PECL inputs/outputs
- ✓ -4 dBm output power
- ✓ -20 dBm receiver sensitivity
- ✓



Ethernet

Key commercial data

Packing unit	1 pc
Weight per Piece (excluding packing)	40.0 GRM
Custom tariff number	85366990
Country of origin	Germany

Technical data

Ambient conditions

Ambient temperature (operation)	-40 °C ... 85 °C
Ambient temperature (storage/transport)	-40 °C ... 85 °C

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Technical data

Ambient conditions

Degree of protection	IP65/IP67
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Material data

Inflammability class according to UL 94	V0
Housing material	Metal

Classifications

eCl@ss

eCl@ss 4.0	272607xx
eCl@ss 4.1	27260701
eCl@ss 5.0	27260701
eCl@ss 5.1	27260701
eCl@ss 6.0	27260708
eCl@ss 7.0	27440312

ETIM

ETIM 3.0	EC001122
ETIM 4.0	EC001122
ETIM 5.0	EC001122

UNSPSC

UNSPSC 6.01	31261501
UNSPSC 7.0901	31261501
UNSPSC 11	31261501
UNSPSC 12.01	31261501
UNSPSC 13.2	31261501

Approvals

Approvals

Approvals

EAC

Ex Approvals

FO transceiver - FOC-M12-MNNA-TC-650 - 1416716

Approvals

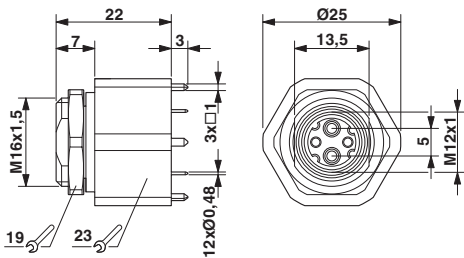
Approvals submitted

Approval details

EAC

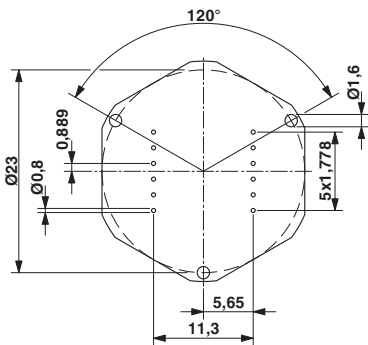
Drawings

Dimensional drawing



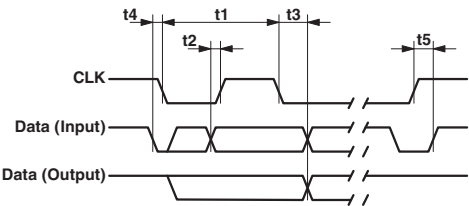
Housing dimensions

Drilling diagram



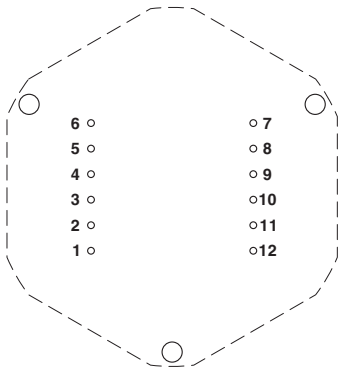
Drilling diagram

Diagram



I2C bus timing

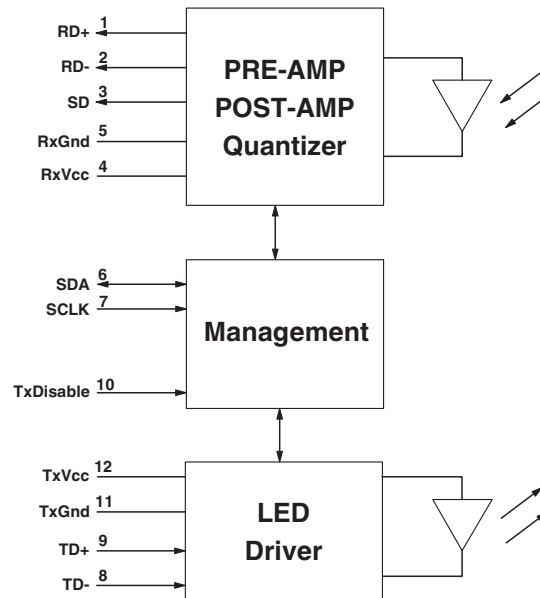
Schematic diagram



Pin assignment, component mounting side view

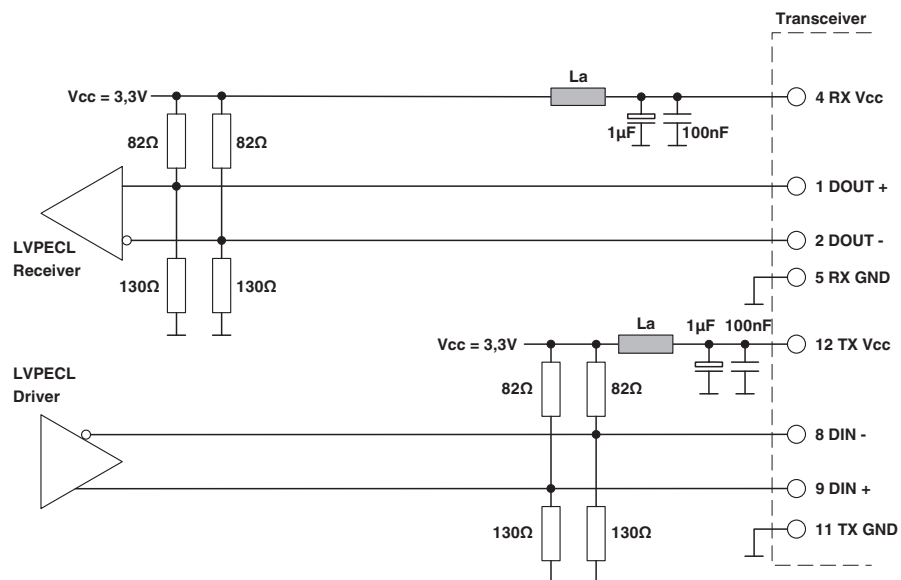
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Block diagram



Transceiver block diagram

Circuit diagram



External wiring of the transceiver.

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Ferrite coil La:

Typical impedance of 120 Ohm at 100 MHz,

Maximum DC resistance of 0.15 Ohm at 200 mA

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