

# NHD-4.3-480272EF-ASXN#-T

## TFT (Thin-Film-Transistor) Color Liquid Crystal Display Module

|         |                                 |
|---------|---------------------------------|
| NHD-    | Newhaven Display                |
| 4.3-    | 4.3" Diagonal                   |
| 480272- | 480xRGBx272 Pixels              |
| EF-     | Model                           |
| A-      | Built-in Driver / No Controller |
| S-      | Sunlight Readable               |
| X-      | TFT                             |
| N-      | TN, Wide Temperature            |
| #-      | <b>RoHS Compliant</b>           |
| T-      | 4-wire Resistive Touch Panel    |

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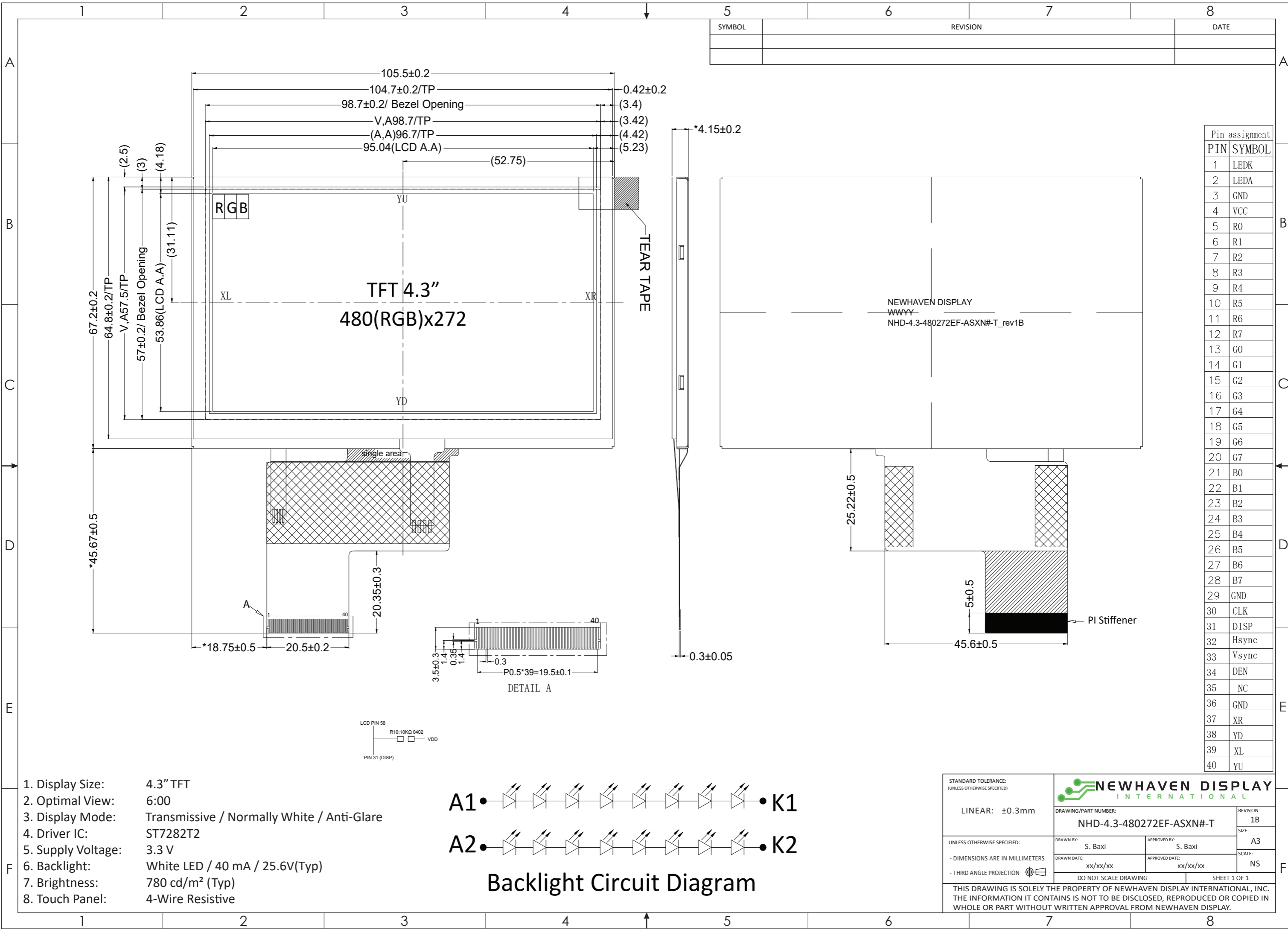
[nhsales@newhavendisplay.com](mailto:nhsales@newhavendisplay.com)

## Document Revision History

| Revision | Date    | Description  | Changed by |
|----------|---------|--|------------|
| 0        | 9/22/15 | Initial Release  | SB         |
| 1        | 1/13/17 | Mechanical Drawing, Electrical & Optical Char. Updated | SB         |
| 2        | 5/2/17  | Driver IC Updated                                      | SB         |
| 3        | 2/26/20 | FPC Layout Updated                                     | SB         |

## Functions and Features

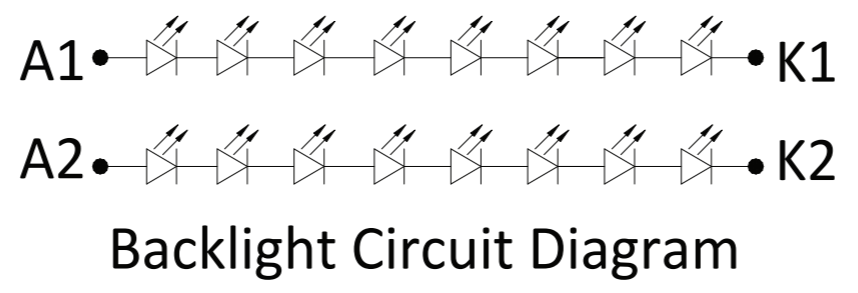
- 480xRGBx272 resolution, up to 16.7M colors
- 16-LED backlight
- 24-Bit RGB interface
- 4-wire Resistive touch panel
- Sunlight Readable



| SYMBOL | REVISION | DATE |
|--------|----------|------|
|        |          |      |

| Pin assignment |        |
|----------------|--------|
| PIN            | SYMBOL |
| 1              | LEDK   |
| 2              | LEDA   |
| 3              | GND    |
| 4              | VCC    |
| 5              | R0     |
| 6              | R1     |
| 7              | R2     |
| 8              | R3     |
| 9              | R4     |
| 10             | R5     |
| 11             | R6     |
| 12             | R7     |
| 13             | G0     |
| 14             | G1     |
| 15             | G2     |
| 16             | G3     |
| 17             | G4     |
| 18             | G5     |
| 19             | G6     |
| 20             | G7     |
| 21             | B0     |
| 22             | B1     |
| 23             | B2     |
| 24             | B3     |
| 25             | B4     |
| 26             | B5     |
| 27             | B6     |
| 28             | B7     |
| 29             | GND    |
| 30             | CLK    |
| 31             | DISP   |
| 32             | Hsync  |
| 33             | Vsync  |
| 34             | DEN    |
| 35             | NC     |
| 36             | GND    |
| 37             | XR     |
| 38             | YD     |
| 39             | XL     |
| 40             | YU     |

- 1. Display Size: 4.3" TFT
- 2. Optimal View: 6:00
- 3. Display Mode: Transmissive / Normally White / Anti-Glare
- 4. Driver IC: ST7282T2
- 5. Supply Voltage: 3.3 V
- 6. Backlight: White LED / 40 mA / 25.6V(Typ)
- 7. Brightness: 780 cd/m<sup>2</sup> (Typ)
- 8. Touch Panel: 4-Wire Resistive



|   |   |                            |
|---|---|----------------------------|
| STANDARD TOLERANCE:<br>(UNLESS OTHERWISE SPECIFIED)   |   |                            |
| LINEAR: ±0.3mm  | DRAWING/PART NUMBER:<br><b>NHD-4.3-480272EF-ASXN#-T</b> |                            |
| UNLESS OTHERWISE SPECIFIED:<br>- DIMENSIONS ARE IN MILLIMETERS<br>- THIRD ANGLE PROJECTION  | DRAWN BY:<br>S. Baxi                                    | APPROVED BY:<br>S. Baxi    |
|   | DRAWN DATE:<br>xx/xx/xx                                 | APPROVED DATE:<br>xx/xx/xx |
|   | DO NOT SCALE DRAWING                                    |                            |
|   | SHEET 1 OF 1  |                            |
| THIS DRAWING IS SOLELY THE PROPERTY OF NEWHAVEN DISPLAY INTERNATIONAL, INC. THE INFORMATION IT CONTAINS IS NOT TO BE DISCLOSED, REPRODUCED OR COPIED IN WHOLE OR PART WITHOUT WRITTEN APPROVAL FROM NEWHAVEN DISPLAY. |   |                            |

## Pin Description

| Pin No. | Symbol          | External Connection | Function Description                    |
|---------|-----------------|---------------------|---|
| 1       | LED-            | Power Supply        | Backlight Cathode                       |
| 2       | LED+            | Power Supply        | Backlight Anode (40mA @ 25.6V)          |
| 3       | GND             | Power Supply        | Ground                                  |
| 4       | V <sub>DD</sub> | Power Supply        | Supply Voltage for LCD and logic (3.3V) |
| 5-12    | [R0-R7]         | MPU                 | Red Data signals                        |
| 13-20   | [G0-G7]         | MPU                 | Green Data signals                      |
| 21-28   | [B0-B7]         | MPU                 | Blue Data signals                       |
| 29      | GND             | Power Supply        | Ground                                  |
| 30      | CLK             | MPU                 | Data sample Clock signal                |
| 31      | DISP            | MPU                 | Display ON/OFF signal                   |
| 32      | HSYNC           | MPU                 | Line synchronization signal             |
| 33      | VSYNC           | MPU                 | Frame synchronization signal            |
| 34      | DE              | MPU                 | Data Enable signal                      |
| 35      | NC              | -                   | No Connect                              |
| 36      | GND             | Power Supply        | Ground                                  |
| 37      | XR              | Touch Controller    | Touch Panel Right                       |
| 38      | YD              | Touch Controller    | Touch Panel Down                        |
| 39      | XL              | Touch Controller    | Touch Pane Left                         |
| 40      | YU              | Touch Controller    | Touch Panel Up                          |

**Recommended LCD connector:** 0.5mm pitch 40-Conductor FFC. Molex p/n: 54104-4031

**Backlight connector:** on LCD connector      **Mates with:** ---

## Driver Information

Built-in Sitronix ST7282T2 Driver.

Please download specification at <http://www.newhavendisplay.com/appnotes/datasheets/LCDs/ST7282T2.pdf>

## Electrical Characteristics

| Item                        | Symbol           | Condition   | Min.                  | Typ.   | Max.                  | Unit |
|-----------------------------|------------------|---|-----------------------|--------|-----------------------|------|
| Operating Temperature Range | T <sub>OP</sub>  | Absolute Max                                      | -20                   | -      | +70                   | °C   |
| Storage Temperature Range   | T <sub>ST</sub>  | Absolute Max                                      | -30                   | -      | +80                   | °C   |
| Supply Voltage              | V <sub>DD</sub>  | -   | 3.0                   | 3.3    | 3.6                   | V    |
| Supply Current              | I <sub>DD</sub>  | V <sub>DD</sub> = 3.3V                            | 10                    | 25     | 50                    | mA   |
| "H" level input             | V <sub>IH</sub>  | -   | 0.7 * V <sub>DD</sub> | -      | V <sub>DD</sub>       | V    |
| "L" level input             | V <sub>IL</sub>  | -   | V <sub>SS</sub>       | -      | 0.3 * V <sub>DD</sub> | V    |
| Backlight Supply Current    | I <sub>LED</sub> | -   | -                     | 40     | 50                    | mA   |
| Backlight Supply Voltage    | V <sub>LED</sub> | I <sub>LED</sub> = 40mA                           | 22.4                  | 25.6   | 27.2                  | V    |
| Backlight Lifetime          | -                | I <sub>LED</sub> = 40mA<br>T <sub>OP</sub> = 25°C | 20,000                | 50,000 | -                     | Hrs. |

\*Backlight lifetime is rated as Hours until **half-brightness**, under normal operating conditions. The LED of the backlight is driven by current drain; drive voltage is for reference only. Drive voltage must be selected to ensure backlight current drain is below MAX level stated.

## Optical Characteristics

| Item                   | Symbol         | Condition                       | Min.                   | Typ.  | Max.  | Unit              |    |    |
|------------------------|----------------|---------------------------------|------------------------|-------|-------|-------------------|----|----|
| Optimal Viewing Angles | Top            | Cr ≥ 10                         | -                      | 40    | -     | °                 |    |    |
|                        | Bottom         |                                 | -                      | 60    | -     | °                 |    |    |
|                        | Left           |                                 | -                      | 60    | -     | °                 |    |    |
|                        | Right          |                                 | -                      | 60    | -     | °                 |    |    |
| Contrast Ratio         | Cr             | -                               | 400                    | 500   | -     | -                 |    |    |
| Luminance              | L <sub>V</sub> | I <sub>LED</sub> = 40 mA        | 620                    | 780   | -     | cd/m <sup>2</sup> |    |    |
| Response Time          | Rise + Fall    | T <sub>R</sub> + T <sub>F</sub> | T <sub>OP</sub> = 25°C |       | -     | 20                | 30 | ms |
| Chromaticity           | Red            | X <sub>R</sub>                  | -                      | 0.530 | 0.580 | 0.630             | -  |    |
|                        |                | Y <sub>R</sub>                  | -                      | 0.298 | 0.348 | 0.398             | -  |    |
|                        | Green          | X <sub>G</sub>                  | -                      | 0.262 | 0.312 | 0.362             | -  |    |
|                        |                | Y <sub>G</sub>                  | -                      | 0.570 | 0.620 | 0.670             | -  |    |
|                        | Blue           | X <sub>B</sub>                  | -                      | 0.093 | 0.143 | 0.193             | -  |    |
|                        |                | Y <sub>B</sub>                  | -                      | 0.048 | 0.098 | 0.148             | -  |    |
| White                  | X <sub>W</sub> | -                               | 0.230                  | 0.283 | 0.330 | -                 |    |    |
|                        | Y <sub>W</sub> | -                               | 0.283                  | 0.333 | 0.383 | -                 |    |    |

## Touch Panel Characteristics

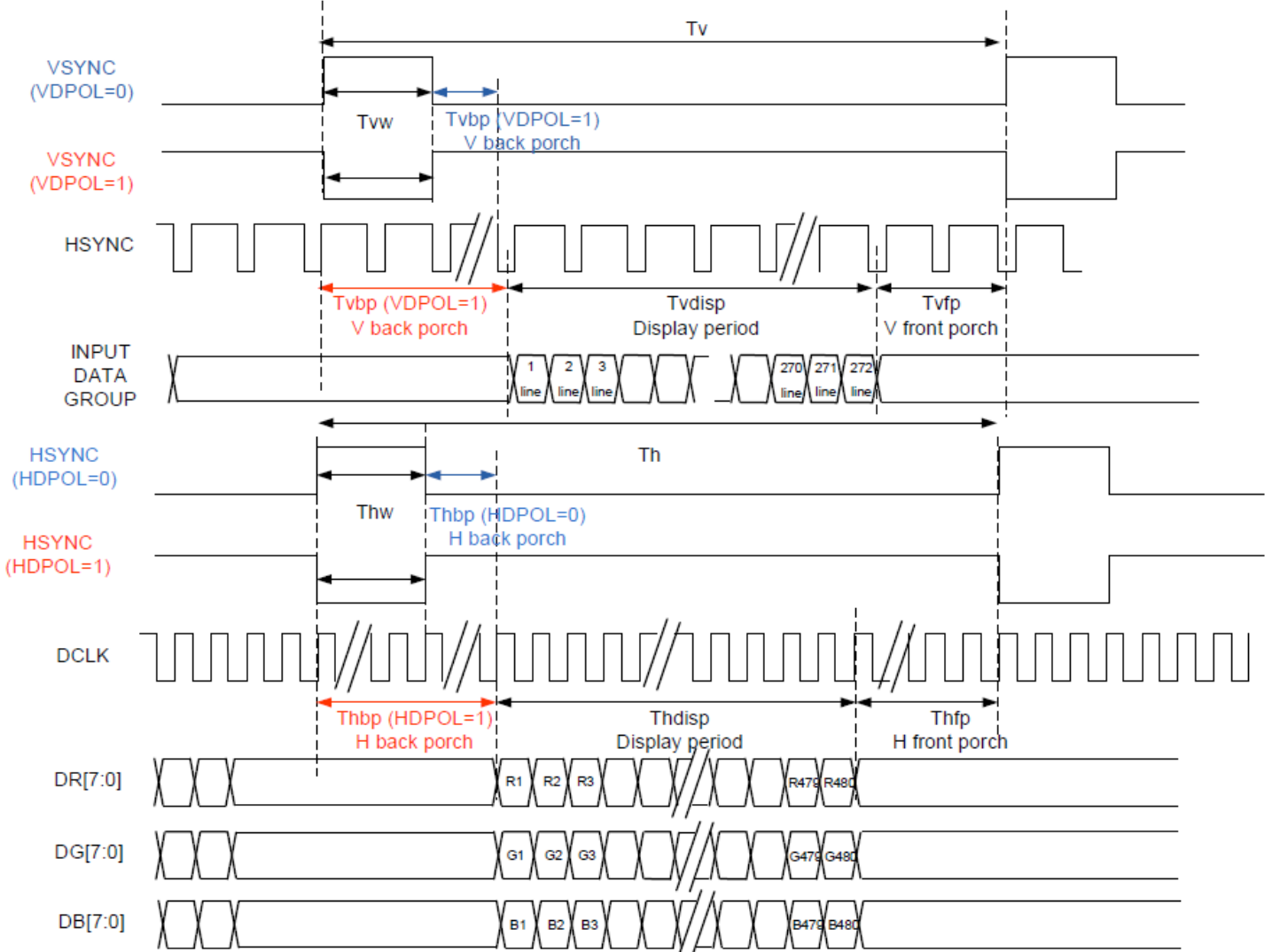
| Item                        | Min.      | Typ. | Max. | Unit       |
|-----------------------------|-----------|------|------|------------|
| Linearity                   | -1.5      | -    | 1.5  | %          |
| Circuit Resistance – X-Axis | 350       | -    | 1050 | Ω          |
| Circuit Resistance – Y-Axis | 100       | -    | 450  | Ω          |
| Insulation Resistance       | 20        | -    | -    | MΩ         |
| Operating Voltage           | -         | -    | 10   | V          |
| Chattering                  | -         | -    | 15   | ms         |
| Transmittance               | 80        | -    | -    | %          |
| Activation Force            | 20        | -    | 80   | g          |
| Pen Writing Durability      | 20,000    | -    | -    | Characters |
| Pitting Durability          | 1,000,000 | -    | -    | Touches    |
| Surface Hardness            | 3         | -    | -    | H          |
| Haze                        | 4         | 7    | 10   | %          |

# Timing Characteristics

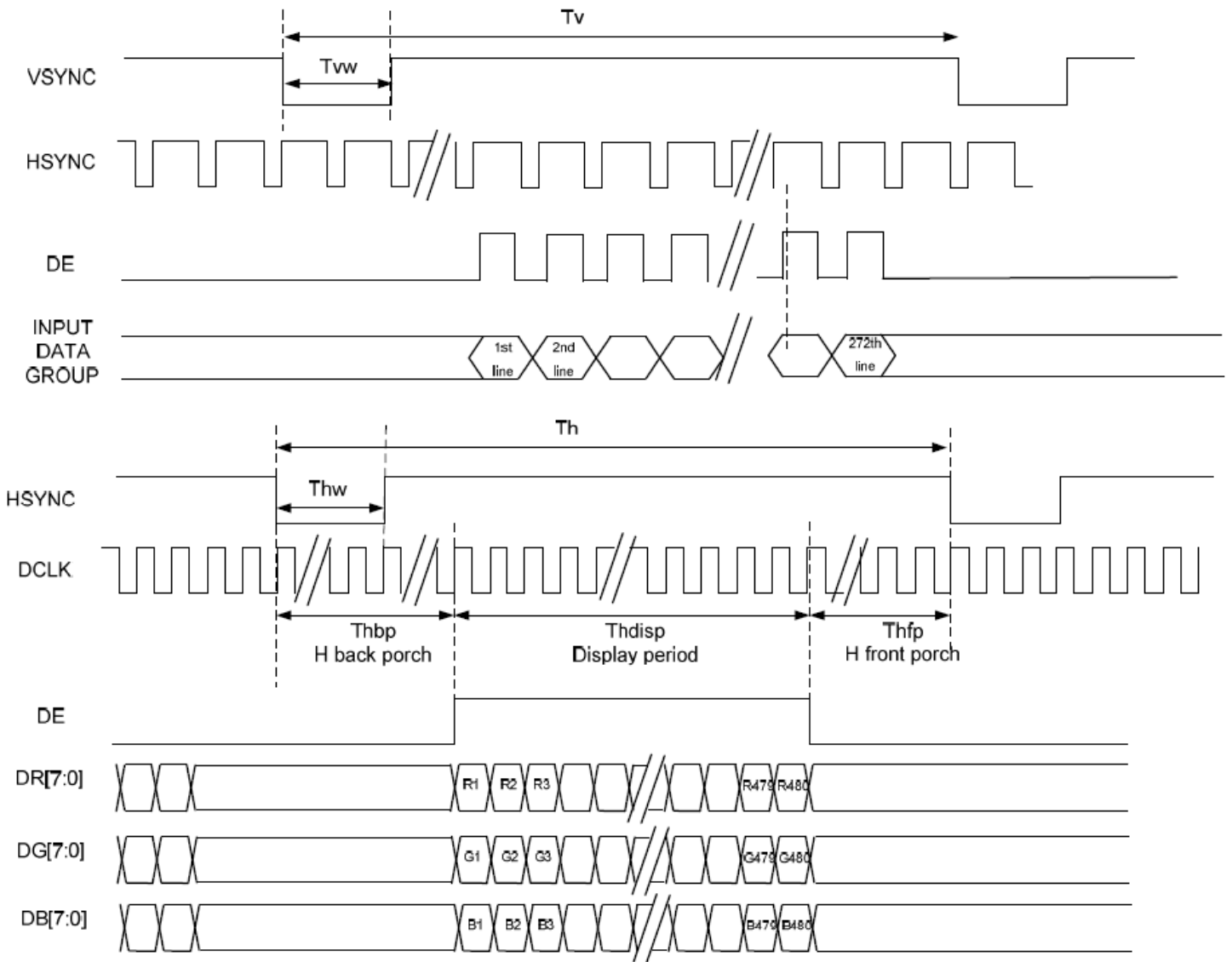
## Parallel RGB input timing requirement

| Item           |                | Symbol | Min. | Typ. | Max. | Unit | Remark                |
|----------------|----------------|--------|------|------|------|------|-----------------------|
| DCLK Frequency |                | FCLK   | 9    | 12   | 15   | MHz  |                       |
| DCLK Period    |                | TCLK   | 10   | 50   | -    | μS   | R=10KΩ, 1μF           |
| HSYNC          | Period Time    | Th     | 485  | 525  | 532  | DCLK |                       |
|                | Display Period | Thdisp | -    | 480  | -    | DCLK |                       |
|                | Back Porch     | Thbp   | 3    | 43   | 50   | DCLK | By H_Blanking Setting |
|                | Front Porch    | Thfp   | 2    | 2    | 2    | DCLK |                       |
|                | Pulse Width    | Thw    | 1    | 1    | 1    | DCLK |                       |
| VSYNC          | Period Time    | Tv     | 275  | 285  | 303  | H    |                       |
|                | Display Period | Tvdisp | -    | 272  | -    | H    |                       |
|                | Back Porch     | Tvbp   | 2    | 12   | 30   | H    | By V_Blanking Setting |
|                | Front Porch    | Tvfp   | 1    | 1    | 1    | H    |                       |
|                | Pulse Width    | Tvw    | 1    | 1    | 1    | H    |                       |

- SYNC Mode Timing



- SYNC-DE Mode Timing

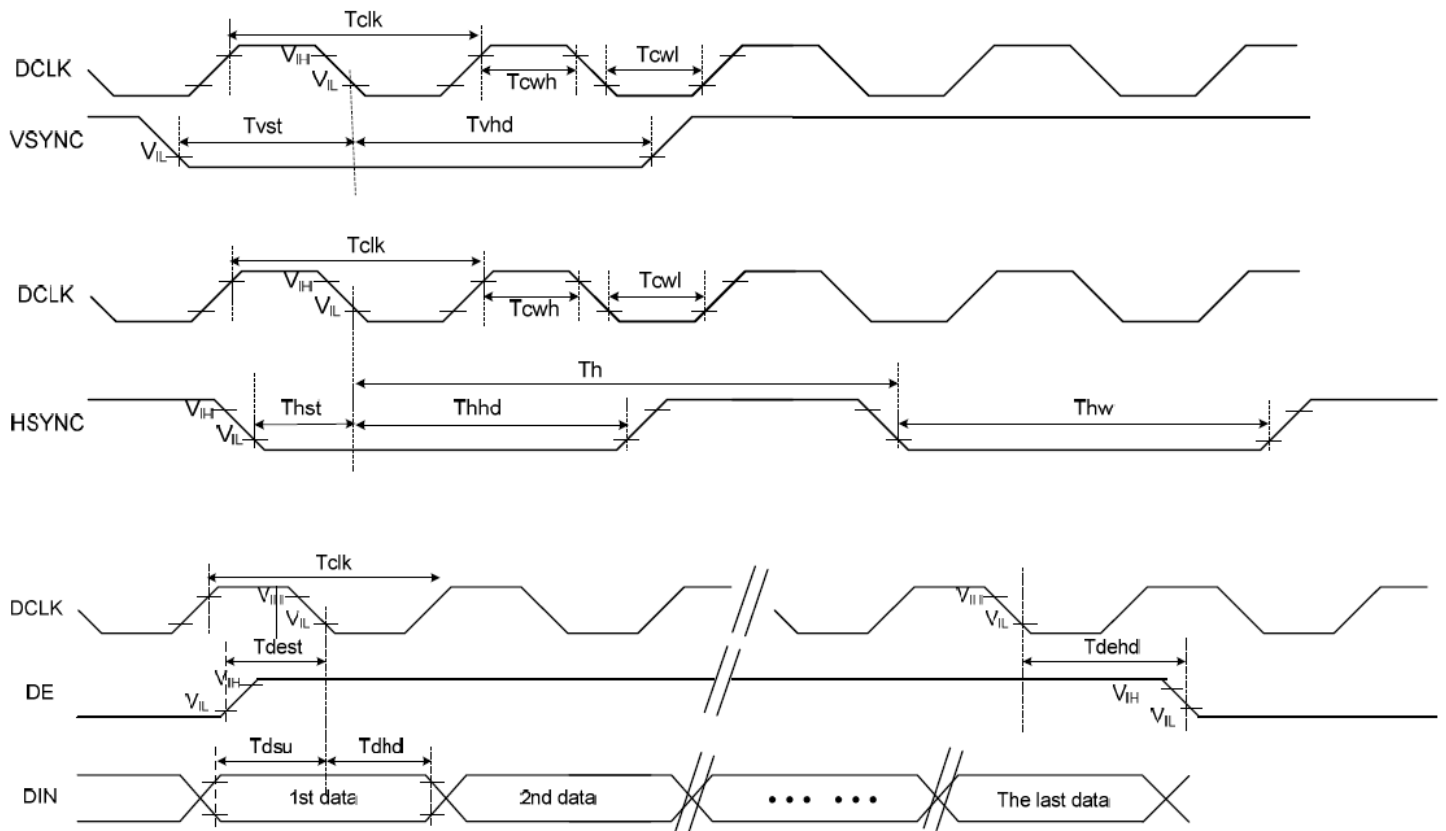




## Input setup timing requirement

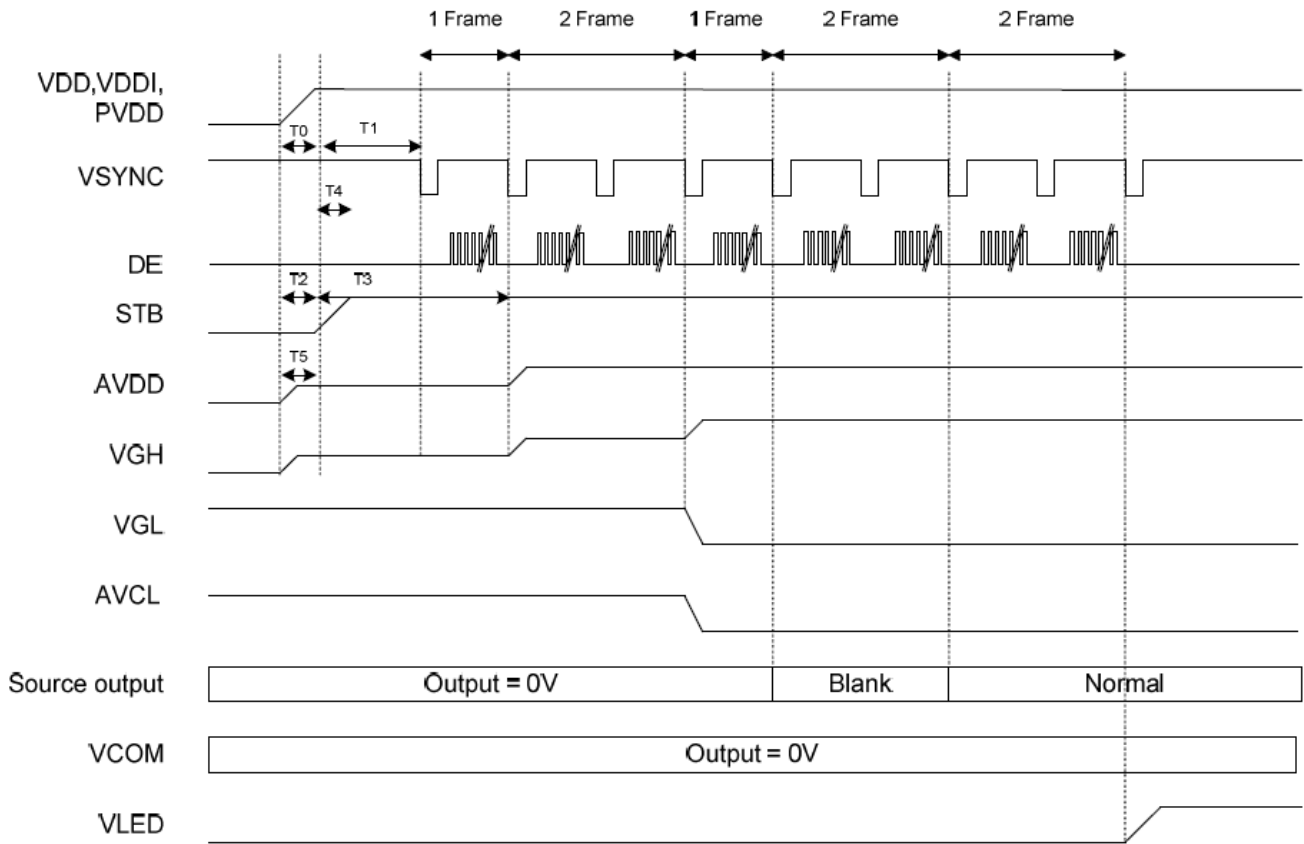
| Item                                   | Symbol | Min. | Typ. | Max. | Unit | Conditions   |
|--|--------|------|------|------|------|--|
| System Operation Timing                |        |      |      |      |      |  |
| V <sub>DD</sub> Power Source Slew Time | TPOR   | -    | -    | 20   | ms   | From 0V to 99% V <sub>DD</sub>                       |
| GRB Pulse Width                        | tRSTW  | 10   | 50   | -    | μS   | R=10KΩ, 1μF  |
| Input / Output Timing                  |        |      |      |      |      |  |
| CLK pulse Duty                         | TCW    | 40   | 50   | 60   | %    |  |
| Hsync Width                            | Thw    | 1    | -    | -    | DCLK |  |
| Hsync Period                           | Th     | 50   | 60   | 65   | μS   |  |
| Vsync setup time                       | Tvst   | 12   | -    | -    | ns   |  |
| Vsync hold time                        | Tvhd   | 12   | -    | -    | ns   |  |
| Hsync setup time                       | Thst   | 12   | -    | -    | ns   |  |
| Hsync hold time                        | Thhd   | 12   | -    | -    | ns   |  |
| Data setup time                        | Tdsu   | 12   | -    | -    | ns   |  |
| Data hold time                         | Tdhd   | 12   | -    | -    | ns   |  |
| SD output stable time                  | Tst    | -    | -    | 12   | μS   | Output settled within +20mV<br>Loading = 6.8k+28.2pF |
| GD output rise and fall time           | Tgst   | -    | -    | 6    | μS   | Output settled (5%~95%)<br>Loading = 4.7k+29.8pF     |

### - Clock And Data Input Timing Diagram



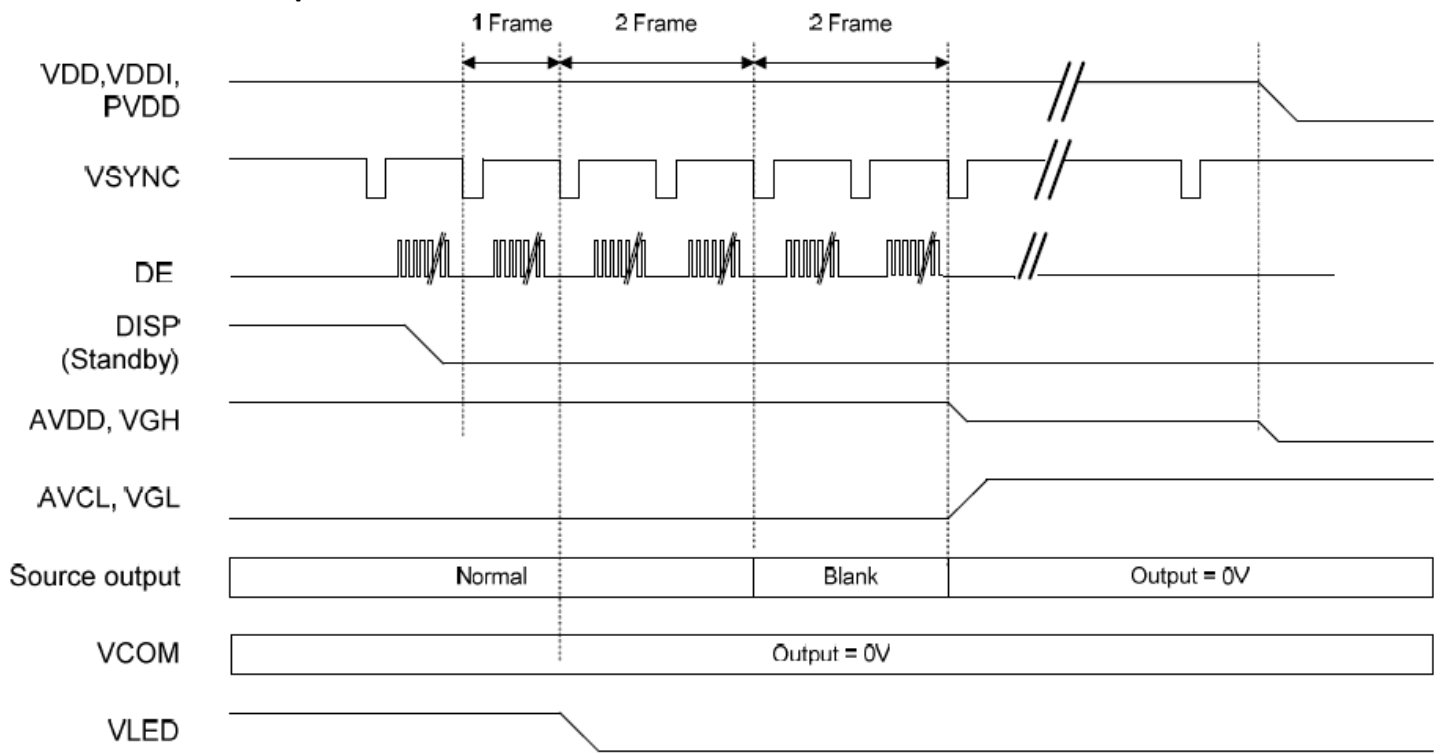
# Power On/Off Sequence

## - Power On Sequence



|    | Description  | Min. Time        |
|----|--|------------------|
| T0 | Determined by the external power                           |                  |
| T1 | Time from stable VDD, VDDI, PVDD set-up to the first VSYNC | T1=0             |
| T2 | Time from AVDD=0V to AVDD=3.3V                             | T2=T0            |
| T3 | Time from AVDD=3.3V to AVDD=6.0V                           | T3=T1+ (1*Frame) |
| T4 | Time from stable VDD, VDDI, PVDD set-up to DISP asserted   | T4=0             |
| T5 | Time from VGH=0V to VGH=3.3V                               | T5=T0            |

- **Power Off Sequence**



## Quality Information

| Test Item                             | Content of Test   | Test Condition  | Note |
|---------------------------------------|---|---|------|
| High Temperature storage              | Endurance test applying the high storage temperature for a long time.   | +80°C , 96hrs   | 2    |
| Low Temperature storage               | Endurance test applying the low storage temperature for a long time.  | -30°C , 96hrs   | 1,2  |
| High Temperature Operation            | Endurance test applying the electric stress (voltage & current) and the high thermal stress for a long time.                    | +70°C , 96hrs   | 2    |
| Low Temperature Operation             | Endurance test applying the electric stress (voltage & current) and the low thermal stress for a long time.                     | -20°C , 96hrs   | 1,2  |
| High Temperature / Humidity Operation | Endurance test applying the electric stress (voltage & current) and the high thermal with high humidity stress for a long time. | +60°C , 90% RH , 96hrs  | 1,2  |
| Thermal Shock resistance              | Endurance test applying the electric stress (voltage & current) during a cycle of low and high thermal stress.                  | -20°C,30min -> 25°C,5min ->70°C,30min = 1 cycle<br>10 cycles                        |      |
| Vibration test                        | Endurance test applying vibration to simulate transportation and use.   | 10-55Hz , 15mm amplitude.<br>60 sec in each of 3 directions X,Y,Z<br>For 15 minutes | 3    |
| Static electricity test               | Endurance test applying electric static discharge.  | VS=800V, RS=1.5kΩ, CS=100pF<br>One time   |      |

**Note 1:** No condensation to be observed.

**Note 2:** Conducted after 4 hours of storage at 25°C, 0%RH.

**Note 3:** Test performed on product itself, not inside a container.

## Precautions for using LCDs/LCMs

See Precautions at [www.newhavendisplay.com/specs/precautions.pdf](http://www.newhavendisplay.com/specs/precautions.pdf)

## Warranty Information

See Terms & Conditions at [http://www.newhavendisplay.com/index.php?main\\_page=terms](http://www.newhavendisplay.com/index.php?main_page=terms)