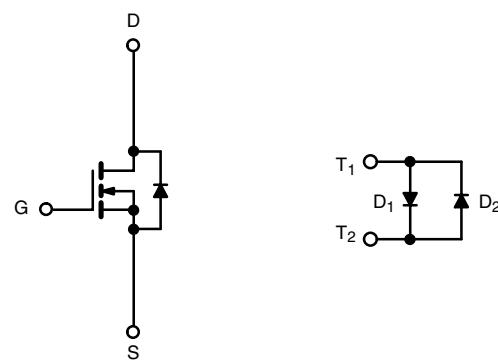
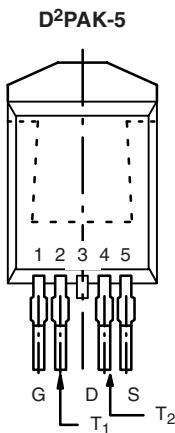


N-Channel 40-V (D-S) MOSFET with Sensing Diode

PRODUCT SUMMARY		
$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ (Ω)	I_D (A)
40	0.0054 at $V_{GS} = 10$ V	60 ^a

FEATURES

- TrenchFET® Power MOSFETs Plus Temperature Sensing Diode
- 175 °C Junction Temperature
- Low Thermal Resistance Package


RoHS*
COMPLIANT


Ordering Information: SUM60N04-05T-E3 (Lead (Pb)-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175$ °C) ^d	I_D	60 ^a	A
		60 ^a	
Pulsed Drain Current	I_{DM}	250	
Continuous Diode Current (Diode Conduction) ^d	I_S	60 ^a	
Avalanche Current	I_{AS}	60 ^a	
Repetitive Avalanche Energy ^b	E_{AS}	180	mJ
Maximum Power Dissipation ^a	P_D	200 ^c	W
		3.75 ^d	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient ^d	R_{thJA}	40	°C/W
Junction-to-Case	R_{thJC}	0.75	

Notes:

- a. Package limited.
- b. Duty cycle $\leq 1\%$.
- c. See SOA curve for voltage derating.
- d. When mounted on 1" square PCB (FR-4 material).

* Pb containing terminations are not RoHS compliant, exemptions may apply.

SUM60N04-05T

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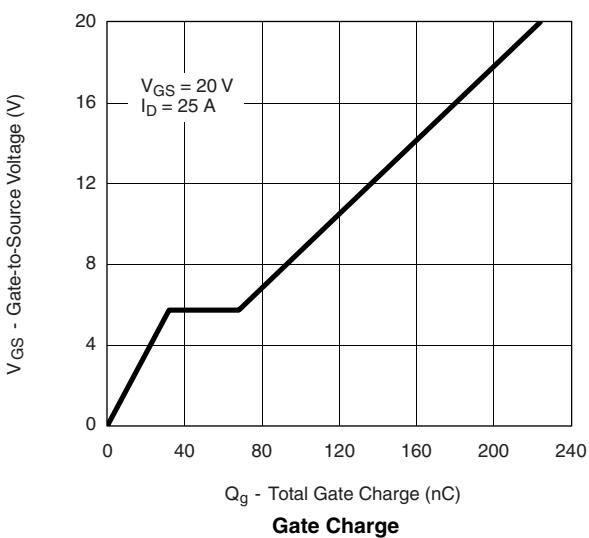
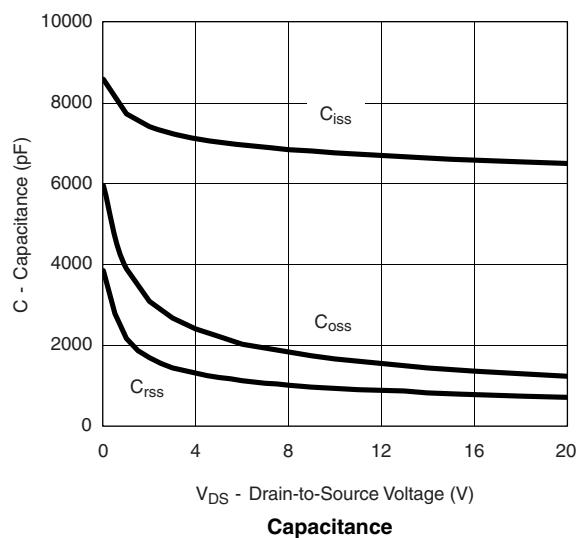
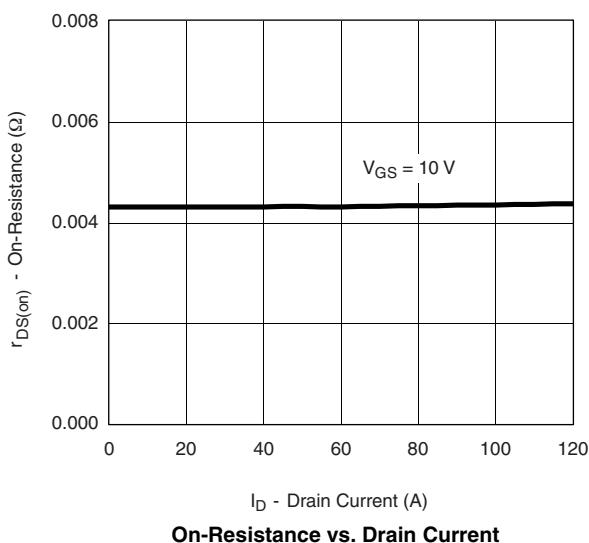
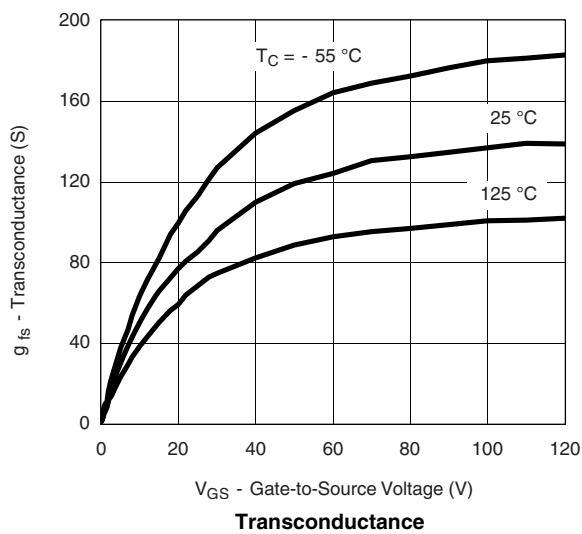
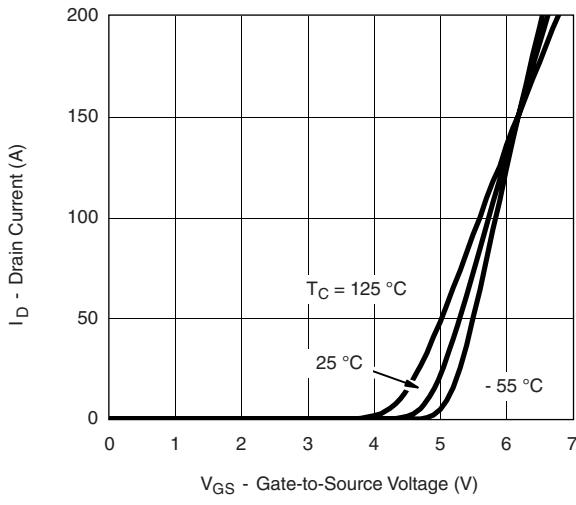
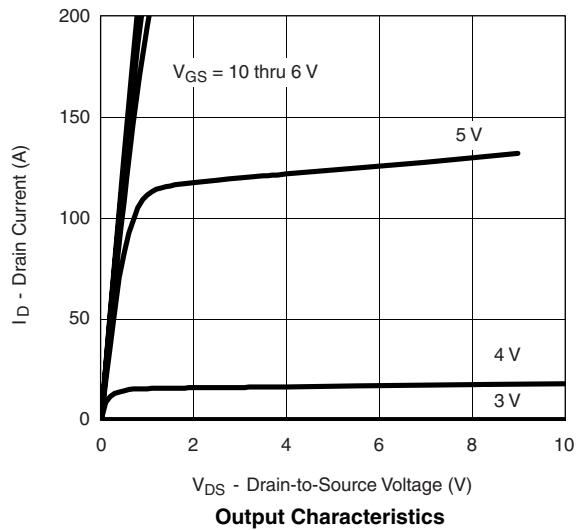
MOSFET SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	40			V
Gate-Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{DS}} = 250 \mu\text{A}$	2.5		4.5	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0 \text{ V}, V_{\text{GS}} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 40 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			1	μA
		$V_{\text{DS}} = 40 \text{ V}, V_{\text{GS}} = 0 \text{ V}, T_J = 125^\circ\text{C}$			50	
		$V_{\text{DS}} = 40 \text{ V}, V_{\text{GS}} = 0 \text{ V}, T_J = 175^\circ\text{C}$			500	
On-State Drain Current ^a	$I_{\text{D}(\text{on})}$	$V_{\text{DS}} = 5 \text{ V}, V_{\text{GS}} = 10 \text{ V}$	120			A
Drain-Source On-State Resistance ^a	$r_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10 \text{ V}, I_D = 25 \text{ A}$		0.0043	0.0054	Ω
		$V_{\text{GS}} = 10 \text{ V}, I_D = 25 \text{ A}, T_J = 125^\circ\text{C}$			0.0088	
		$V_{\text{GS}} = 10 \text{ V}, I_D = 25 \text{ A}, T_J = 175^\circ\text{C}$			0.011	
Sense Diode Forward Voltage	$V_{\text{FD}1}$	$I_F = 50 \mu\text{A}$	675		825	mV
	$V_{\text{FD}2}$	$I_F = 25 \mu\text{A}$	600		750	
Sense Diode Forward Voltage Increase	ΔV_F	From $I_F = 25 \mu\text{A}$ to $I_F = 50 \mu\text{A}$	50		100	
Forward Transconductance ^a	g_{fs}	$V_{\text{DS}} = 15 \text{ V}, I_D = 20 \text{ A}$		35		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 25 \text{ V}, f = 1 \text{ MHz}$		6400		pF
Output Capacitance	C_{oss}			1100		
Reverse Transfer Capacitance	C_{rss}			630		
Gate Resistance	R_g	$f = 1 \text{ MHz}$		2.2		Ω
Total Gate Charge ^c	Q_g	$V_{\text{DS}} = 20 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 25 \text{ A}$		115	150	nC
Gate-Source Charge ^c	Q_{gs}			35		
Gate-Drain Charge ^c	Q_{gd}			35		
Turn-On Delay Time ^c	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 20 \text{ V}, R_L = 0.8 \Omega$ $I_D \approx 25 \text{ A}, V_{\text{GEN}} = 10 \text{ V}, R_G = 2.5 \Omega$		15	20	ns
Rise Time ^c	t_r			150	210	
Turn-Off Delay Time ^c	$t_{\text{d}(\text{off})}$			60	85	
Fall Time ^c	t_f			80	110	
Source-Drain Diode Ratings and Characteristics $T_C = 25^\circ\text{C}$^b						
Continuous Current	I_S				60	A
Pulsed Current	I_{SM}				200	
Forward Voltage ^a	V_{SD}	$I_F = 60 \text{ A}, V_{\text{GS}} = 0 \text{ V}$		1.0	1.5	V
Reverse Recovery Time	t_{rr}	$I_F = 60 \text{ A}, \text{di/dt} = 100 \text{ A}/\mu\text{s}$		45	70	ns
Peak Reverse Recovery Current	$I_{\text{RM}(\text{REC})}$			2.5	5	A
Reverse Recovery Charge	Q_{rr}			0.06	0.18	μC

Notes:

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

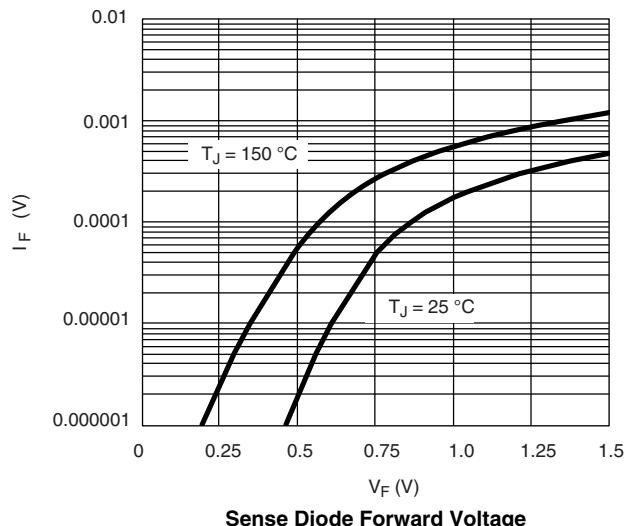
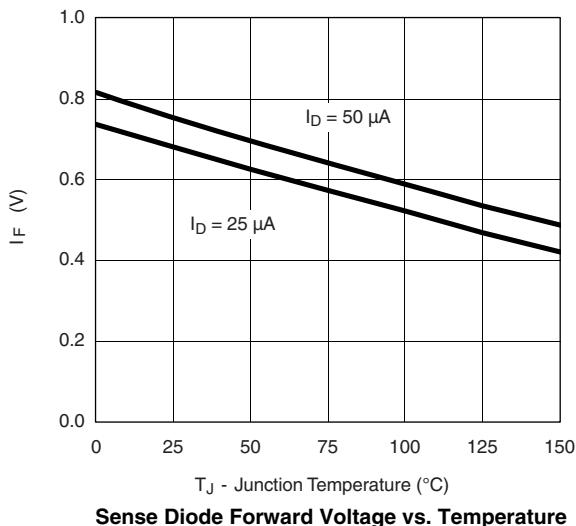
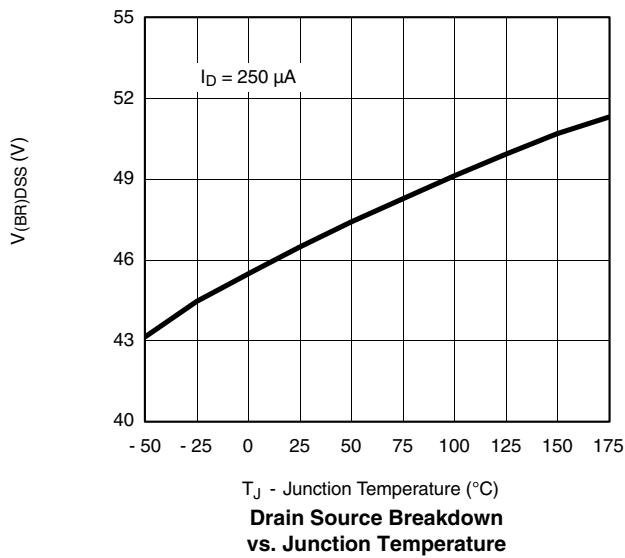
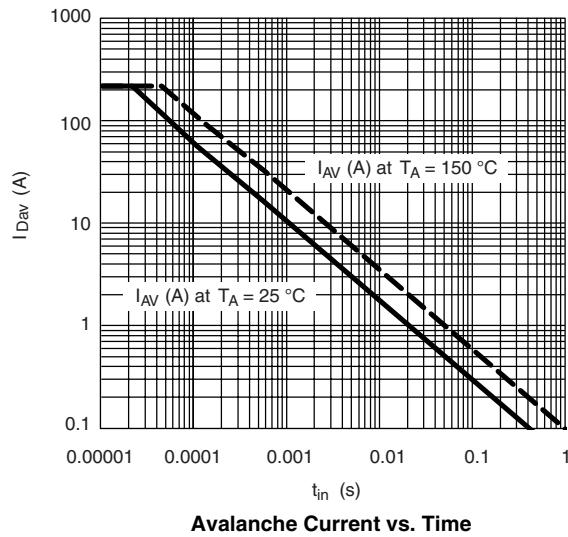
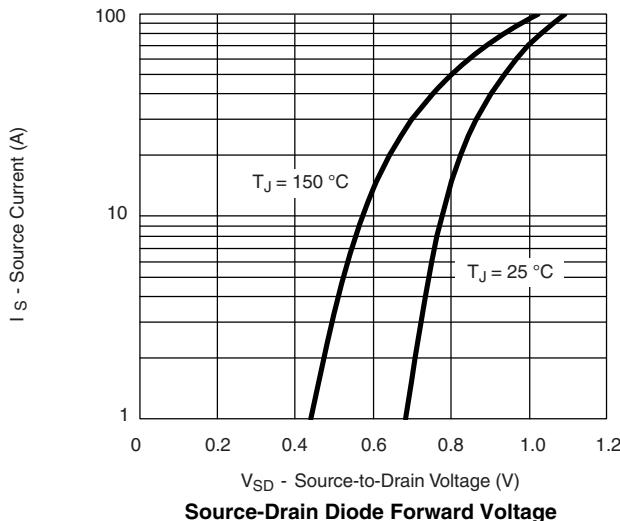
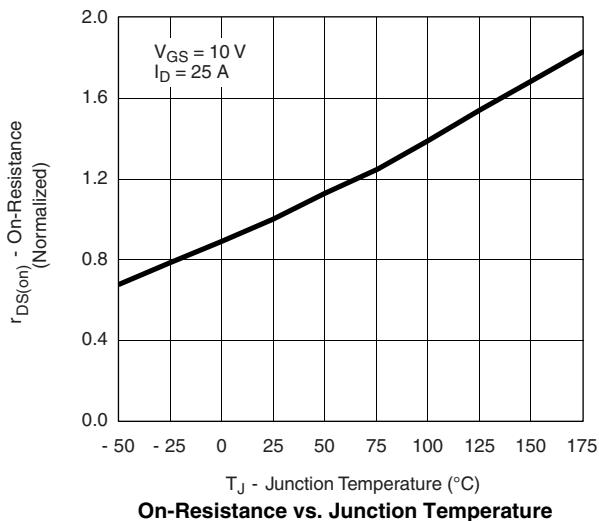
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


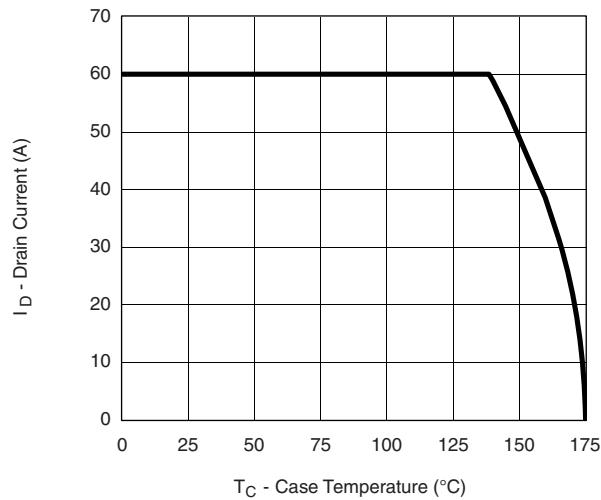
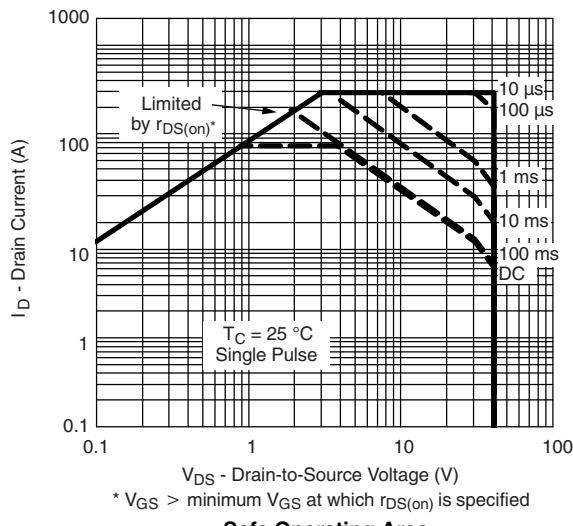
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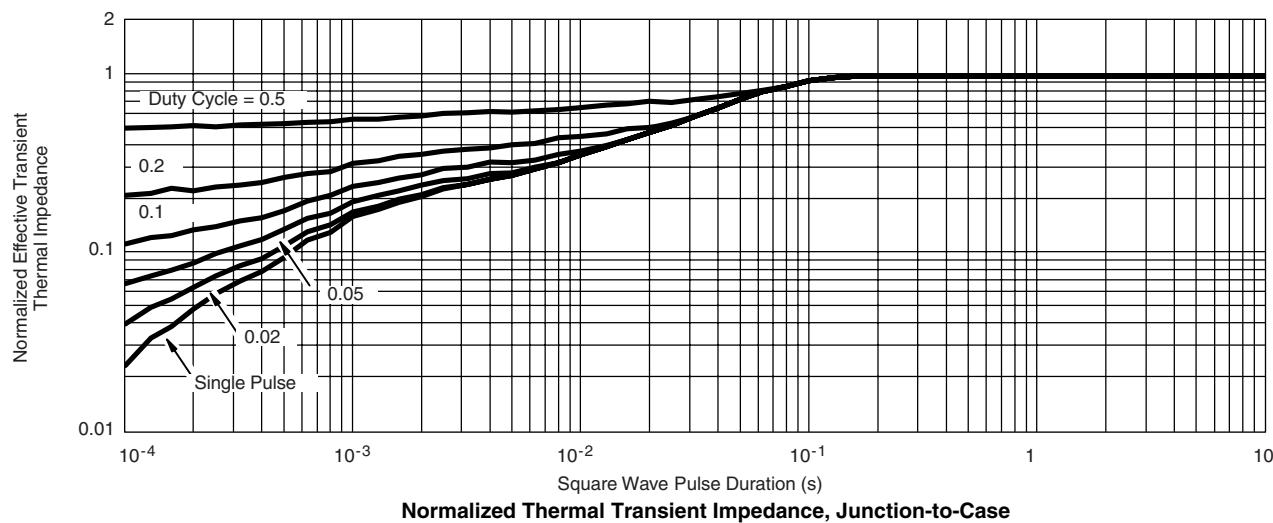
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



THERMAL RATINGS

Maximum Avalanche and Drain Current vs. Case Temperature

* $V_{GS} >$ minimum V_{GS} at which $r_{DS(on)}$ is specified

Safe Operating Area

Normalized Thermal Transient Impedance, Junction-to-Case

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